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Properties (9 of 9 total)

Patent	Publication	Application	PCT	International registration
1. NONVOLATILE MEMORY CONTROLLER WITH HOST CONTROLLER INTERFACE FOR RETRIEVING AND DISPATCHING NONVOLATILE MEMORY COMMANDS IN A DISTRIBUTED MANNER				
Inventors: Peter Z. Onufryk, Jayesh Patel, Ihab Jaser, Ganesh T. Seshan				

8588228
Nov 19, 2013

13052008
Mar 18, 2011

2. INTERRUPT TECHNIQUE FOR A NONVOLATILE MEMORY CONTROLLER
Inventors: Peter Z. Onufryk, Jayesh Patel, Ihab Jaser

8554968
Oct 08, 2013

13052388
Mar 21, 2011

3. SYSTEM AND METHOD FOR GENERATING PARITY DATA IN A NONVOLATILE MEMORY CONTROLLER BY USING A DISTRIBUTED PROCESSING TECHNIQUE
Inventors: Peter Z. Onufryk, Inna Levit

8601346
Dec 03, 2013

13052835
Mar 21, 2011

4. SYSTEM AND METHOD FOR ROUTING A DATA MESSAGE THROUGH A MESSAGE NETWORK
Inventors: Peter Z. Onufryk, Ganesh T. Seshan

8656071
Feb 18, 2014

13107265
May 13, 2011

5. SYSTEMS AND METHODS FOR ADAPTIVELY SELECTING FROM AMONG A PLURALITY OF ERROR CORRECTION CODING SCHEMES IN A FLASH DRIVE FOR ROBUSTNESS AND LOW LATENCY
Inventors: Philip L. Northcott

9183085
Nov 10, 2015

13477600
May 22, 2012

6. SYSTEMS AND METHODS FOR ADAPTIVELY SELECTING AMONG DIFFERENT ERROR CORRECTION CODING SCHEMES IN A FLASH DRIVE
Inventors: Philip L. Northcott

9021337
Apr 28, 2015

13477629
May 22, 2012

7. SYSTEM AND METHOD FOR MEMORY BLOCK POOL WEAR LEVELING

Inventors: Rino Micheloni, Alessia Marelli, Luca Crippa

9417804	20160004458	14325212
Aug 16, 2016	Jan 07, 2016	Jul 07, 2014

8. NONVOLATILE MEMORY SYSTEM THAT USES PROGRAMMING TIME TO REDUCE BIT ERRORS

Inventors: Luca Crippa, Rino Micheloni

9305661	20160064096	14475757
Apr 05, 2016	Mar 03, 2016	Sep 03, 2014

9. NONVOLATILE MEMORY SYSTEM WITH READ CIRCUIT FOR PERFORMING READS USING THRESHOLD VOLTAGE SHIFT READ INSTRUCTION

Inventors: Rino Micheloni, Alessia Marelli, Stephen Bates

9799405	14812891
Oct 24, 2017	Jul 29, 2015