Credo Introduces 800G HiWire ZeroFlap AECs to Support AI Backend Networks

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SAN JOSE, Calif.--(BUSINESS WIRE)--Oct. 10, 2024-- Credo Technology Group Holding Ltd (Credo) (Nasdaq: CRDO), an innovator in providing secure, high-speed connectivity solutions that deliver improved energy efficiency as data rates and corresponding bandwidth requirements increase throughout the data infrastructure market, is excited to announce the new 800G ZeroFlap (ZF) family of HiWire Active Electrical Cables (AECs) reaching lengths of 7 m. These high-performance HiWire ZF AECs are designed to deliver highly reliable interconnect for artificial intelligence (AI) backend networks.

This press release features multimedia. View the full release here: https://www.businesswire.com/news/home/20241010046982/en/

Credo Introduces 800G HiWire ZeroFlap AECs For AI Backend Networks. With improved reach and signal integrity, the HiWire ZF AECs deliver zero soft link flaps to support the lossless backend RDMA network that AI clusters are built on. These 800G AECs benefit from the newest advances in liquid cooling, allowing the 7 m length to enable full host-to-switch connectivity in leading GPU clusters. "Cluster reliability is of paramount importance when building the biggest supercomputers in the world with 100,000+ GPUs," said Massine Merzouk, Network Engineer at X, assisting xAI. "Credo's HiWire AECs offer the stable transport platform we need to build such massive systems." (Graphic: Business Wire)

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Credo's 800G HiWire ZF AECs build on Credo's already successful HiWire AEC product family, with millions of units of HiWire AECs already deployed at tier one hyperscalers. With improved reach and signal integrity, the HiWire ZF AECs deliver zero soft link flaps to support the lossless backend RDMA network that AI clusters are built on. These 800G AECs benefit from the newest advances in liquid cooling, allowing the 7 m length to enable full host-to-switch connectivity in leading GPU clusters.

The HiWire ZF family includes four new product offerings:

- 800G OSFP to OSFP
- 800G OSFP to OSFP-RHS
- 800G OSFP to 2*OSFP-RHS
- 800G OSFP to 2*Q112

"Credo's new HiWire ZeroFlap AECs enable a step function improvement in GPU cluster reliability by eliminating the soft link flaps frequently seen with legacy optics," said Ameet Suri, Head of AEC Product at Credo. "Further, when compared to legacy optics, Credo's HiWire AECs offer power savings of up to 14W per link and cost savings of up to $1,000 per GPU."

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"Optics link flap is a developing issue in large GPU clusters – with each link flap event resulting in up to 30 minutes of lost training time and costing up to $200,000," said Alan Weckel, Founding Analyst at the 650 Group. "Credo HiWire AECs have much higher hardware reliability, but more importantly, have been shown to operate without link flaps for billions of field operating hours."

Credo will demonstrate the new HiWire ZF AEC cables at the upcoming Open Compute Project 2024 Summit (OCP) in San Jose, USA from Oct 15 – 17, 2024. Conference attendees are encouraged to visit Credo in booth #B31 to learn more about these new HiWire ZF AEC devices.

To learn more about the Credo products in this release go to the product pages linked here.

About Credo

Our mission is to deliver high-speed solutions to break bandwidth barriers on every wired connection in the data infrastructure market. Credo is an innovator in providing secure, high-speed connectivity solutions that deliver improved power efficiency as data rates and corresponding bandwidth requirements increase exponentially throughout the data infrastructure market. Our innovations ease system bandwidth bottlenecks while simultaneously improving on power, security, and reliability. Our connectivity solutions are optimized for optical and electrical Ethernet applications, including the emerging 100G (or Gigabits per second), 200G, 400G, 800G and the emerging 1.6T (or Terabits per second) port markets. Credo products are based on our proprietary Serializer/Deserializer (SerDes) and Digital Signal Processor (DSP) technologies. Our product families include

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Integrated Circuits (ICs) for the optical and line card markets, Active Electrical Cables (AECs) and SerDes Chiplets. Our intellectual property (IP) solutions consist primarily of SerDes IP licensing.

For more information, please visit https://www.credosemi.com. Follow Credo on LinkedIn.

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