

**UNITED STATES DISTRICT COURT
EASTERN DISTRICT OF TEXAS
MARSHALL DIVISION**

ICASHE, INC., Plaintiff, v. SAMSUNG ELECTRONICS CO., LTD., <i>et al.</i> Defendants.	Civil Action No. 2:24-cv-00429-JRG JURY TRIAL DEMANDED
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**PLAINTIFF ICASHE, INC.’S DISCLOSURE OF ASSERTED CLAIMS
AND INFRINGEMENT CONTENTIONS**

Pursuant to Patent Rules 3-1 and 3-2 and the Court’s Order dated August 8, 2024 (Dkt. 14), Plaintiff iCashe, Inc. (“iCashe”) hereby provides its Disclosure of Asserted Claims and Infringement Contentions (“Infringement Contentions”) against Defendants Samsung Electronics Co., Ltd. and Samsung Electronics America, Inc. (collectively, “Samsung” or “Defendants”) and identifies the documents included in its document production made herewith.

iCashe reserves the right to amend or supplement these Infringement Contentions pursuant to Patent Rule 3-6(a) in view of any claim construction ruling(s) issued by the Court. iCashe further reserves the right to seek leave pursuant to Patent Rule 3-6(b) to supplement and/or amend these Infringement Contentions, including identification of asserted claims, accused instrumentalities, and additional infringing structures within

already accused instrumentalities, based upon information obtained through discovery and/or other means.

iCashe is providing these Infringement Contentions before it has had any discovery from Samsung. Detailed information regarding Samsung’s smartphones, wearable devices, and tablet computers and aspects of their NFC and/or MST functionality is not publicly available. iCashe expects to obtain more detailed information through discovery.

iCashe’s Infringement Contentions do not constitute a disclosure of any information that is subject to attorney-client privilege or work-product protection or that is otherwise immune from discovery.

I. Identification of Infringed Claims (P.R. 3-1(a))

iCashe asserts seven patents: U.S. Patent Nos. 9,122,965 (“’965 patent”); 9,483,722 (“’722 patent”); 11,694,053 (“’053 patent”); 8,403,219 (“’219 patent”); 9,202,156 (“’156 patent”); 9,208,423 (“’423 patent”); and 11,270,174 (“’174 patent”) (collectively, the “Asserted Patents”). Based on information presently available, iCashe identifies in the table below each claim of each patent-in-suit that Samsung has infringed and/or continues to infringe.

Patent	Infringed Claims
’965 patent	1-20
’722 patent	1-14
’053 patent	1-8, 17-20
’219 patent	1-6
’156 patent	1-5, 8-12
’423 patent	1-5, 8-18
’174 patent	1-9

II. Identification of Accused Instrumentalities (P.R. 3-1(b))

iCashe contends that Samsung has infringed, and/or continues to infringe, the Asserted Patents by making, using, offering for sale, selling, and/or importing into the United States, Samsung smartphones, wearable devices, and tablet computers that provide for contactless payment and/or other contactless “tap to” transactions using near field communication (NFC) and/or magnetic secure transmission (MST) functionality. Based on information presently available, iCashe identifies in the table below, the Accused Instrumentalities.

Patent	Accused Instrumentalities	Infringed Claims
'965 patent	Samsung smartphones that include an NFC controller, such as the NXP SN110U, another NXP NFC controller, a Samsung NFC controller, a Broadcom NFC controller, an STMicroelectronics NFC controller, or any other NFC controller capable of supporting “tap to” transactions, as applicable with respect to the charted claims.	1-20
	Samsung wearable devices and tablet computers that include an NFC controller, such as the NXP SN110U, another NXP NFC controller, a Samsung NFC controller, a Broadcom NFC controller, an STMicroelectronics NFC controller, or any other NFC controller capable of supporting “tap to” transactions, as applicable with respect to the charted claims.	1-5, 7-11, 13-19
'722 patent	Samsung smartphones that include an NFC controller, such as the NXP SN110U, another NXP NFC controller, a Samsung NFC controller, a Broadcom NFC controller, an STMicroelectronics NFC controller, or any other NFC controller capable of supporting “tap to” transactions, as applicable with respect to the charted claims.	1-14
	Samsung wearable devices and tablet computers that include an NFC controller, such as the NXP SN110U, another NXP NFC controller, a Samsung NFC controller, a Broadcom NFC controller, an	1-3, 5-9, 11-13

Patent	Accused Instrumentalities	Infringed Claims
	STMicroelectronics NFC controller, or any other NFC controller capable of supporting “tap to” transactions, as applicable with respect to the charted claims.	
’053 patent	Samsung smartphones, wearable devices, and tablet computers that include an NFC controller, such as the NXP SN110U, another NXP NFC controller, a Samsung NFC controller, a Broadcom NFC controller, an STMicroelectronics NFC controller, or any other NFC controller capable of supporting “tap to” transactions, as applicable with respect to the charted claims, and the methods used to manufacture and/or test those products.	1-8, 17-20
’219 patent	Samsung smartphones that include an NFC controller, such as the NXP SN110U, another NXP NFC controller, a Samsung NFC controller, a Broadcom NFC controller, an STMicroelectronics NFC controller, or any other NFC controller capable of supporting “tap to” transactions, as applicable with respect to the charted claims.	1-6
’156 patent	Samsung smartphones that include an MST controller, such as the Samsung S2MIS01A MST Driver chip, another Samsung MST controller, a Zinitix MST controller, an Exar MST controller, an STMicroelectronics MST controller, or any other MST controller capable of supporting “tap to” transactions, as applicable with respect to the charted claims.	1-5, 8-12
’423 patent	Samsung smartphones that include an MST controller, such as the Samsung S2MIS01A MST Driver chip, another Samsung MST controller, a Zinitix MST controller, an Exar MST controller, an STMicroelectronics MST controller, or any other MST controller capable of supporting “tap to” transactions, as applicable with respect to the charted claims.	1-5, 8-18
’174 patent	Samsung smartphones that include an NFC controller, such as the NXP SN110U, another NXP NFC controller, a Samsung NFC controller, a Broadcom NFC controller, an STMicroelectronics NFC controller, or any other NFC controller capable	1-4, 6-9

Patent	Accused Instrumentalities	Infringed Claims
	of supporting “tap to” transactions, as applicable with respect to the charted claims.	
	Samsung smartphones that include an MST controller, such as the Samsung S2MIS01A MST Driver chip, another Samsung MST controller, a Zinitix MST controller, an Exar MST controller, an STMicroelectronics MST controller, or any other MST controller capable of supporting “tap to” transactions, as applicable with respect to the charted claims.	1-2, 4-5, 7-9

Attached hereto as Exhibit A is a list of accused products, including model numbers, that iCashe has been able to identify based on presently available information. iCashe may identify additional devices, and may accuse additional model numbers, as it obtains discovery. Additionally, Exhibit A includes, for each device identified, an indication of whether the device provides contactless payment and/or other contactless “tap to” transactions using NFC and/or MST functionality, to the extent such information could be determined from publicly available sources.¹ iCashe is without sufficient information at this time to identify the specific NFC and/or MST chip manufacturer or model number contained within each device.

The following Samsung website page was used to identify the accused products in Exhibit A that include MST functionality: <https://www.samsung.com/my/samsung-pay/supported-devices/?srsltid=AfmBOor09aM0X0486788EnkJAA8iWp31nTqw>

¹ In the NFC and MST columns of Exhibit A, “Y” indicates that, on information and belief, the indicated functionality is present in the accused product, and “-” indicates that, on information and belief, the indicated functionality is not present in the accused product or that iCashe is without sufficient information at this time to identify whether the indicated functionality is present in the accused product.

V2I2Kc30O-0IIGgOd9iM. The page appears to be affiliated with a Samsung Malaysia-based website and thus may not accurately reflect accused products with MST functionality that were made, used, sold, offered for sale, or imported into the United States. However, public sources identifying MST functionality of the accused products is limited. iCashe reserves the right to amend its identification of NFC and/or MST functionality of the accused products identified in Exhibit A, and any additionally identified accused products, based on information obtained in discovery.

iCashe's infringement charts, attached hereto, correlate the asserted claims to the Accused Instrumentalities. iCashe expects to identify during discovery additional infringing devices, including devices for which limited information is available, newly released devices, and devices Samsung no longer sells (for which limited information is available), that fall within the scope of iCashe's infringement contentions.

III. Charts Identifying Each Element of Each Asserted Claim within Accused Instrumentalities (P.R. 3-1(c))

iCashe contends that Samsung has infringed and continues to infringe the Asserted Patents, directly and indirectly, by: (1) making, using, offering to sell, selling, and/or importing into the United States display panels and/or modules that practice the claims of the Asserted Patents (35 U.S.C. § 271(a)); and (2) inducing third parties (*e.g.*, Samsung customers and end users) to directly infringe by making, using, offering to sell, selling, and/or importing into the United States Samsung smartphones, wearable devices, and/or tablet computers that practice the claims of the Asserted Patents, with knowledge of the

Asserted Patents and of the third parties' direct infringement and specific intent to cause that direct infringement (35 U.S.C. § 271(b)). (See Dkt. 1 at ¶¶ 3, 31-44.)

Attached as Exhibits B through H-2, and as set forth below, iCashe provides charts identifying specifically where each element of each asserted claim is found within the Accused Instrumentalities.

Patent	Claim Chart(s)
'965 patent	Exhibit B
'722 patent	Exhibit C
'053 patent	Exhibit D
'219 patent	Exhibit E
'156 patent	Exhibit F
'423 patent	Exhibit G
'174 patent	Exhibit H-1
	Exhibit H-2

iCashe contends that the charted products provide representative infringement reads applicable to the broader set of Accused Instrumentalities, including substantially similar products that iCashe may identify during discovery. iCashe reserves the right to provide additional support, based on information obtained during discovery, for its contention that the charted products are representative, or to chart additional products as additional information becomes available.

iCashe does not contend that any element of any asserted claim is governed by 35 U.S.C. § 112(6).

IV. Literal Infringement and Infringement under the Doctrine of Equivalents (P.R. 3-1(d))

iCashe contends that each element of each asserted claim is literally present in each Accused Instrumentality. iCashe reserves the right to assert, in the alternative, that

certain elements of certain asserted claims are present in the Accused Instrumentalities under the doctrine of equivalents in light of information obtained during discovery and/or in view of any claim construction ruling(s) issued by the Court.

V. Priority Dates of the Asserted Patents (P.R. 3-1(e))

iCashe asserts that all asserted claims of U.S. Patent Nos. 9,122,965, 9,483,722, and 11,694,053 claim priority to U.S. Application 12/188,346 and are thus entitled to a priority date of August 8, 2008, the filing date of that application.

iCashe asserts that all asserted claims of U.S. Patent Nos. 8,403,219, 9,202,156, 9,208,423, and 11,270,174 claim priority to U.S. Application 11/063,291 and are thus entitled to a priority date of February 22, 2005, the filing date of that application.

VI. Disclosure Regarding Practice of Claimed Inventions (P.R. 3-1(f))

iCashe does not contend that any iCashe apparatus, device, process, method, act, or other instrumentality practices any of the asserted claims. Some apparatuses, devices, processes, methods, acts, or other instrumentalities provided by third party Tyfone, Inc., including SideTap and/or iCashe Mobile Wallet, may have practiced some of the asserted claims.

VII. Document Production Accompanying Disclosure (P.R. 3-2)

In accordance with Patent Rule 3-2, iCashe produces the following categories of documents in the following production ranges.

Patent Rule 3-2(a): iCashe is not presently aware of any documents evidencing a discussion, disclosure to, or other manner of providing to a third party, or sale of or offer to sell, the claimed invention(s) prior to the dates of application for the patents in suit.

Patent Rule 3-2(b): ICASHE_00010725 – ICASHE_00010784

Patent Rule 3-2(c):

Patent	File History Production Range
'219 patent	ICASHE_00000247 – ICASHE_00000372
'965 patent	ICASHE_00000373 – ICASHE_00002163
'156 patent	ICASHE_00002164 – ICASHE_00003833
'423 patent	ICASHE_00003834 – ICASHE_00005504
'722 patent	ICASHE_00005505 – ICASHE_00007498
'174 patent	ICASHE_00007499 – ICASHE_00010225
'053 patent	ICASHE_00010226 – ICASHE_00010724

Dated: September 4, 2024

Respectfully submitted,

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CERTIFICATE OF SERVICE

I hereby certify that a copy of the foregoing document was served on September 4, 2024 on all counsel of record for Defendants via electronic mail using the following contact information:

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EXHIBIT C

Exhibit C
iCashe's Infringement Contentions for U.S. Patent No. 9,483,722
Samsung Galaxy S21 5G Smartphone

The chart below identifies where each element of the asserted claims of U.S. Patent No. 9,483,722 ("722 patent") is found in the exemplary Samsung Galaxy S21 5G smartphone. iCashe contends that all Samsung smartphones made, used, tested, offered for sale, sold, or imported into the United States by Samsung, that share the features detailed below with reference to the Samsung Galaxy S21 5G smartphone, infringe claims 1-14 of the '722 patent in the manner charted herein. iCashe further contends that all Samsung wearable devices and tablet computers made, used, tested, offered for sale, sold or imported into the United States by Samsung, that share the features detailed below with reference to the Samsung Galaxy S21 5G smartphone, infringe at least claims 1-3, 5-9, and 11-13 of the '722 patent in the manner charted herein. Specifically, all Samsung smartphones, wearable devices, and tablet computers that include an NFC controller, such as the NXP SN110U, another NXP NFC controller, a Samsung NFC controller, a Broadcom NFC controller, an STMicroelectronics NFC controller, or any other NFC controller capable of supporting "tap to" transactions, as applicable with respect to the charted claims, infringe as set forth below, including but not limited to the smartphones, wearable devices, and tablet computers listed in Exhibit A.

The Accused Products iCashe has been able to identify to date are listed in Exhibit A to iCashe's Disclosure of Asserted Claims and Infringement Contentions ("Infringement Contentions"). The Accused Products are not limited to the listed devices, and iCashe reserves the right to identify additional devices based on information obtained in discovery, including but not limited to devices released after service of iCashe's Infringement Contentions.

Exhibit C
iCashe's Infringement Contentions for U.S. Patent No. 9,483,722
Samsung Galaxy S21 5G Smartphone

Claim Language	Infringement Evidence
1. A mobile device comprising:	The Samsung Galaxy S21 5G smartphone is a mobile device.  https://www.samsung.com/us/smartphones/galaxy-s-series/certified-re-newed-store/buy/?modelCode=SM5G991UZAAXAA.

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iCashe's Infringement Contentions for U.S. Patent No. 9,483,722
Samsung Galaxy S21 5G Smartphone

Claim Language	Infringement Evidence																		
about a carrier frequency of an interrogating radio frequency (RF) field;	The Galaxy S21 includes an NXP SN110U V3 NFC and Secure Element chip (“SN110U”), which contains a smartcard controller.  Date: November 4, 2020 Harry Lee SAMSUNG ELECTRONICS Co., Ltd 129 Samsung-ro Yeongtong-gu Gyeonggi-do Suwon-si 16677 KOREA, REPUBLIC OF Re: <i>EMVCo Letter of Approval – EMV Contactless Level 1 Mobile Product</i> <table style="width: 100%;"> <tr> <td style="width: 40%;">Approval Number:</td> <td>MTA_LOA_SAEL_02859</td> </tr> <tr> <td>Approval Date:</td> <td>November 4, 2020</td> </tr> <tr> <td>EMVCo Website Publication Date:</td> <td>January 15, 2021</td> </tr> <tr> <td>Approval Expiration Date:</td> <td>November 2, 2023</td> </tr> </table> <table style="width: 100%;"> <tr> <td style="width: 40%;">Product Provider Name:</td> <td>SAMSUNG ELECTRONICS Co., Ltd</td> </tr> <tr> <td>Registration Number:</td> <td>SAEL.V 14 0068</td> </tr> <tr> <td>Commercial Name:</td> <td style="border: 2px solid red;">Galaxy S21 5G</td> </tr> <tr> <td>Product Name:</td> <td>SM-G991B/DS</td> </tr> <tr> <td>Product Version:</td> <td>V1</td> </tr> </table>	Approval Number:	MTA_LOA_SAEL_02859	Approval Date:	November 4, 2020	EMVCo Website Publication Date:	January 15, 2021	Approval Expiration Date:	November 2, 2023	Product Provider Name:	SAMSUNG ELECTRONICS Co., Ltd	Registration Number:	SAEL.V 14 0068	Commercial Name:	Galaxy S21 5G	Product Name:	SM-G991B/DS	Product Version:	V1
Approval Number:	MTA_LOA_SAEL_02859																		
Approval Date:	November 4, 2020																		
EMVCo Website Publication Date:	January 15, 2021																		
Approval Expiration Date:	November 2, 2023																		
Product Provider Name:	SAMSUNG ELECTRONICS Co., Ltd																		
Registration Number:	SAEL.V 14 0068																		
Commercial Name:	Galaxy S21 5G																		
Product Name:	SM-G991B/DS																		
Product Version:	V1																		

Exhibit C
iCashe's Infringement Contentions for U.S. Patent No. 9,483,722
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Claim Language	Infringement Evidence
	Contactless Communication Protocol: ☒ Type A ☒ Type B Execution Environment: ☒ UICC² slot 1 ☐ UICC² slot 2 ☐ eSE ☒ HCE ☐ eUICC Mobile Product Operating System Name and Version: Android Version 11.0 NFC Controller Name and Version: SN110U V3 Version N/A NFC Controller Firmware Name and Version: N/A Version FW01.10.5A https://www.emvco.com/wp-content/uploads/loa/MTA_LOA_SAEL_02859_24Nov20_SHORT.pdf (emphasis added).

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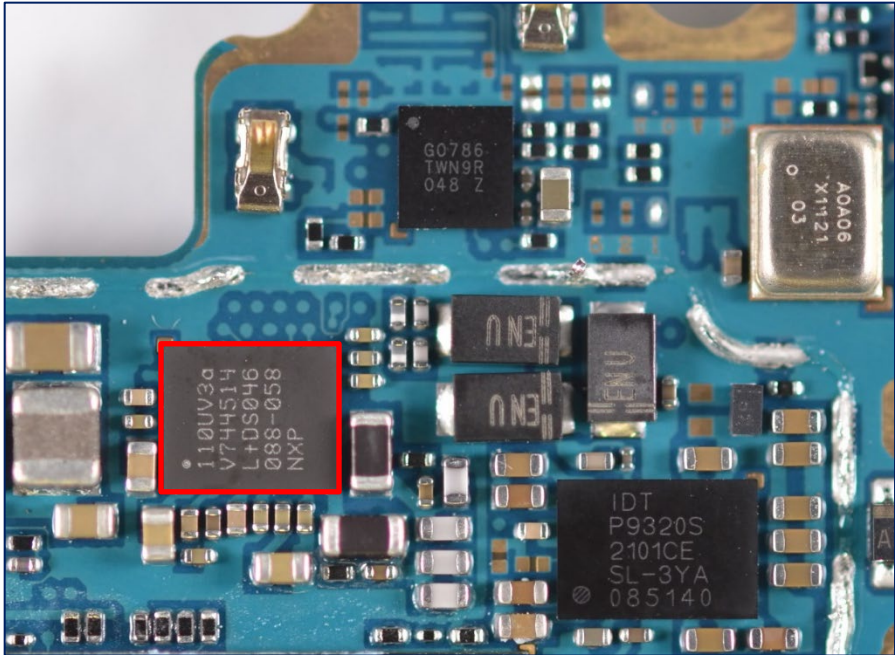
Claim Language	Infringement Evidence
	 Teardown image from Samsung Galaxy S21 5G smartphone (emphasis added). This SN110U chip provides near-field communications functionality, enabling NFC payment transactions, as discussed in further detail below.

Exhibit C
iCash's Infringement Contentions for U.S. Patent No. 9,483,722
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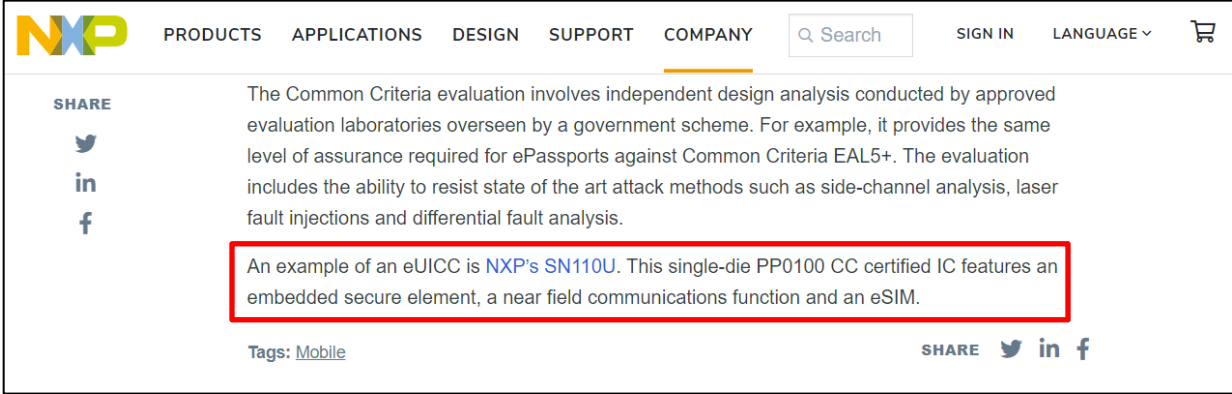
Claim Language	Infringement Evidence
	A screenshot of an NXP blog post. The header includes the NXP logo, navigation links (PRODUCTS, APPLICATIONS, DESIGN, SUPPORT, COMPANY), a search bar, and links for SIGN IN and LANGUAGE. The main content area has a 'SHARE' section with social media icons (Twitter, LinkedIn, Facebook) on the left. The text of the blog post discusses Common Criteria evaluation and mentions 'NXP's SN110U'. A red rectangular box highlights the sentence: 'An example of an eUICC is NXP's SN110U. This single-die PP0100 CC certified IC features an embedded secure element, a near field communications function and an eSIM.' Below the text are tags ('Mobile') and another 'SHARE' section with social media icons. https://www.nxp.com/company/blog/imagine-never-having-to-change-a-sim-again:BL-NEVER-CHANGE-SIM-AGAIN (emphasis added).

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iCashe's Infringement Contentions for U.S. Patent No. 9,483,722
Samsung Galaxy S21 5G Smartphone

Claim Language	Infringement Evidence
	The image is a certification document from the NFC Forum. It features a blue and white logo with a stylized 'N' and the text 'Near Field Communication CERTIFIED'. Below the logo, it says 'SN110x Android Reference Platform'. To the right, it states 'This is to certify that NXP Semiconductors has successfully achieved certification in the NFC Forum Certification Program indicating that their product has demonstrated its conformance to the Applicable Certification Requirements listed below.' It lists 'Certification Requirements: Certification Release 11, Test Release 11.1'. Below that, it specifies 'Device Class: NFC Universal Device', 'Card emulation: T3T, T4AT, T4BT', 'Date certified: 2019-10-23', and 'Certification ID: 58603'. At the bottom, there is a small disclaimer about the NFC Forum Certification Mark and a link to the certification register. NFC Forum Certification NXP SN110x. The NFC functionality of the SN110U chip in the Galaxy S21 can be used for, e.g., completing Samsung Pay transactions.

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iCashe's Infringement Contentions for U.S. Patent No. 9,483,722
Samsung Galaxy S21 5G Smartphone

Claim Language	Infringement Evidence
	Use Samsung Pay Use Samsung Pay by opening the app and holding your device over the store's card reader. 1. From  Samsung Pay, select a card to pay with and authorize payments by scanning your finger or by entering your Samsung Pay PIN. 2. Hold your phone over the store's card reader. • When your payment is complete, a receipt is sent to your registered email.  NOTE Make sure the NFC feature is enabled on your device. For more information, see NFC and payment. <i>Samsung Galaxy S21 5G, Galaxy S21+ 5G, Galaxy S21 Ultra 5G User Manual (2021) ("Galaxy S21 User Manual") at 106-07.</i>

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iCashe's Infringement Contentions for U.S. Patent No. 9,483,722
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Claim Language	Infringement Evidence
	NFC and payment Near Field Communication (NFC) allows you to communicate with another device without connecting to a network. This technology is used by Android Beam and certain payment apps. The device that you are transferring to needs to support NFC, and it needs to be within four centimeters of your device. - From Settings, tap  Connections > NFC and contactless payments, and then tap  to turn on this feature. Tap and pay Use an NFC payment app to make payments by touching your device to a compatible credit card reader. - From Settings, tap  Connections > NFC and contactless payments, and then tap  to turn on NFC. - Tap Contactless payments to see the default payment app. - To use another payment app, tap an available app to choose it. - To use a payment app that is open, tap Pay with currently open app. - To set another payment service as the default, tap Others, and then tap the service you prefer.  TIP NFC technology is used with  Samsung Pay. Turn on this feature to see how easy and secure it is to use your device to make payments. <i>Galaxy S21 User Manual at 118.</i>

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iCashe's Infringement Contentions for U.S. Patent No. 9,483,722
Samsung Galaxy S21 5G Smartphone

Claim Language	Infringement Evidence
	Fast, secure payments with Samsung Pay Samsung Pay allows you to tap to pay with your device anywhere NFC is available, making in-store payments quick and secure¹. You can also use Samsung Pay for in-app and online purchases at participating merchants, including Samsung.com. https://www.samsung.com/us/apps/samsung-wallet/. What differentiates Samsung Pay from Apple Pay or Android Pay? Both Apple Pay and Google Pay rely on Near Field Communication (NFC) technology to make contactless payment. These services are limited to merchants with NFC-enabled payment terminals. Merchants are required to upgrade to a new payment terminal with NFC capabilities in order to accept Apple Pay or Android Pay. Samsung Pay, on the other hand, uses both NFC and Magnetic Secure Transmission (MST) technology on supported devices, making it work with nearly all current payment terminals. Some payment terminals may require software updates. https://www.samsung.com/us/support/answer/ANS00080583/. Although NXP has not published certain information regarding the functionality or functional blocks of the SN110U chip, it has published block diagrams showing the components of SN1xx

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Samsung Galaxy S21 5G Smartphone

Claim Language	Infringement Evidence
	family devices, including the embedded NFC controller, the embedded secure element, and the system mailbox, as shown below. The diagram, titled 'Fig. 1.2: SN100x Product Configurations', shows the internal architecture of two device variants: SN100U and SN100T. Both variants share a common 'Embedded NFC Controller (SN100_NFC)' and an 'SN100 System MailBox'. The SN100U variant includes an external antenna. The SN100T variant includes an 'eSE - Software Domain' (Embedded Secure Element) which contains an 'ISD' (ISD-R) component. The 'eUICC - software Domain' is also present, containing 'ISD-P' and 'App' components, along with an 'eUICC Plugin' and 'ECASD'. Communication between the NFC controller and the secure element is shown via 'UART / I2C' and 'SPI' protocols. A 'Virtual ISO' connection is shown between the SN100T and the eUICC domain. An 'ISO 7816' connection is shown between the SN100U and the eUICC domain. Fig. 1.2: SN100x Product Configurations <i>NXP SN100T – JCOP5.1 on SN100.C48 Secure Element Security Target Lite, Rev. 1.0 (Apr. 18, 2019) (“NXP SN100T Secure Element Specification”) at 5.</i>

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Samsung Galaxy S21 5G Smartphone

Claim Language	Infringement Evidence
	Portions of the embedded secure element of the SN110U form all or part of the smartcard controller within the Galaxy S21. Additionally, portions of the embedded NFC controller of the SN110U may form part of the smartcard controller within the Galaxy S21. Although certain information regarding the functionality or functional blocks of the SN1xx family devices is not publicly available, NXP has published detailed information about the functionality and functional blocks of other NXP NFC chips, including the PN7150. The functionality of the PN7150 is described in the publicly available <i>PN7150 High Performance NFC Controller with Integrated Firmware, Supporting All NFC Forum Modes, Product Data Sheet, Rev. 4.1</i> (Jan. 27, 2023) (“<i>PN7150 Data Sheet</i>”). Additionally, a publicly available Antenna Design and Matching Guide published in 2019 describes functionality of the PN7150 in connection with Active Load Modulation (ALM) communication. <i>Application Note AN11755, PN7150 Antenna Design and Matching Guide, Rev. 1.7</i> (July 10, 2019) (“<i>PN7150 Antenna Design and Matching Guide</i>”). On information and belief and as detailed below, the NXP PN7150 chip—as described in the publications cited herein—contains certain functional blocks and interfaces that are similar to some of the relevant functional blocks and interfaces of the NXP SN110U chip and surrounding circuitry in the Galaxy S21. As discussed below, each of the SN110U and the PN7150 NFC controllers operates at 13.56 MHz and supports Type A and Type B communications under the ISO/IEC 14443 standard. The SN110U supports RF communication at 13.56 MHz, as shown below, and the two standard sideband frequencies of $f_c/16$ based on the occupied bandwidth of 1.69 MHz ($2 \times 13.56/16$).

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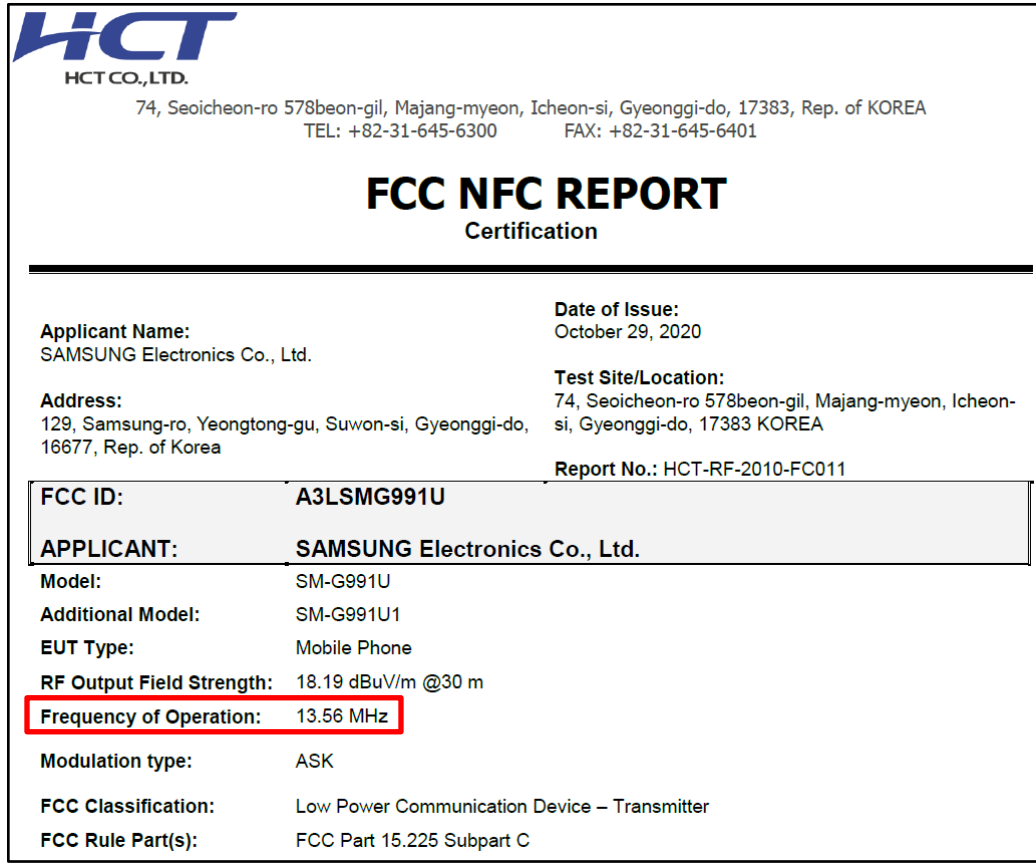
Claim Language	Infringement Evidence				
	 HCT HCT CO., LTD. 74, Seoicheon-ro 578beon-gil, Majang-myeon, Icheon-si, Gyeonggi-do, 17383, Rep. of KOREA TEL: +82-31-645-6300 FAX: +82-31-645-6401 FCC NFC REPORT Certification Applicant Name: SAMSUNG Electronics Co., Ltd. Date of Issue: October 29, 2020 Test Site/Location: 74, Seoicheon-ro 578beon-gil, Majang-myeon, Icheon-si, Gyeonggi-do, 17383 KOREA Address: 129, Samsung-ro, Yeongtong-gu, Suwon-si, Gyeonggi-do, 16677, Rep. of Korea Report No.: HCT-RF-2010-FC011 <table border="1"> <tr> <td>FCC ID:</td> <td>A3LSMG991U</td> </tr> <tr> <td>APPLICANT:</td> <td>SAMSUNG Electronics Co., Ltd.</td> </tr> </table> Model: SM-G991U Additional Model: SM-G991U1 EUT Type: Mobile Phone RF Output Field Strength: 18.19 dBuV/m @30 m Frequency of Operation: 13.56 MHz Modulation type: ASK FCC Classification: Low Power Communication Device – Transmitter FCC Rule Part(s): FCC Part 15.225 Subpart C <i>FCC NFC Test Report for Samsung Galaxy S21 Smartphone at 1 (emphasis added).</i>	FCC ID:	A3LSMG991U	APPLICANT:	SAMSUNG Electronics Co., Ltd.
FCC ID:	A3LSMG991U				
APPLICANT:	SAMSUNG Electronics Co., Ltd.				

Exhibit C
iCashe's Infringement Contentions for U.S. Patent No. 9,483,722
Samsung Galaxy S21 5G Smartphone

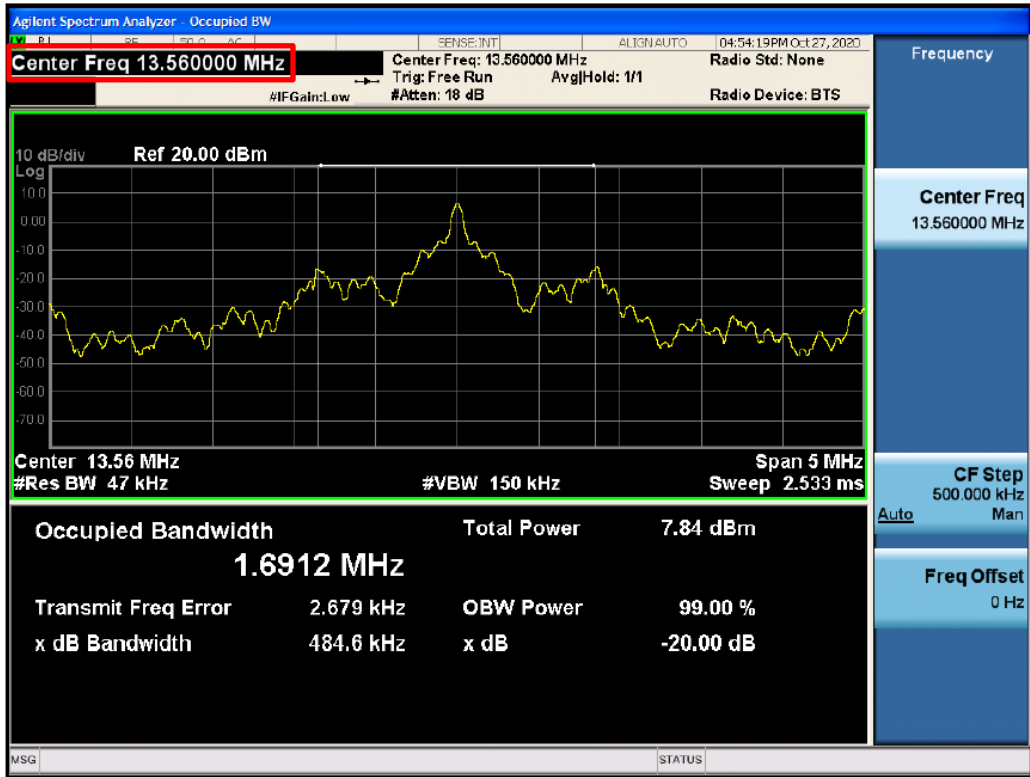
Claim Language	Infringement Evidence
	 <i>FCC NFC Test Report for Samsung Galaxy S21 Smartphone at 23 (emphasis added).</i> The NXP SN100T Secure Element Specification confirms that the embedded NFC controller of the SN110U supports Type A and Type B communications.

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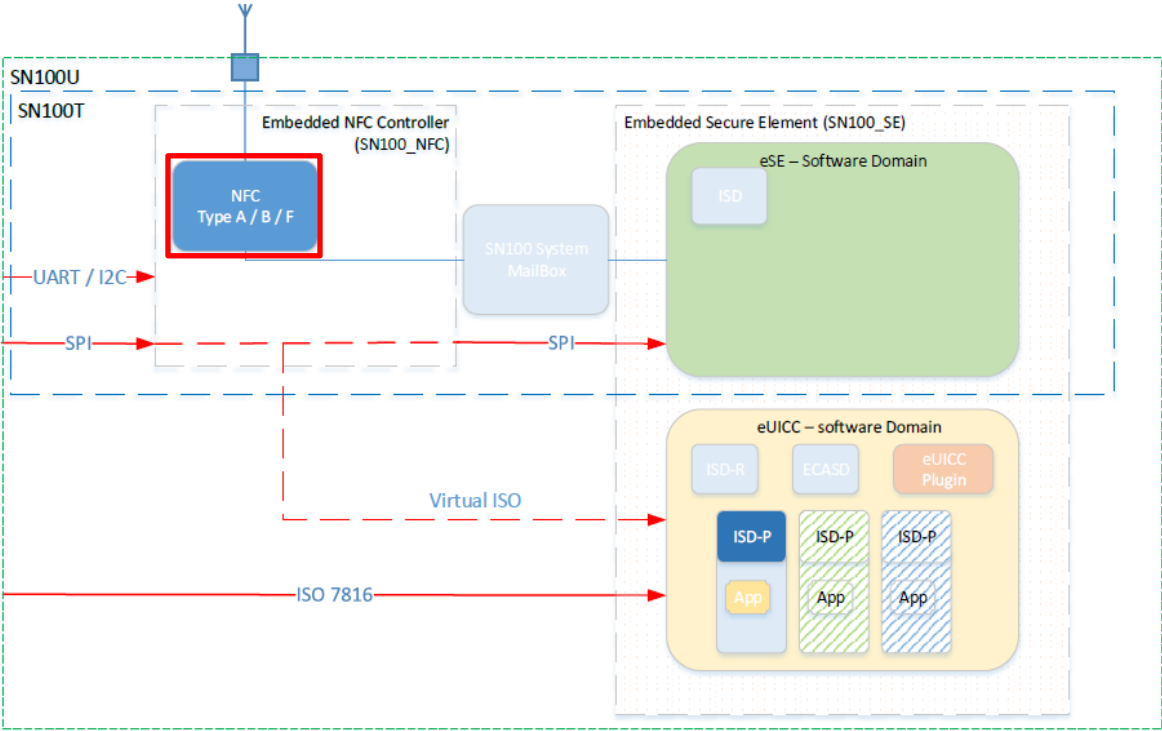
Claim Language	Infringement Evidence
	 Fig. 1.2: SN100x Product Configurations <i>NXP SN100T Secure Element Specification at 5 (emphasis added).</i> The EMVCo certification of the Galaxy S21 as an EMV Contactless Level 1 Mobile Product indicates that the Galaxy S21 supports Type A and Type B communications under the ISO/IEC 14443 standard.

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Claim Language	Infringement Evidence
	Contactless Communication Protocol: ☒ Type A☒ Type B Execution Environment: ☒ UICC² slot 1☐ UICC² slot 2 ☐ eSE☒ HCE ☐ eUICC Mobile Product Operating System Name and Version:Android Version 11.0 NFC Controller Name and Version:SN110U V3 Version N/A NFC Controller Firmware Name and Version:N/A Version FW01.10.5A https://www.emvco.com/wp-content/uploads/loa/MTA_LOA_SAEL_02859_24Nov20_SHORT.pdf (emphasis added). Like the SN110U, the PN7150 is a highly integrated NFC transceiver IC for contactless communication at 13.56 MHz that supports various ISO/IEC 14443 modes. The PN7150 is a highly integrated NFC transceiver IC for contactless communication at 13.56MHz. This transceiver IC utilizes an outstanding modulation and demodulation concept completely integrated for different kinds of contactless communication methods and protocols at 13.56 MHz. <i>PN7150 Antenna Design and Matching Guide</i> at 3.

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Claim Language	Infringement Evidence
	Plug'n play and high-performance full NFC solution PN7150 is a full NFC controller solution with integrated firmware and NCI interface designed for contactless communication at 13.56 MHz. It is compatible with NFC forum requirements. PN7150 is designed based on learnings from previous NXP NFC device generation. It is the ideal solution for rapidly integrating NFC technology in any application, especially those running O/S environment like Linux and Android, reducing Bill of Material (BOM) size and cost, thanks to: • Full NFC forum compliancy (see [1]) with small form factor antenna • Embedded NFC firmware providing all NFC protocols as pre-integrated feature • Direct connection to the main host or microcontroller, by I²C-bus physical and NCI protocol • Ultra-low power consumption in polling loop mode • Highly efficient integrated power management unit (PMU) allowing direct supply from a battery PN7150 embeds a new generation RF contactless front-end supporting various transmission modes according to NFCIP-1 and NFCIP-2, ISO/IEC 14443, ISO/IEC 15693, MIFARE Classic IC-based card and FeliCa card specifications. It embeds an Arm Cortex-M0 microcontroller core loaded with the integrated firmware supporting the NCI 1.0 host communication. It also allows to provide a higher output power by supplying the transmitter output stage from 3.0 V to 4.75 V. <i>PN7150 Data Sheet at 2.</i> Like the SN110U, the PN7150 is designed to be connected to an external coil antenna.

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Claim Language	Infringement Evidence
	The PN7150 is intended to be connected to an external coil antenna through a specific matching/tuning network. The purpose of this document is first to provide some guidelines regarding the design of the NFC antenna to be connected to the PN7150. It then depicts a measurement method in order to evaluate the performances of the antenna prior to connecting it to the NXP NFC chip. The next chapter explains how to determine the matching network to be placed between a given antenna and the PN7150 (based on the antenna electrical equivalent circuit) Then, an RF performance validation procedure is proposed. Finally, an example of PN7150 antenna and tuning design is given as reference. <i>PN7150 Antenna Design and Matching Guide</i> at 3. Given the similarities and common functionalities across the SN110U and the PN7150, and in the absence of publicly available information regarding the SN110U, detailed technical information regarding the PN7150 is set forth below for illustrative purposes and relied upon herein to demonstrate relevant functionality of the SN110U and surrounding circuitry. NXP provides block diagrams for the PN7150. Certain functional blocks of the PN7150 are demonstrative of some functional blocks of the SN110U chip for the reasons discussed above. Notably, as shown in the block diagram below, the PN7150 contains smartcard circuitry, including at least a Contactless Interface Unit (CIU). The functionality found in the CIU is relevant for reasons discussed below.

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Claim Language	Infringement Evidence
	aaa-016737 Fig 3. PN7150 block diagram <i>PN7150 Data Sheet at 10 (emphasis added).</i>

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Claim Language	Infringement Evidence
	On information and belief, the SN110U chip contains circuitry and functional blocks within the embedded NFC controller that correspond to certain circuitry and functional blocks within the CIU of the PN7150. As evidenced by the Galaxy S21's NFC-based Samsung Pay functionality, for example, the smartcard circuitry includes (1) a functional block to amplify the signal received from the NFC reader via the Galaxy S21's antenna (discussed below with respect to limitation 1.c), and (2) a functional block (i.e., the load modulation circuitry) for actively transmitting modulated data to the NFC reader for conducting the transaction. Many NXP NFC chips, including the SN110U, contain load modulation circuitry for active load modulation. In a paper published in 2012, the authors explain the benefits of active load modulation in NFC chips for use in mobile phones. Gebhart, Michael, et al., <i>Active Load Modulation for Contactless Near-Field Communication</i>, IEEE 2012 International Conference on RFID-Technologies and Applications (RFID-TA). The authors describe a test in which an NXP NFC chip (PN547) was operated to communicate with an NFC reader using each of active load modulation (ALM) and passive load modulation (PLM). As shown below, with ALM, the PN547 was capable of communicating with the NFC reader at significantly greater distances. The test shows that passive load modulation limited the communication distance between the chip and reader to less than 3cm, while ALM allowed for greater distances between the chip and reader.

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Claim Language	Infringement Evidence												
	<table><thead><tr><th>Configuration</th><th>30x17 PLM (mm)</th><th>30x17 ALM (mm)</th></tr></thead><tbody><tr><td>PN544 + Ant. 30 x 50 mm</td><td>~20</td><td>~70</td></tr><tr><td>PN512 + Ant. 30 x 50 mm</td><td>~25</td><td>~75</td></tr><tr><td>RC663 + Ant. 70 x 110</td><td>~2</td><td>~92</td></tr></tbody></table> Fig. 10. Comparison of operating distance achieved with ALM and PLM. Gebhart, Michael, et al., <i>Active Load Modulation for Contactless Near-Field Communication</i>, IEEE 2012 International Conference on RFID-Technologies and Applications (RFID-TA) at 5. Due to the demonstrated benefit of <i>active</i> load modulation, many NXP NFC chips contain ALM circuitry and functionality. For example, as shown below with respect to the PN7150 chip, the NXP SN110U chip and surrounding circuitry was designed for ALM. As further discussed below, testing indicates that the Galaxy S21 utilizes this ALM functionality.	Configuration	30x17 PLM (mm)	30x17 ALM (mm)	PN544 + Ant. 30 x 50 mm	~20	~70	PN512 + Ant. 30 x 50 mm	~25	~75	RC663 + Ant. 70 x 110	~2	~92
Configuration	30x17 PLM (mm)	30x17 ALM (mm)											
PN544 + Ant. 30 x 50 mm	~20	~70											
PN512 + Ant. 30 x 50 mm	~25	~75											
RC663 + Ant. 70 x 110	~2	~92											

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Claim Language	Infringement Evidence
	2.2 Single loop antenna dedicated for Active Load Modulation PN7150 was designed for Active Load Modulation (ALM) concept. ALM provides high performances and significant margins to NFC standard criteria. It also allows the use of smaller antenna. <i>PN7150 Antenna Design and Matching Guide at 4.</i>

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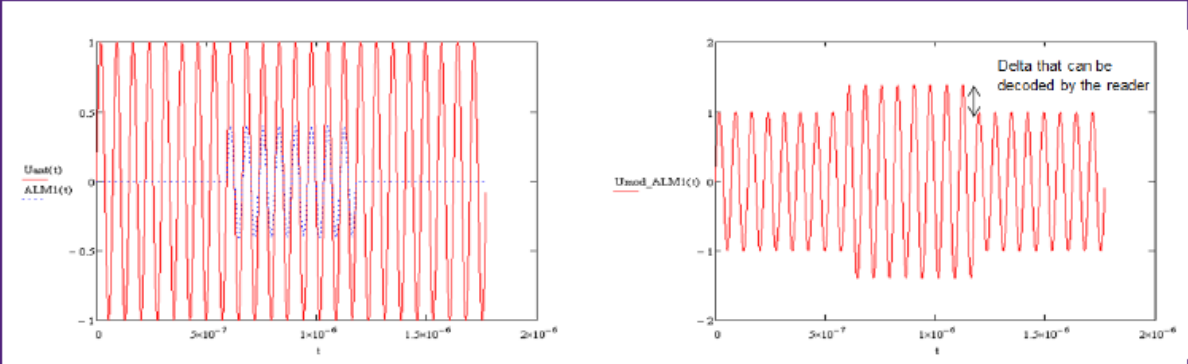
Claim Language	Infringement Evidence
	ALM Mode (SINGLE):  Fig 2. ALM mode SINGLE concept On the left graph the red 13.56MHz signal shows the voltage at the NFC antenna which is induced by the reader field, the blue curve shows the modulation pattern. This modulation pattern is generated by actively driving 13.56MHz with TX1 or TX2 while the other TX pin (TX2 or TX1) is kept silent. On the right we can see the modulated reader field. <i>PN7150 Antenna Design and Matching Guide at 5.</i>

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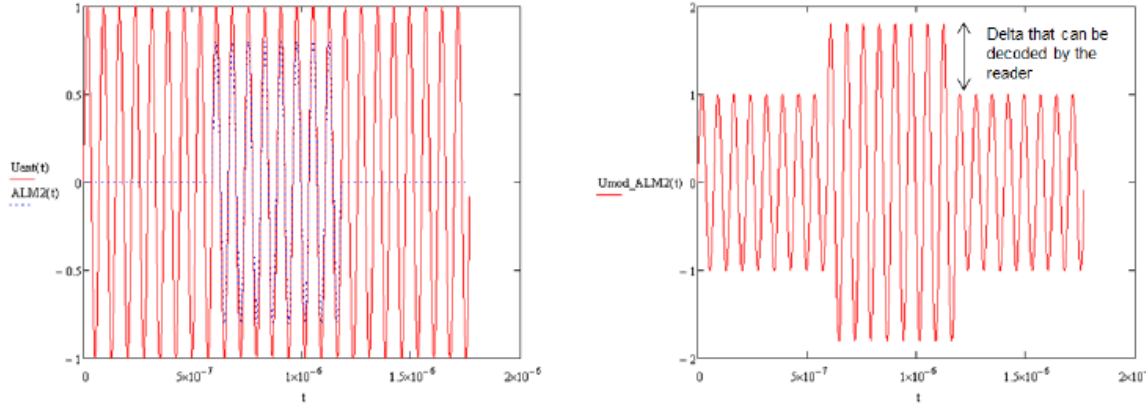
Claim Language	Infringement Evidence
	ALM Mode (DUAL):  Fig 3. ALM mode DUAL concept In this mode the modulation pattern is generated by actively driving 13.56MHz with TX1 and TX2. The modulation depth observed is twice the modulation depth of mode 1. <i>PN7150 Antenna Design and Matching Guide at 5.</i> As shown below, the PN7150 circuitry produces a modulation signal that is driven to the antenna via TX1 and/or TX2.

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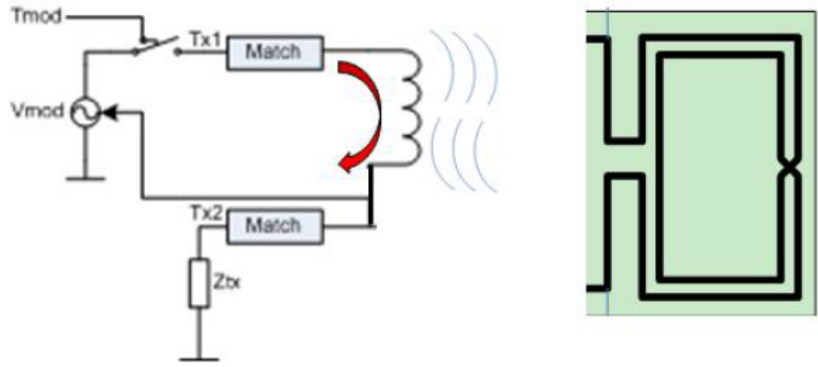
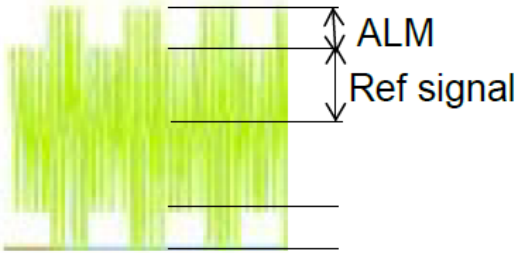
Claim Language	Infringement Evidence
	 Fig 4. Single Loop Antenna concept (mode 1 illustration) <i>PN7150 Antenna Design and Matching Guide at 6.</i>  Fig 5. Load modulation illustration

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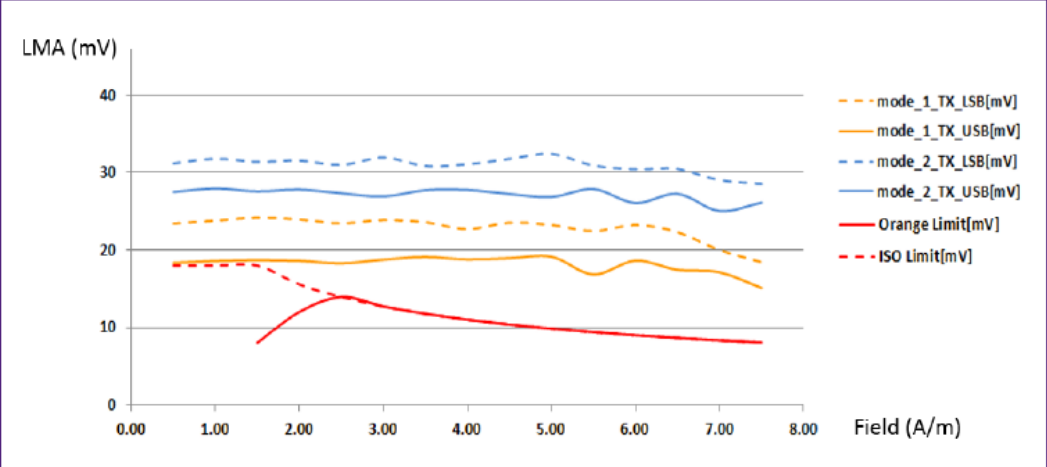
Claim Language	Infringement Evidence
	<i>PN7150 Antenna Design and Matching Guide</i> at 6. The ALM produces upper and lower sidebands with respect to the 13.56 MHz signal, as required by the Type A and Type B communications under the ISO/IEC 14443 standard as will be described. On <i>Fig 6</i> below LMA result examples are shown for Upper Side Band and Lower Side Band in the 2 modes available for PN7150 (respectively mode 1, mode 2). In order to minimize the phase error, it is necessary to limit the level of the load modulation amplitude (LMA) and to adjust the balance of the Side Bands when the field strength is low.  Fig 6. Example Side Bands ISO results for modes 1, 2

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Claim Language	Infringement Evidence
	<i>PN7150 Antenna Design and Matching Guide</i> at 7. On information and belief, the SN110U chip contains functional blocks and circuitry within the embedded NFC controller that correspond to the active load modulation circuitry shown and described with respect to the PN7150. The EMVCo certification of the Galaxy S21 as an EMV Contactless Level 1 Mobile Product indicates that the Galaxy S21 has active load modulation circuitry compliant with the requirements of the EMV Level 1 Specifications for Payment Systems. The EMVCo Certification further indicates that the Galaxy S21 supports Type A and Type B communications under the ISO/IEC 14443 standard.

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	Re: <i>EMVCo Letter of Approval – EMV Contactless Level 1 Mobile Product</i> <table style="width: 100%;"><tr><td style="width: 40%;">Approval Number:</td><td>MTA_LOA_SAEL_02859</td></tr><tr><td>Approval Date:</td><td>November 4, 2020</td></tr><tr><td>EMVCo Website Publication Date:</td><td>January 15, 2021</td></tr><tr><td>Approval Expiration Date:</td><td>November 2, 2023</td></tr></table> <table style="width: 100%;"><tr><td style="width: 40%;">Product Provider Name:</td><td>SAMSUNG ELECTRONICS Co., Ltd</td></tr><tr><td>Registration Number:</td><td>SAEL.V 14 0068</td></tr><tr><td>Commercial Name:</td><td>Galaxy S21 5G</td></tr><tr><td>Product Name:</td><td>SM-G991B/DS</td></tr><tr><td>Product Version:</td><td>V1</td></tr><tr><td>Type Allocation Code (TAC) or product identifier for testing sample:</td><td>35285930</td></tr><tr><td>Type Allocation Code (TAC) or product identifier for production²:</td><td>35285930</td></tr><tr><td>Implementation Conformance Statement:</td><td>MTA_ICS_SAEL_02859_71</td></tr><tr style="border: 2px solid red;"><td>EMV Specification:</td><td>Version 3.0</td></tr><tr><td>EMV Specification Bulletins:</td><td>N/A</td></tr><tr><td>Contactless Level 1 Analog Test Cases:</td><td>Version 3.0a</td></tr><tr><td>Contactless Level 1 Digital Test Cases:</td><td>Version 3.0a</td></tr><tr><td>Contactless Level 1 Performance Test Cases:</td><td>Version 2.3</td></tr><tr><td>Contactless Level 1 Interoperability Test Requirements:</td><td>Version 1.4</td></tr><tr><td>Contactless Level 1 Test Cases Bulletins:</td><td>N/A</td></tr></table>	Approval Number:	MTA_LOA_SAEL_02859	Approval Date:	November 4, 2020	EMVCo Website Publication Date:	January 15, 2021	Approval Expiration Date:	November 2, 2023	Product Provider Name:	SAMSUNG ELECTRONICS Co., Ltd	Registration Number:	SAEL.V 14 0068	Commercial Name:	Galaxy S21 5G	Product Name:	SM-G991B/DS	Product Version:	V1	Type Allocation Code (TAC) or product identifier for testing sample:	35285930	Type Allocation Code (TAC) or product identifier for production ² :	35285930	Implementation Conformance Statement:	MTA_ICS_SAEL_02859_71	EMV Specification:	Version 3.0	EMV Specification Bulletins:	N/A	Contactless Level 1 Analog Test Cases:	Version 3.0a	Contactless Level 1 Digital Test Cases:	Version 3.0a	Contactless Level 1 Performance Test Cases:	Version 2.3	Contactless Level 1 Interoperability Test Requirements:	Version 1.4	Contactless Level 1 Test Cases Bulletins:	N/A
Approval Number:	MTA_LOA_SAEL_02859																																						
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Product Provider Name:	SAMSUNG ELECTRONICS Co., Ltd																																						
Registration Number:	SAEL.V 14 0068																																						
Commercial Name:	Galaxy S21 5G																																						
Product Name:	SM-G991B/DS																																						
Product Version:	V1																																						
Type Allocation Code (TAC) or product identifier for testing sample:	35285930																																						
Type Allocation Code (TAC) or product identifier for production ² :	35285930																																						
Implementation Conformance Statement:	MTA_ICS_SAEL_02859_71																																						
EMV Specification:	Version 3.0																																						
EMV Specification Bulletins:	N/A																																						
Contactless Level 1 Analog Test Cases:	Version 3.0a																																						
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Contactless Level 1 Performance Test Cases:	Version 2.3																																						
Contactless Level 1 Interoperability Test Requirements:	Version 1.4																																						
Contactless Level 1 Test Cases Bulletins:	N/A																																						

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Claim Language	Infringement Evidence
	Contactless Communication Protocol: ☒ Type A ☒ Type B Execution Environment: ☒ UICC² slot 1 ☐ UICC² slot 2 ☐ eSE ☒ HCE ☐ eUICC Mobile Product Operating System Name and Version: Android Version 11.0 NFC Controller Name and Version: SN110U V3 Version N/A NFC Controller Firmware Name and Version: N/A Version FW01.10.5A As a result, EMVCo has found reasonable evidence that the submitted samples sufficiently conform to the EMV Contactless Interface Specification version 3.0 and other EMVCo requirements. This finding is subject to the restrictions and comments in Attachment A. For a more detailed listing of the features supported by the product please review the Implementation Conformance Statement. EMVCo hereby (a) grants your EMVCo mobile type approval, based on the requirements stated in EMV Contactless Interface Specification version 3.0 and other EMVCo requirements, and (b) agrees to include your EMV mobile product in EMVCo's list of approved products. EMVCo mobile type approval does not extend to portions or components of a card or application that are not defined by EMV Contactless Interface Specification version 3.0. EMVCo's grant to your EMV mobile product is subject to and specifically incorporates (i) the General Terms and Conditions to the Letter of Approval enclosed as Exhibit A, and (ii) the Specific Terms and Conditions to the Letter of Approval attached hereto as Attachment A. Because EMVCo's grant is subject to such limitations, including certain events of termination, you and any third parties should confirm that such approval is current and has not been terminated by referring to the EMVCo list of approved products. https://www.emvco.com/wp-content/uploads/loa/MTA_LOA_SAEL_02859_24Nov20_SHORT.pdf (emphasis added).

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Claim Language	Infringement Evidence
	The <i>EMV Level 1, Contactless Interface Specification</i>, Version 3.0 (“<i>EMV Level 1 Specification</i>”) confirms that compliant devices, including the Galaxy S21, use load modulation for both Type A and Type B communications. Whether a device meets the reception requirements is measured by having the appropriate EMV Contactless Level 1 Test Equipment transmit a range of values for a number of parameters. That is: • Whether the receiver of a PCD meets the requirements is measured by having the EMV – TEST PICCs transmit different levels of load modulation. • The quality of the receiver of a PICC is verified by having the EMV – TEST PCD transmit different levels of modulation. In order to set up the transmitter of the EMV Contactless Level 1 Test Equipment, the receiver of the matching EMV Contactless Level 1 Test Equipment is used. That is: • The load modulation level of an EMV – TEST PICC is characterized with respect to the EMV – TEST PCD. • The modulation level of the EMV – TEST PCD is characterized with respect to the EMV – TEST PICCs. <i>EMV Level 1 Specification</i> at 24.

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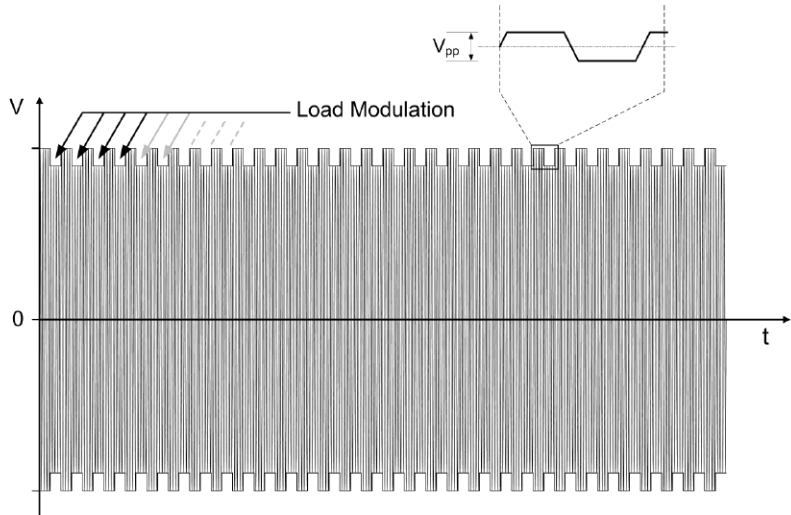
Claim Language	Infringement Evidence
	3.4.1 Introduction For the communication from PICC to PCD, both Type A and Type B use load modulation as shown in Figure 3.3. The carrier frequency f_c is used to derive a subcarrier with frequency f_s equal to $f_c/16$ (~847 kHz). Switching a load on and off at this frequency creates the subcarrier causing a different current to flow through the antenna of the PICC when in the loaded state than when in the unloaded state (the unloaded state of the subcarrier is the stable state when the PICC is not sending bits). This difference in current in the PICC antenna is sensed by the PCD. Figure 3.3: Load Modulation  Type A modulates the subcarrier using On-Off Keying (OOK). <i>EMV Level 1 Specification at 56.</i>

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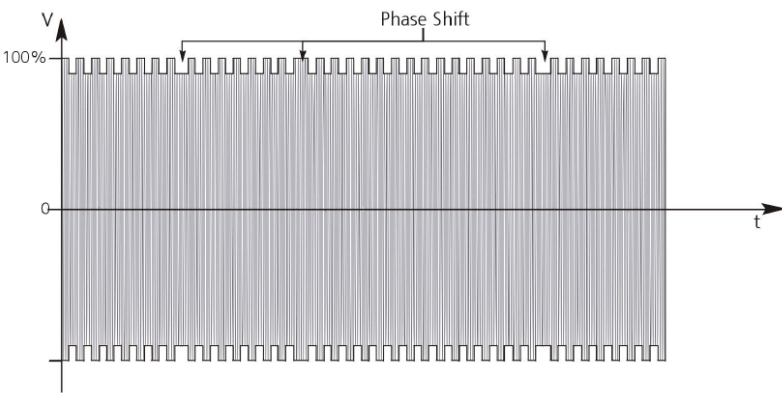
Claim Language	Infringement Evidence
	Type B modulates the subcarrier using Binary Phase Shift Keying (BPSK) as shown in Figure 3.4. BPSK uses two signal phases: 0 degrees and 180 degrees. If the phase of the wave does not change with regard to a reference phase, then the signal state stays the same (low or high). If the phase of the wave changes by 180 degrees (i.e. the phase reverses) then the signal state changes. The reference phase is referred to as $\emptyset 0$. Figure 3.4: BPSK  Note that, although this specification considers load modulation on a purely amplitude basis, in reality the load modulation produced by a PICC has a mixture of amplitude and phase components and consequently it is anticipated that future versions of this specification will introduce requirements that PCDs are able to receive load modulation signals that are comprised of a mixture of amplitude and phase modulation, e.g. by using IQ demodulation. All measurements described in this section are performed with the EMV – TEST PCD and the EMV – TEST PICCs calibrated as specified in <i>EMV Contactless Specifications for Payment Systems – Level 1 – Test Equipment Specifications – PCD Manual</i> and <i>EMV Contactless Specifications for Payment Systems – Level 1 – Test Equipment Specifications – PICC Manual</i>. The position of a PICC within the Operating Volume is indicated according to the convention specified in Annex B.

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Claim Language	Infringement Evidence
	<i>EMV Level 1 Specification</i> at 57. The <i>ISO/IEC 14443-2:2020(E) Standard</i> (“<i>ISO/IEC 14443-2:2020(E)</i>”) further confirms that compliant devices, including the Galaxy S21, use load modulation for both Type A and Type B communications.

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Claim Language	Infringement Evidence												
	ISO/IEC 14443-2:2020(E) In order to transmit data to the PCD, the PICC passively loads the PCD alternating magnetic field and/or actively contributes with its own alternating magnetic field. This is called load modulation. Within the manufacturer specified operating volumes (see 3.6), the PCD shall generate modulation pulses as described in Clauses 8 and 9 and shall be capable of receiving the minimum load modulation amplitude. NOTE 1 As an indication of the operating volume, the manufacturer can give the operating range (e.g. 0 to X cm) within which all requirements of this document are fulfilled. Test methods for the PCD communication signal interface are defined in ISO/IEC 10373-6. Two communication signal interfaces, Type A and Type B, are described in Clauses 8 and 9. The PCD shall alternate between modulation methods when idling before detecting the presence of a PICC of Type A or Type B. Only one communication signal interface may be active during a communication session until deactivation by the PCD or removal of the PICC. Subsequent session(s) may then proceed with either modulation method. Table 3 and Table 4 illustrate the concepts described in Clauses 8 and 9. Table 3 — Example PCD to PICC communication signals for Type A and Type B interfaces <table><tr><td></td><td>Type A</td><td>Type B</td></tr><tr><td>bit rates of $f_c/128$, $f_c/64$, $f_c/32$, and $f_c/16$</td><td>ASK ~ 100 % modified Miller </td><td>ASK ~ 10 % NRZ-L </td></tr><tr><td>bit rates of $f_c/8$, $f_c/4$, and $f_c/2$</td><td colspan="2">ASK ~ 10 % NRZ-L </td></tr><tr><td>bit rates of $3f_c/4$, f_c, $3f_c/2$, and $2f_c$</td><td colspan="2">PSK, see 8.1.2.5.</td></tr></table> NOTE 2 For the coding of modified Miller, see 8.1.3.1. ISO/IEC 14443-2:2020(E) at 8.		Type A	Type B	bit rates of $f_c/128$, $f_c/64$, $f_c/32$, and $f_c/16$	ASK ~ 100 % modified Miller 	ASK ~ 10 % NRZ-L 	bit rates of $f_c/8$, $f_c/4$, and $f_c/2$	ASK ~ 10 % NRZ-L 		bit rates of $3f_c/4$, f_c , $3f_c/2$, and $2f_c$	PSK, see 8.1.2.5.	
	Type A	Type B											
bit rates of $f_c/128$, $f_c/64$, $f_c/32$, and $f_c/16$	ASK ~ 100 % modified Miller 	ASK ~ 10 % NRZ-L 											
bit rates of $f_c/8$, $f_c/4$, and $f_c/2$	ASK ~ 10 % NRZ-L 												
bit rates of $3f_c/4$, f_c , $3f_c/2$, and $2f_c$	PSK, see 8.1.2.5.												

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Samsung Galaxy S21 5G Smartphone

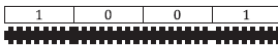
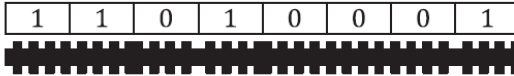
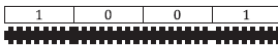
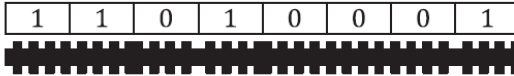
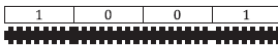
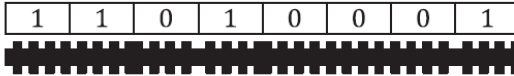
Claim Language	Infringement Evidence																																																																													
	Table 4 — Example PICC to PCD communication signals for Type A and Type B interfaces <table><tr><td></td><td>Type A</td><td>Type B</td></tr><tr><td>bit rate of $f_c/128$</td><td>Load modulation Subcarrier $f_c/16$ OOK Manchester <table><tr><td>0</td><td>1</td><td>0</td><td>0</td></tr></table></td><td>Load modulation Subcarrier $f_c/16$ BPSK NRZ-L <table><tr><td>1</td><td>0</td><td>0</td><td>1</td></tr></table></td></tr><tr><td>bit rate of $f_c/64$</td><td colspan="2">Load modulation Subcarrier $f_c/16$ BPSK NRZ-L <table><tr><td>1</td><td>1</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>1</td></tr></table></td></tr></table> Table 4 (continued) <table><tr><td></td><td>Type A</td><td>Type B</td></tr><tr><td>bit rate of $f_c/32$</td><td colspan="2">Load modulation Subcarrier $f_c/16$ BPSK NRZ-L <table><tr><td>1</td><td>1</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td>1</td><td>1</td><td>1</td></tr></table></td></tr><tr><td>bit rates of $f_c/16, f_c/8, f_c/4,$ and $f_c/2$</td><td colspan="2">Load modulation Subcarrier equals the bit rate BPSK NRZ-L <table><tr><td>1</td><td>1</td><td>1</td><td>0</td><td>1</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td><td>1</td><td>0</td><td>1</td><td>1</td><td>1</td></tr></table></td></tr></table> ISO/IEC 14443-2:2020(E) at 8-9.		Type A	Type B	bit rate of $f_c/128$	Load modulation Subcarrier $f_c/16$ OOK Manchester <table><tr><td>0</td><td>1</td><td>0</td><td>0</td></tr></table> 	0	1	0	0	Load modulation Subcarrier $f_c/16$ BPSK NRZ-L <table><tr><td>1</td><td>0</td><td>0</td><td>1</td></tr></table> 	1	0	0	1	bit rate of $f_c/64$	Load modulation Subcarrier $f_c/16$ BPSK NRZ-L <table><tr><td>1</td><td>1</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>1</td></tr></table> 		1	1	0	1	0	0	0	1		Type A	Type B	bit rate of $f_c/32$	Load modulation Subcarrier $f_c/16$ BPSK NRZ-L <table><tr><td>1</td><td>1</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td>1</td><td>1</td><td>1</td></tr></table> 		1	1	0	1	0	0	0	1	0	0	0	0	1	1	1	1	bit rates of $f_c/16, f_c/8, f_c/4,$ and $f_c/2$	Load modulation Subcarrier equals the bit rate BPSK NRZ-L <table><tr><td>1</td><td>1</td><td>1</td><td>0</td><td>1</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td><td>1</td><td>0</td><td>1</td><td>1</td><td>1</td></tr></table> 		1	1	1	0	1	1	1	1	0	0	0	0	1	0	0	1	1	0	0	1	1	0	1	0	1	1	1
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Claim Language	Infringement Evidence
	Testing of the Galaxy S21 further indicates the presence of the load modulation circuitry discussed above. As shown below in Figure 1, which displays the RF signal analysis during an ongoing transaction with an NFC reader, the Galaxy S21 emits two actively modulated sideband spikes plus the carrier frequency (13.56 MHz), i.e., at least one frequency sideband about a carrier frequency of an interrogating RF field. This confirms the presence of the load modulation circuitry discussed above. The screenshot shows the Tek SignalVu-PC interface. The main window displays a spectrum plot with a central peak at 13.56 MHz and two sideband spikes labeled M1 and M2. The interface includes various controls like Time/div, RBW, DPXogram, and a replay section at the bottom. <i>Figure 1: Samsung Galaxy S21 5G with ongoing transaction showing actively modulated spikes of power</i>

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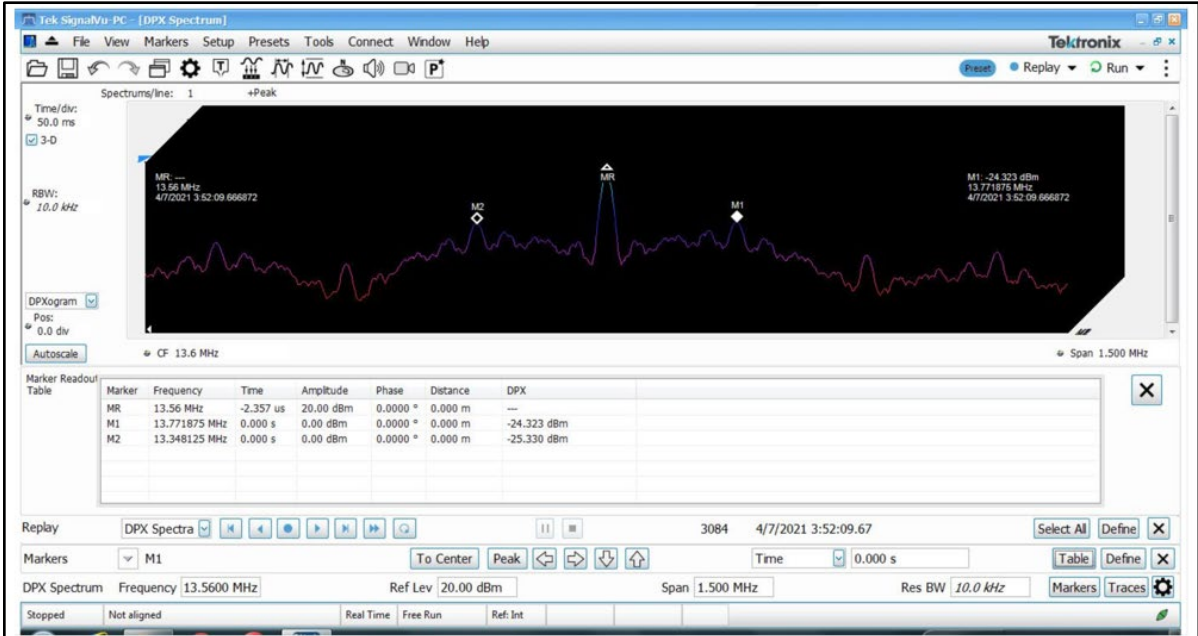
Claim Language	Infringement Evidence
	Figure 2 below displays the RF signal analysis of the Galaxy S21 with the Samsung Pay application enabled but with no NFC reader present. Like Figure 1, the figure shows the frequency spectrum enabled by the SN110U consists of two actively modulated spikes (at $f_c/64$) plus the carrier frequency (13.56 MHz), i.e., at least one frequency sideband about a carrier frequency of an interrogating RF field. Thus, Figure 2 demonstrates that the NFC transmission from the Galaxy S21 does not depend on coupling with the NFC reader and is therefore the product of active load modulation circuitry of the SN110U.  <i>Figure 2: Samsung Galaxy S21 5G near Square reader showing actively modulated spikes of power</i>

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	Additionally, as shown below, the <i>PN7150 Antenna Design and Matching Guide</i> depicts the transmit (Tx) and receive (Rx) paths between the Tx and Rx pads of the NFC chip, discussed above, and the antenna coil and matching circuitry. On information and belief and for the reasons discussed above, the SN110U chip and surrounding circuitry, matching circuitry, and antenna coil in the Galaxy S21 is configured in substantially the same manner in relevant part. Fig 21. Rx path <i>PN7150 Antenna Design and Matching Guide</i> at 22. The load modulation circuitry is coupled to the TX pad(s) to form an outgoing data path. The amplifier, discussed below regarding limitation 1.c, is coupled to the RX pad to form an incoming data path. The load modulation circuitry thereby provides for half duplex communication, as per the NFC, ISO/IEC 14443 standard, the communications are at half-duplex, i.e., only TX or RX occurs at a given instance.

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	This specification addresses the digital protocol for NFC-enabled device communication, providing an implementation specification on top of the ISO/IEC 18092 and ISO/IEC 14443 standards. It harmonizes the integrated technologies, specifies implementation options and limits the interpretation of the standards; in essence, showing developers how to use NFC, ISO/IEC 14443 and JIS X6319-4 standards together to ensure global interoperability between different NFC devices, and between NFC devices and existing contactless infrastructure. The specification defines the common feature set that can be used consistently and without further modification for major NFC applications in areas such as financial services and public transport. The specification covers the digital interface and the half-duplex transmission protocol of the NFC-enabled device in its four roles as Initiator, Target, Reader/Writer and Card Emulator. It includes bit level coding, bit rates, frame formats, protocols, and command sets, which are used by NFC-enabled devices to exchange data and bind to the LLCP protocol. https://nfc-forum.org/build/specifications.
[1.b] an antenna tuned to operate at 13.56 MHz; and	The Galaxy S21 contains an antenna tuned to operate at 13.56 MHz. The antenna, used to transmit and receive NFC information and transaction sequences, is part of the NFC antenna and charging coil assembly component depicted below.

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Claim Language	Infringement Evidence
	 Teardown image from Samsung Galaxy S21 5G smartphone (emphasis added).

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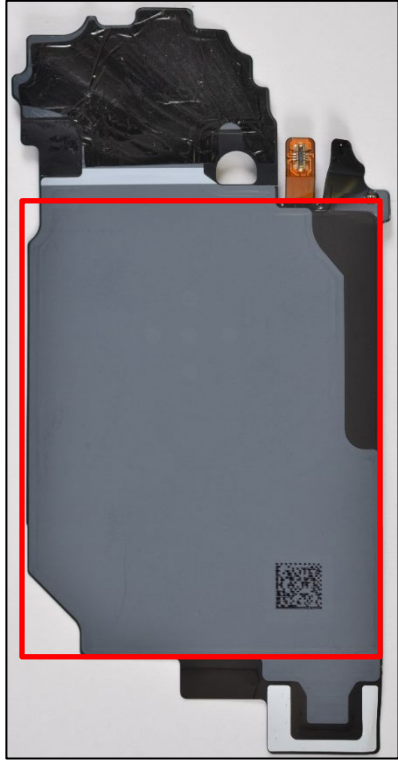
Claim Language	Infringement Evidence
	 Teardown image from Samsung Galaxy S21 5G smartphone (emphasis added). The antenna is tuned to operate at 13.56 MHz. As shown below, the NFC system of the Galaxy S21 operates at 13.56 MHz.

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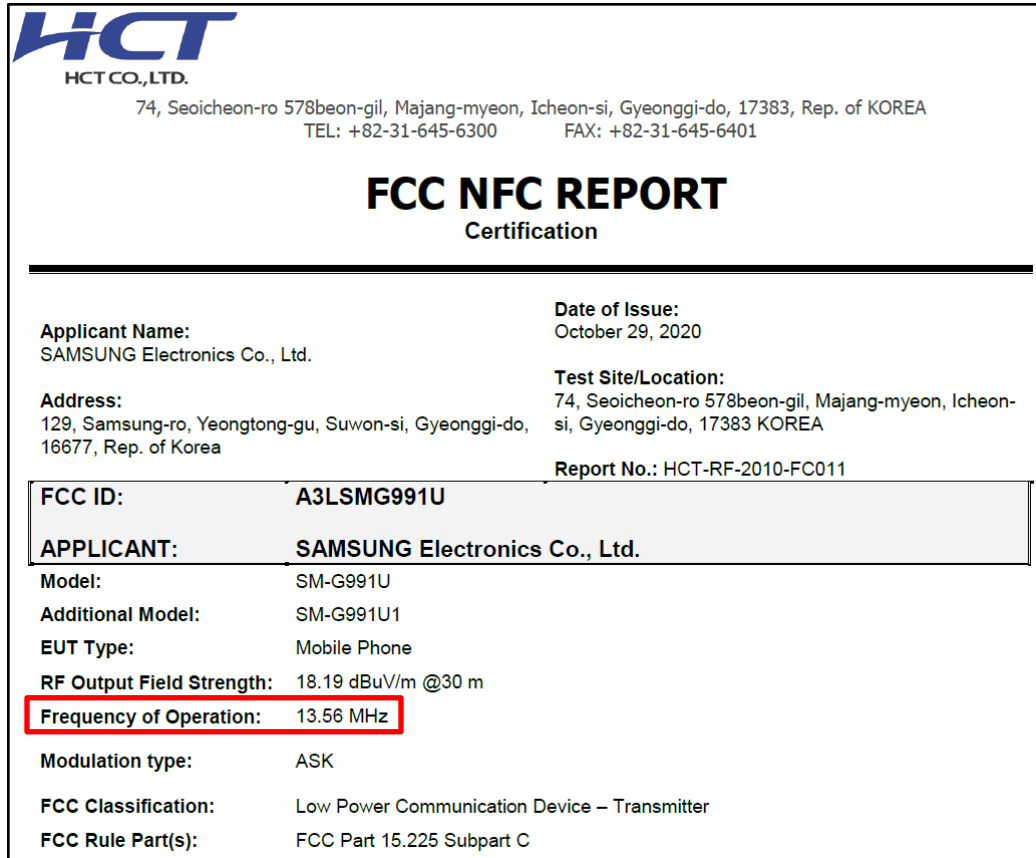
Claim Language	Infringement Evidence																										
	 HCT HCT CO., LTD. 74, Seoicheon-ro 578beon-gil, Majang-myeon, Icheon-si, Gyeonggi-do, 17383, Rep. of KOREA TEL: +82-31-645-6300 FAX: +82-31-645-6401 FCC NFC REPORT Certification <table> <tr> <td>Applicant Name: SAMSUNG Electronics Co., Ltd.</td> <td>Date of Issue: October 29, 2020</td> </tr> <tr> <td>Address: 129, Samsung-ro, Yeongtong-gu, Suwon-si, Gyeonggi-do, 16677, Rep. of Korea</td> <td>Test Site/Location: 74, Seoicheon-ro 578beon-gil, Majang-myeon, Icheon-si, Gyeonggi-do, 17383 KOREA</td> </tr> <tr> <td colspan="2">Report No.: HCT-RF-2010-FC011</td> </tr> </table> <table border="1"> <tr> <td>FCC ID:</td> <td>A3LSMG991U</td> </tr> <tr> <td>APPLICANT:</td> <td>SAMSUNG Electronics Co., Ltd.</td> </tr> </table> <table> <tr> <td>Model:</td> <td>SM-G991U</td> </tr> <tr> <td>Additional Model:</td> <td>SM-G991U1</td> </tr> <tr> <td>EUT Type:</td> <td>Mobile Phone</td> </tr> <tr> <td>RF Output Field Strength:</td> <td>18.19 dBuV/m @30 m</td> </tr> <tr> <td>Frequency of Operation:</td> <td>13.56 MHz</td> </tr> <tr> <td>Modulation type:</td> <td>ASK</td> </tr> <tr> <td>FCC Classification:</td> <td>Low Power Communication Device – Transmitter</td> </tr> <tr> <td>FCC Rule Part(s):</td> <td>FCC Part 15.225 Subpart C</td> </tr> </table> <i>FCC NFC Test Report for Samsung Galaxy S21 Smartphone at 1(emphasis added).</i>	Applicant Name: SAMSUNG Electronics Co., Ltd.	Date of Issue: October 29, 2020	Address: 129, Samsung-ro, Yeongtong-gu, Suwon-si, Gyeonggi-do, 16677, Rep. of Korea	Test Site/Location: 74, Seoicheon-ro 578beon-gil, Majang-myeon, Icheon-si, Gyeonggi-do, 17383 KOREA	Report No.: HCT-RF-2010-FC011		FCC ID:	A3LSMG991U	APPLICANT:	SAMSUNG Electronics Co., Ltd.	Model:	SM-G991U	Additional Model:	SM-G991U1	EUT Type:	Mobile Phone	RF Output Field Strength:	18.19 dBuV/m @30 m	Frequency of Operation:	13.56 MHz	Modulation type:	ASK	FCC Classification:	Low Power Communication Device – Transmitter	FCC Rule Part(s):	FCC Part 15.225 Subpart C
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FCC ID:	A3LSMG991U																										
APPLICANT:	SAMSUNG Electronics Co., Ltd.																										
Model:	SM-G991U																										
Additional Model:	SM-G991U1																										
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	 <i>FCC NFC Test Report for Samsung Galaxy S21 Smartphone at 23 (emphasis added).</i>
[1.c] at least one active circuit coupled between the smartcard controller and the antenna, wherein the at least one active circuit includes an amplifier	The Galaxy S21 includes at least one active circuit coupled between the smartcard controller and the antenna, wherein the at least one active circuit includes an amplifier coupled to be powered by the mobile device, and wherein the amplifier is coupled to amplify a signal received from the antenna and to provide an amplified signal to the smartcard controller.

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coupled to be powered by the mobile device, and wherein the amplifier is coupled to amplify a signal received from the antenna and to provide an amplified signal to the smartcard controller, and	The at least one active circuit coupled between the smartcard controller and the antenna includes an amplifier. As discussed above regarding limitation 1.a, the SN110U chip contains a secure element and an embedded NFC controller and associated blocks and functionality for communicating with a point-of-sale terminal via NFC protocols. Portions of the embedded secure element of the SN110U form all or part of the smartcard controller within the Galaxy S21. Additionally, portions of the embedded NFC controller of the SN110U may form part of the smartcard controller within the Galaxy S21.

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	Fig. 1.2: SN100x Product Configurations <i>NXP SN100T Secure Element Specification at 5.</i> The circuitry of the SN110U, including the embedded NFC controller and its functional blocks and circuitry, as well as the embedded secure element, has smartcard circuitry that performs various

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	signal amplification and load modulation functions that provide for communication with a point-of-sale terminal. As evidenced by the Galaxy S21's NFC-based Samsung Pay functionality, for example, the smartcard circuitry includes a functional block to amplify the signal received from the NFC reader via the Galaxy S21's antenna. The Galaxy S21 is able to perform NFC payment transactions with an NFC reader both at a distance and at off-angles from the reader. An NFC payment transaction can be completed by placing the Galaxy S21 (and specifically its antenna) "within four centimeters" of an NFC payment terminal. This distance indicates that the received signal is necessarily amplified, as further discussed below.

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	NFC and payment Near Field Communication (NFC) allows you to communicate with another device without connecting to a network. This technology is used by Android Beam and certain payment apps. The device that you are transferring to needs to support NFC, and it needs to be within four centimeters of your device. ○ From Settings, tap  Connections > NFC and contactless payments, and then tap  to turn on this feature. Tap and pay Use an NFC payment app to make payments by touching your device to a compatible credit card reader. 1. From Settings, tap  Connections > NFC and contactless payments, and then tap  to turn on NFC. 2. Tap Contactless payments to see the default payment app. • To use another payment app, tap an available app to choose it. • To use a payment app that is open, tap Pay with currently open app. • To set another payment service as the default, tap Others, and then tap the service you prefer.  TIP NFC technology is used with  Samsung Pay. Turn on this feature to see how easy and secure it is to use your device to make payments. <i>Galaxy S21 User Manual</i> at 118 (emphasis added). As discussed above as to limitation 1.a, the NXP SN110U chip and surrounding circuitry operates in substantially the same manner in relevant part as the PN7150 and its surrounding circuitry. On

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	information and belief, the NXP SN110U chip and surrounding circuitry include a baseband amplifier that operates to amplify received antenna signals as described below. The <i>PN7150 Antenna Design and Matching Guide</i> describes the manner in which the PN7150 amplifies a received signal. As shown below, the <i>PN7150 Antenna Design and Matching Guide</i> depicts the receive (Rx) path between the antenna, the matching circuitry, and the NFC chip itself. The diagram, labeled 'Fig 21. Rx path', shows the internal structure of an NFC chip (NFCC) and its connection to an external antenna. The NFCC is represented by a blue rounded rectangle. Inside, there are two red zigzag lines labeled 'Ragc' (receive antenna gain control) connected to a '0.9V' supply. The NFCC has several pins: 'RXp' at the top, 'TX1' and 'TX2' in the middle, 'VMID' below them, and 'RXn' at the bottom. To the right of the NFCC is a blue rounded rectangle labeled 'Matching + antenna'. This block is connected to the NFCC pins: 'RXp' is connected to the top of the 'Matching + antenna' block, 'TX1' and 'TX2' are connected to its side, and 'RXn' is connected to the bottom. The 'Matching + antenna' block is also connected to two external resistors labeled 'Rrx', one at the top and one at the bottom, which are connected to a common ground line. <i>PN7150 Antenna Design and Matching Guide</i> at 22. The Rx path is received at Rx pins (RXN and RXP), as shown below with respect to the PN7150.

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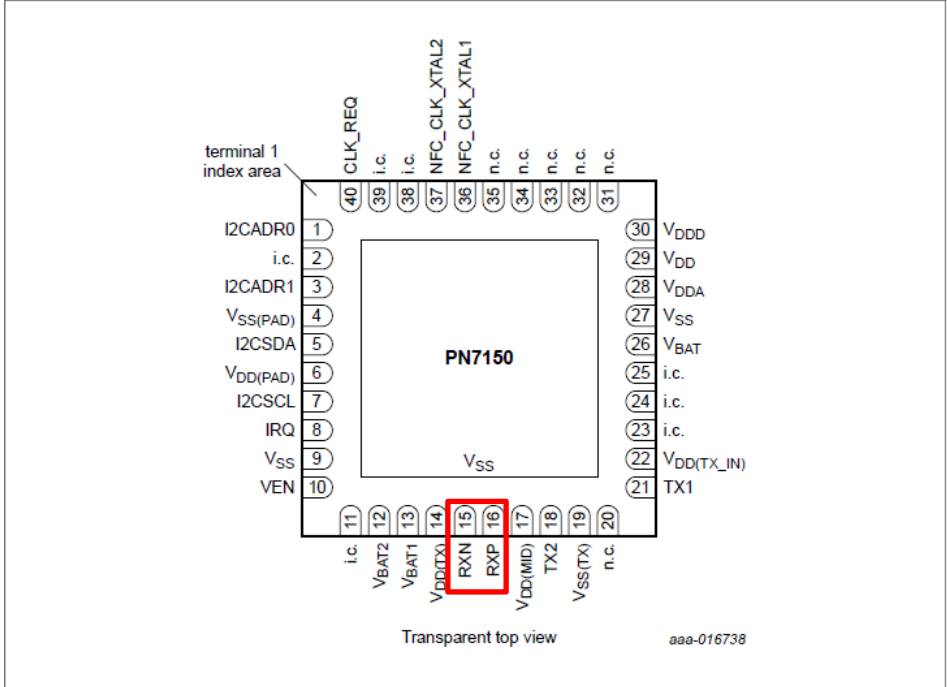
Claim Language	Infringement Evidence
	 Fig 4. Pinning PN7150 Data Sheet at 11 (emphasis added). The PN7150 Antenna Design and Matching Guide describes setting an amplification level of a baseband amplifier (i.e., RX_GAIN) of the NFC chip to amplify a received Rx signal.

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	5.5.3 Configuring registers in Reader Mode for Rx path optimization 5.5.3.1 CLIF_ANA_RX_REG a. Register definition CLIF_ANA_RX_REG can be fine-tuned to improve the analog down-sampling and baseband amplification of the card response before it is processed by the digital block. <i>PN7150 Antenna Design and Matching Guide at 62.</i>

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	○ [1:0]: RX_GAIN Set the amplification level of the BaseBandAmplifier - Care: The gain must be set in combination with the HPCF parameter taking into account the optimization of the disturbances in the down-mixed RX signal. - Value range: 10b ... 11b High performance & sensitivity for max reading rang: Strongly depends on the SNR in the system 01b ... 10b Typical 00b High robustness & stability but low reading range Table 17. CLIF_ANA_RX_REG register in reader mode <table><tr><th>Bit</th><th>Symbol</th><th>Description</th></tr><tr><td>[15:4]</td><td>Internal use</td><td>Must not be modified</td></tr><tr><td>[3:2]</td><td>RX_HPCF</td><td>Lower Corner Frequency: 00->45kHz 01->85kHz 10->150kHz 11->250kHz</td></tr><tr><td>[1:0]</td><td>RX_GAIN</td><td>Gain Adjustment BBA: 00->33dB</td></tr></table> PN7150 Antenna Design and Matching Guide at 63. Although certain information regarding the functional blocks and circuitry of the baseband amplifier in the SN110U and PN7150 is not publicly available, NXP also has published detailed information about the functional blocks and circuitry of other NXP NFC chips, including the PN5180. The PN5180 is a highly integrated high performance full NFC Forum-compliant frontend IC for contactless communication at 13.56 MHz. Functionality of the PN5180 is described in the	Bit	Symbol	Description	[15:4]	Internal use	Must not be modified	[3:2]	RX_HPCF	Lower Corner Frequency: 00->45kHz 01->85kHz 10->150kHz 11->250kHz	[1:0]	RX_GAIN	Gain Adjustment BBA: 00->33dB
Bit	Symbol	Description											
[15:4]	Internal use	Must not be modified											
[3:2]	RX_HPCF	Lower Corner Frequency: 00->45kHz 01->85kHz 10->150kHz 11->250kHz											
[1:0]	RX_GAIN	Gain Adjustment BBA: 00->33dB											

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Claim Language	Infringement Evidence
	publicly available <i>PN5180 A0xx/C1/C2 High-Performance Multi-Protocol Full NFC Frontend, Supporting all NFC Forum Modes, Rev. 3.6</i> (May 7, 2018) (“<i>PN5180 Data Sheet</i>”). As a highly integrated high performance full NFC Forum-compliant frontend IC for contactless communication at 13.56 MHz, this frontend IC utilizes an outstanding modulation and demodulation concept completely integrated for different kinds of contactless communication methods and protocols. <i>PN5180 Data Sheet</i> at 2. The PN5180 frontend IC supports the following RF operating modes: • Reader/Writer mode supporting ISO/IEC 14443 type A up to 848 kBit/s • Reader/Writer communication mode for MIFARE Classic contactless IC • Reader/Writer mode supporting ISO/IEC 14443 type B up to 848 kBit/s • Reader/Writer mode supporting JIS X 6319-4 (comparable with FeliCa scheme) • Supports reading of all NFC tag types (type 1, type 2, type 3, type 4A and type 4B) • Reader/Writer mode supporting ISO/IEC 15693 • Reader/Writer mode supporting ISO/IEC 18000-3 Mode 3 • ISO/IEC 18092 (NFC-IP1) • ISO/IEC 21481 (NFC-IP-2) • ISO/IEC 14443 type A Card emulation up to 848 kBit/s <i>PN5180 Data Sheet</i> at 2. The <i>PN5180 Data Sheet</i> provides additional detail regarding amplifier circuitry within NXP NFC chips like the PN5180, PN7150, and SN110U. Like the PN7150, the PN5180 also has Rx pins for receiving an antenna signal along an Rx path. The antenna signal is received at a Receiver block, shown below.

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	Figure 2. PN5180 Block diagram <i>PN5180 Data Sheet at 11 (emphasis added).</i>

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	The Receiver block includes baseband amplifiers (BBAs) to adjust the analog gain (i.e., the RX_GAIN) of the I-channel and Q-channel. The Receiver block diagram is shown below. Figure 38. PN5180 Receiver Block diagram <i>PN5180 Data Sheet</i> at 82 (emphasis added). On information and belief, the baseband amplifier of the PN7150, as well as the amplifier circuitry of the SN110U, is substantially similar in relevant part to amplifier circuitry described with respect to the PN5180. iCashe has not received any discovery regarding the operation of the SN110U chip. iCashe reserves the right to modify its contentions based on non-public information learned through discovery, including, <i>inter alia</i>, any differences in functionality across the SN110U, the PN7150, and the PN5180. As further described with respect to the PN5180, the amplifier circuitry provides the amplified signal to the smartcard controller via I-channel and Q-channel analog-to-digital converters (ADCs).

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	Reader Mode Receiver In Reader Mode, the response of the PICC device is coupled from the PCB antenna to the differential input RXP/RXN. The Reader Mode Receiver extracts this signal by first removing the carrier in passive mixers (direct conversion for I and Q), then filtering and amplifying the baseband signal, and finally converting to digital values with 2 separate ADCs for I and Q channel. Both the I and Q channels have a differential structure which improves the signal quality. The I/Q-Mixer mixes the differential input RF-signal down to the baseband. The mixer has a band with of 2 MHz. The down mixed differential RX input signals are passed to the BBA and band-pass filtered. In order to consider all the various protocols (Type A/B, FeliCa), the high-pass cut-off frequency of BBA can be configured between 45 kHz and 250 kHz in 4 different steps. The low-pass cut-off frequency is above 2 MHz. This band-passed signal is then further amplified with a gain factor which is configurable between 30 dB and 60 dB. The baseband amplifier (BBA)/ADC I- and Q- channel can be enabled separately. This is required for ADC-based CardMode functionality as only the I-channel is used in this case. <i>PN5180 Data Sheet at 81.</i> The PN7150 also contains an ADC, which receives the amplified signal and provides it to the smartcard controller, as shown below.

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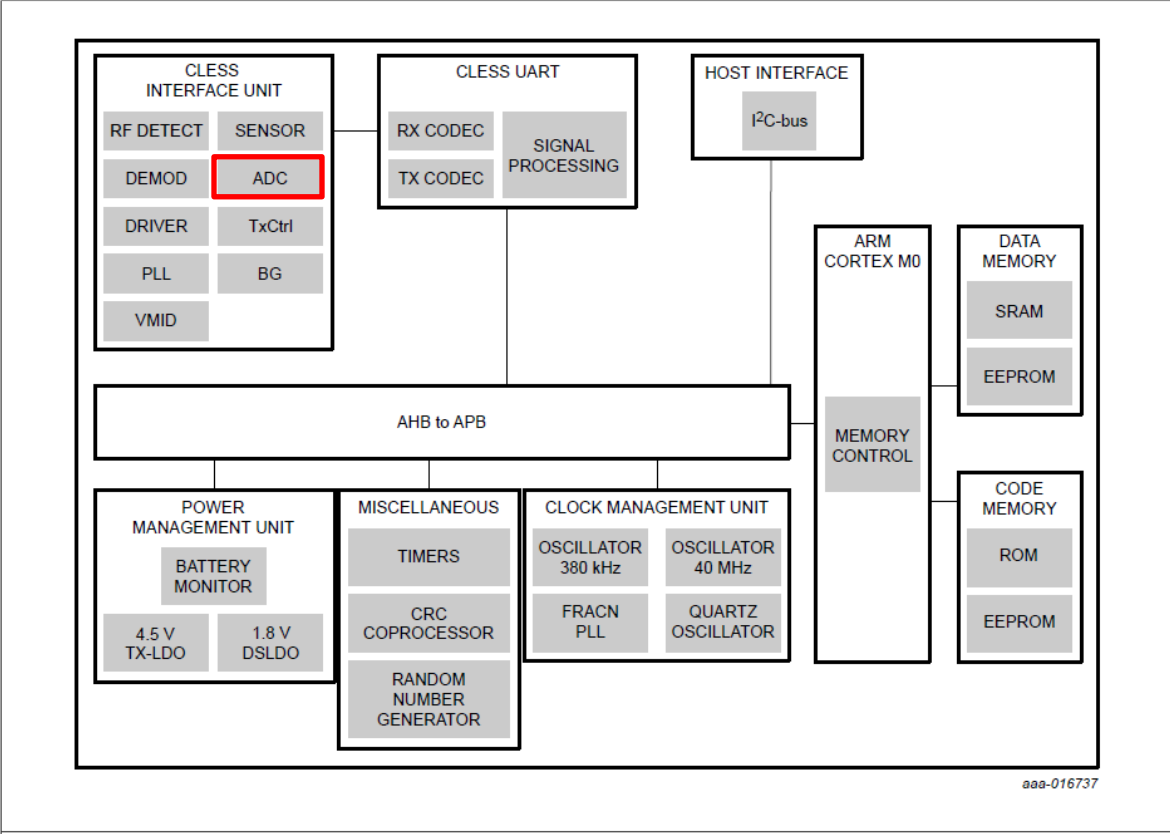
Claim Language	Infringement Evidence
	 aaa-016737 Fig 3. PN7150 block diagram <i>PN7150 Data Sheet</i> at 10 (emphasis added). On information and belief, the SN110U chip contains functional blocks and circuitry within the embedded NFC controller that correspond to the amplifier(s) and ADC(s) shown and described

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	with respect to the PN5180 and PN7150. iCashe reserves the right to modify its contentions based on non-public information learned through discovery, including, <i>inter alia</i>, any differences in functionality across the SN110U, the PN7150, and the PN5180. The amplifier is arranged between the antenna and the smartcard controller, so as to amplify signals received at the antenna and provide an amplified signal to the smartcard controller.

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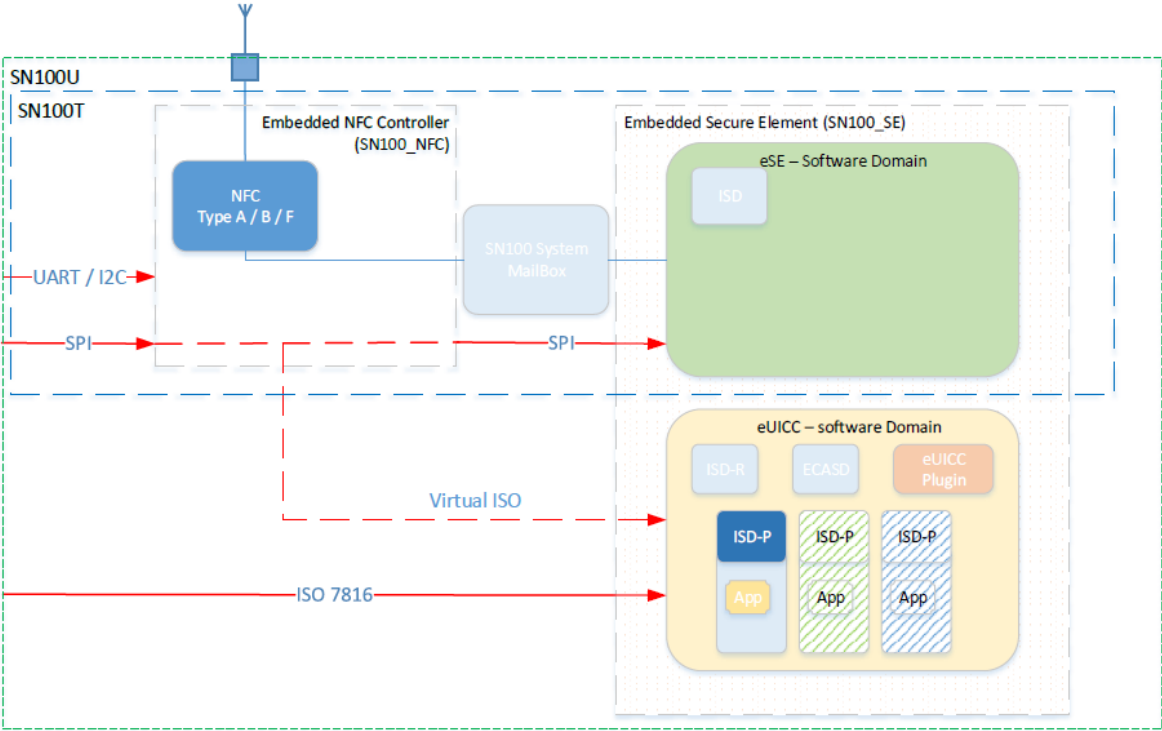
Claim Language	Infringement Evidence
	 Fig. 1.2: SN100x Product Configurations <i>NXP SN100T Secure Element Specification at 5.</i> The amplifier is located on the NXP SN110U chip, which is coupled to the power source, i.e., the Galaxy S21 battery shown below, to receive power.

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	Teardown image from Samsung Galaxy S21 5G smartphone (emphasis added). Additionally, testing demonstrates that the NFC transmission from the Galaxy S21 does not depend on coupling with the NFC reader, indicating that the SN110U chip is powered. Its power source is the battery of the Galaxy S21.

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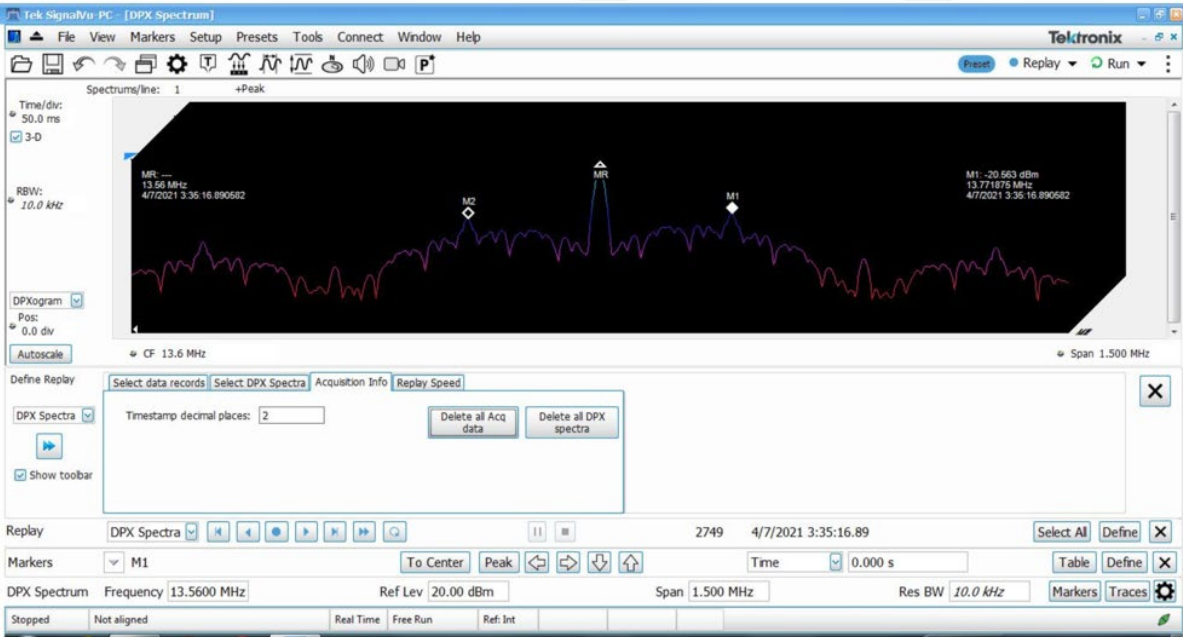
Claim Language	Infringement Evidence
	As described with respect to limitation 1.a, testing indicates that the Galaxy S21 utilizes ALM functionality. As shown below in Figure 1, which displays the RF signal analysis during an ongoing transaction with an NFC reader, the Galaxy S21 emits two actively modulated sideband spikes plus the carrier frequency (13.56 MHz). This confirms the presence of the load modulation circuitry discussed above.  <i>Figure 1: Samsung Galaxy S21 5G with ongoing transaction showing actively modulated spikes of power</i> Figure 2 below displays the RF signal analysis of the Galaxy S21 with the Samsung Pay application enabled but with no NFC reader present. Like Figure 1, the figure shows the frequency spectrum

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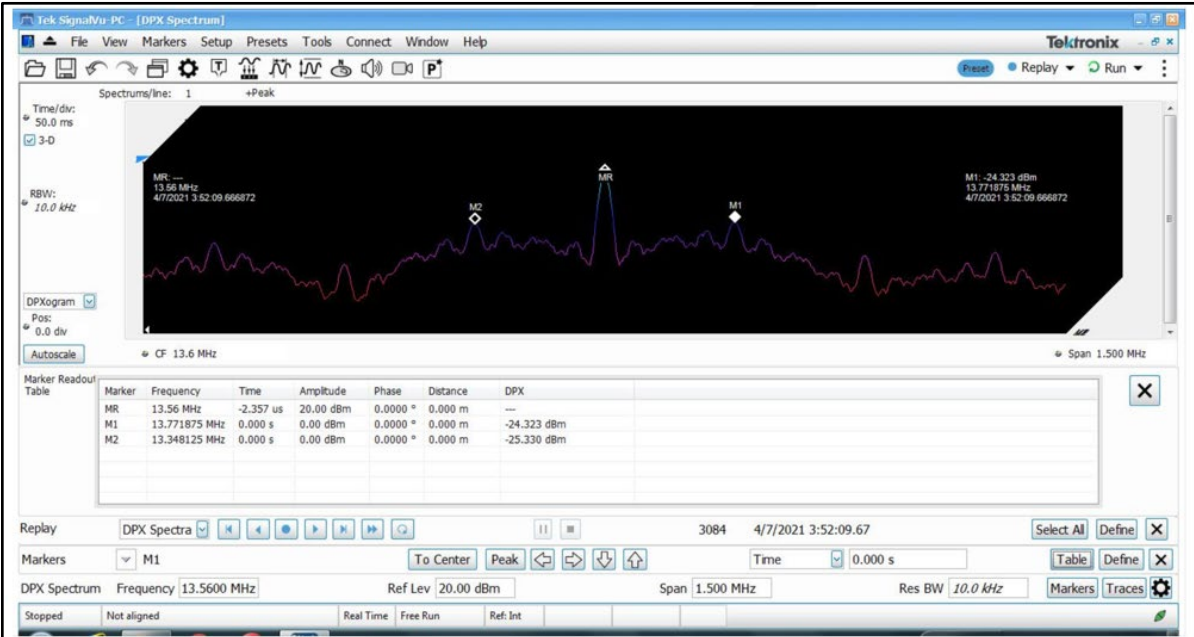
Claim Language	Infringement Evidence
	enabled by the smartcard circuitry consists of two actively modulated spikes (at $f_c/64$) plus the carrier frequency (13.56 MHz). Thus, Figure 2 demonstrates that the NFC transmission from the Galaxy S21 does not depend on coupling with (or power from) the NFC reader. Figure 2 demonstrates that the SN110U chip is powered by the Galaxy S21 battery.  <i>Figure 2: Samsung Galaxy S21 5G near Square reader showing actively modulated spikes of power</i>
[1.d] the at least one active circuit further includes a transmit circuit coupled between the smartcard	The at least one active circuit of the Galaxy S21 further includes a transmit circuit coupled between the smartcard controller and the antenna that in operation forms a signal that mimics the at least one frequency sideband and wherein the signal drives the antenna.

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controller and the antenna that in operation forms a signal that mimics the at least one frequency sideband and wherein the signal drives the antenna.	As discussed above with respect to limitation 1.a, the SN110U chip of the Galaxy S21 has a functional block for actively transmitting modulated data to the NFC reader for conducting the transaction. As discussed above as to limitation 1.a, the NXP SN110U chip and surrounding circuitry operates in substantially the same manner in relevant part as the PN7150 and its surrounding circuitry. Specifically, on information and belief, the NXP SN110U chip and surrounding circuitry include a transmit circuit that operates in substantially the same manner as the PN7150 as described in the <i>PN7150 Antenna Design and Matching Guide</i>. As shown below, the <i>PN7150 Antenna Design and Matching Guide</i> depicts the transmit (Tx) path between the Tx pads of the NFC chip and the antenna coil and matching circuitry. On information and belief and for the reasons discussed above, the NXP SN110U chip and surrounding circuitry, matching circuitry, and antenna coil in the Galaxy S21 are configured in substantially the same manner in relevant part.

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	Fig 21. Rx path <i>PN7150 Antenna Design and Matching Guide at 22.</i> The signal drives the antenna. As discussed above, on information and belief, the SN110U chip contains circuitry and functional blocks within the embedded NFC controller that correspond to certain circuitry and functional blocks within the CIU of the PN7150. The CIU includes a driver block/circuits and transmitter control block, TxCtrl, coupled to drive the antenna, as shown below. The transmit circuitry in the CIU is coupled between the smartcard controller and the antenna.

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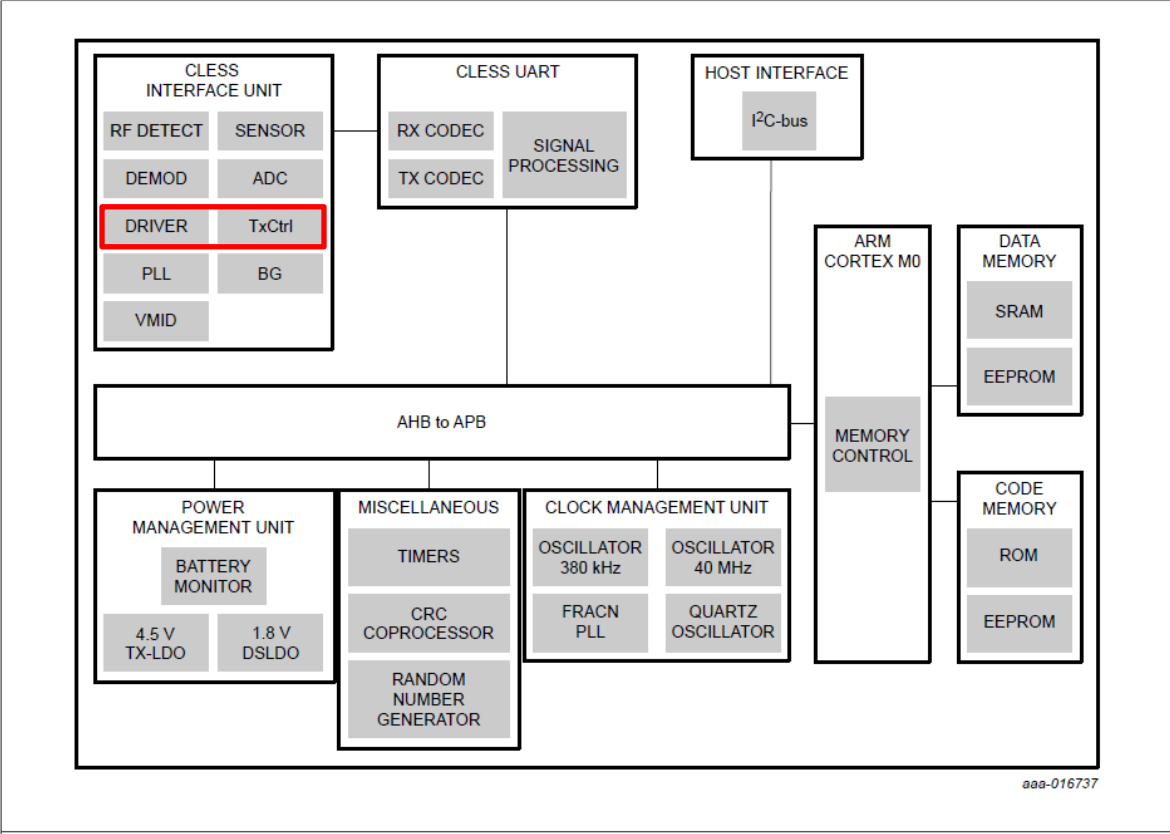
Claim Language	Infringement Evidence
	 aaa-016737 Fig 3. PN7150 block diagram <i>PN7150 Data Sheet</i> at 10 (emphasis added). The <i>PN5180 Data Sheet</i> provides additional detail regarding transmitter circuitry within NXP NFC chips like the PN5180, PN7150, and SN110U. Like the PN7150, the PN5180 also has Tx pins for

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	driving an antenna signal along a Tx path. iCashe has not received any discovery regarding the operation of the SN110U chip. iCashe reserves the right to modify its contentions based on non-public information learned through discovery, including, <i>inter alia</i>, any differences in functionality across the SN110U, the PN7150, and the PN5180. Figure 2. PN5180 Block diagram

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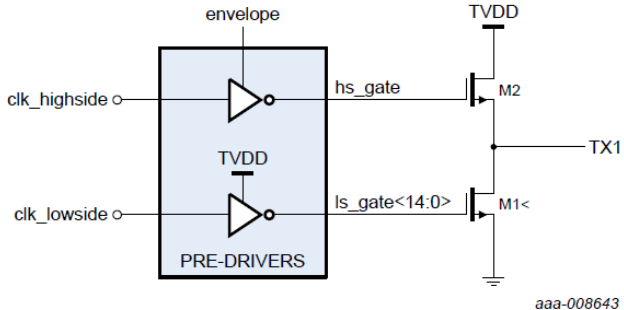
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	<i>PN5180 Data Sheet at 11 (emphasis added).</i> Transmitter TX The transmitter is able to drive an antenna circuit connected to outputs TX1 and TX2 with a 13.56 MHz carrier signal. The signal delivered on pins TX1 and pin TX2 is the 13.56 MHz carrier modulated by an envelope signal for energy and data transmission. It can be used to drive an antenna directly, using a few passive components for matching and filtering. For a differential antenna configuration, either TX1 or TX2 can be configured to put out an inverted clock. 100 % modulation and several levels of amplitude modulation on the carrier can be performed to support 13.56 MHz carrier-based RF-reader/writer protocols as defined by standards ISO/IEC14443 type A and B, FeliCa and ISO/IEC 18092.  Figure 29. PN5180 Output driver <i>PN5180 Data Sheet at 70.</i>

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	On information and belief, the SN110U chip contains functional blocks and circuitry within the embedded NFC controller that correspond to the active load modulation circuitry (i.e., the load modulation circuit) shown and described with respect to the PN7150 and PN5180.¹ Testing of the Samsung Galaxy S21 5G smartphone indicates that the transmit circuit of the NXP SN110U chip forms a signal that mimics the at least one frequency sideband and drives the antenna. Figure 2 below displays an RF signal analysis of the Samsung Galaxy S21 5G smartphone with the Samsung Pay application enabled but with no NFC reader present. The figure shows the frequency spectrum enabled by the transmit circuit consists of two actively modulated sidebands plus the carrier frequency.

¹ Again, iCashe has not received any discovery regarding the operation of the SN110U chip and thus uses the PN5180 throughout this chart for the limited purpose of illustrating the likely presence of similar, corresponding circuitry in the SN110U chip. On information and belief, similar circuitry (and additional circuitry) is present in the SN110U chip. To the extent the cited circuitry of the PN5180 includes passive circuitry components or to the extent the PN5180 does not support active load modulation in certain operating modes, iCashe does not contend that such passive circuitry, alone, satisfies the relevant limitations of the asserted claims.

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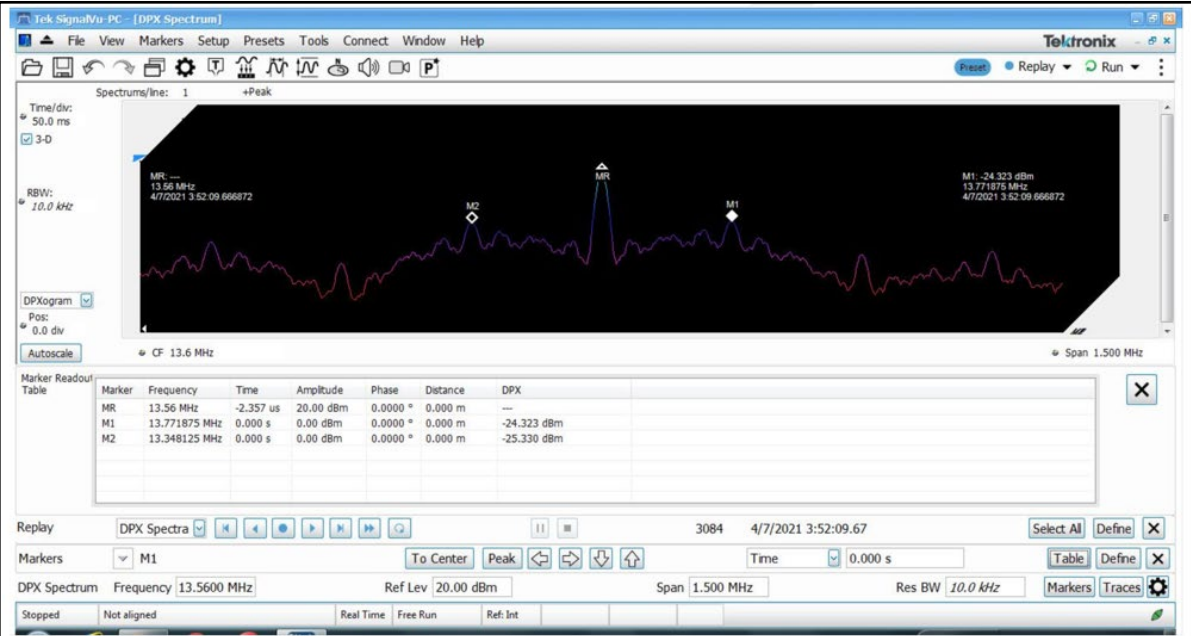
Claim Language	Infringement Evidence
	 <i>Figure 2: Samsung Galaxy S21 5G near Square reader showing actively modulated spikes of power</i>
2. The mobile device of claim 1 wherein the antenna comprises an inductive element too small to draw enough power sufficient to operate the smartcard controller from the interrogating radio frequency (RF) field.	<i>See claim 1, above.</i> The antenna of Galaxy S21 comprises an inductive element too small to draw enough power sufficient to operate the smartcard controller from the interrogating radio frequency (RF) field. As described above with respect to claim 1, the SN110U contains a smartcard controller used to complete NFC operations, such as NFC payment transactions via Samsung Pay.

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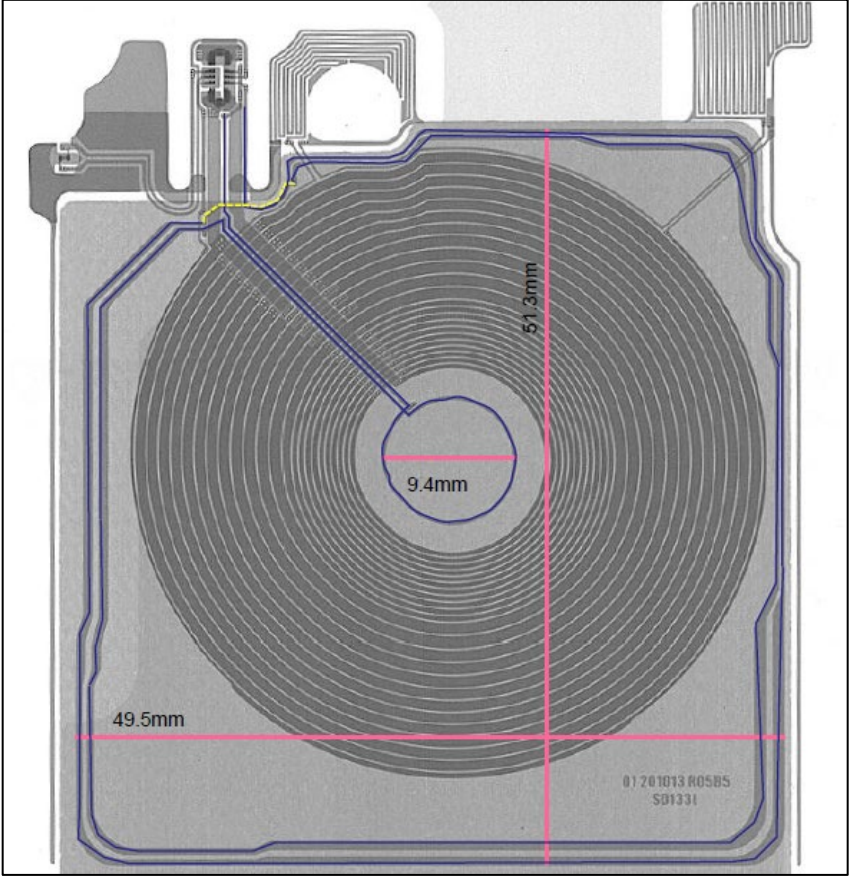
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	As additionally described with respect to claim 1, the Galaxy S21 has an antenna used to transmit and receive NFC information and transaction sequences. The antenna is tuned to operate at 13.56 MHz. The antenna is shown below. 

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	Teardown image from Samsung Galaxy S21 5G smartphone (emphasis added). As shown above, the antenna of the Galaxy S21 has two loops and an approximate length of between 400 mm and 500 mm. On information and belief, the antenna of the Galaxy S21 is too small to draw enough power sufficient to operate the smartcard controller from an interrogating radio frequency (RF) field. Further, testing indicates that the NFC transmission from the Samsung Galaxy S21 does not depend on power derived from an RF field. Testing indicates that the Galaxy S21 utilizes ALM functionality. As shown below in Figure 1, which displays the RF signal analysis during an ongoing transaction with an NFC reader, the Galaxy S21 emits two actively modulated sideband spikes plus the carrier frequency (13.56 MHz). This confirms the presence of the load modulation circuitry discussed above.

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	<i>Figure 1: Samsung Galaxy S21 5G with ongoing transaction showing actively modulated spikes of power</i> Figure 2 below displays the RF signal analysis of the Galaxy S21 with the Samsung Pay application enabled but with no NFC reader present. Like Figure 1, the figure shows the frequency spectrum enabled by the smartcard circuitry consists of two actively modulated spikes (at $f_c/64$) plus the carrier frequency (13.56 MHz). Thus, Figure 2 demonstrates that the NFC transmission from the Galaxy S21 does not depend on coupling with (or power from) the NFC reader.

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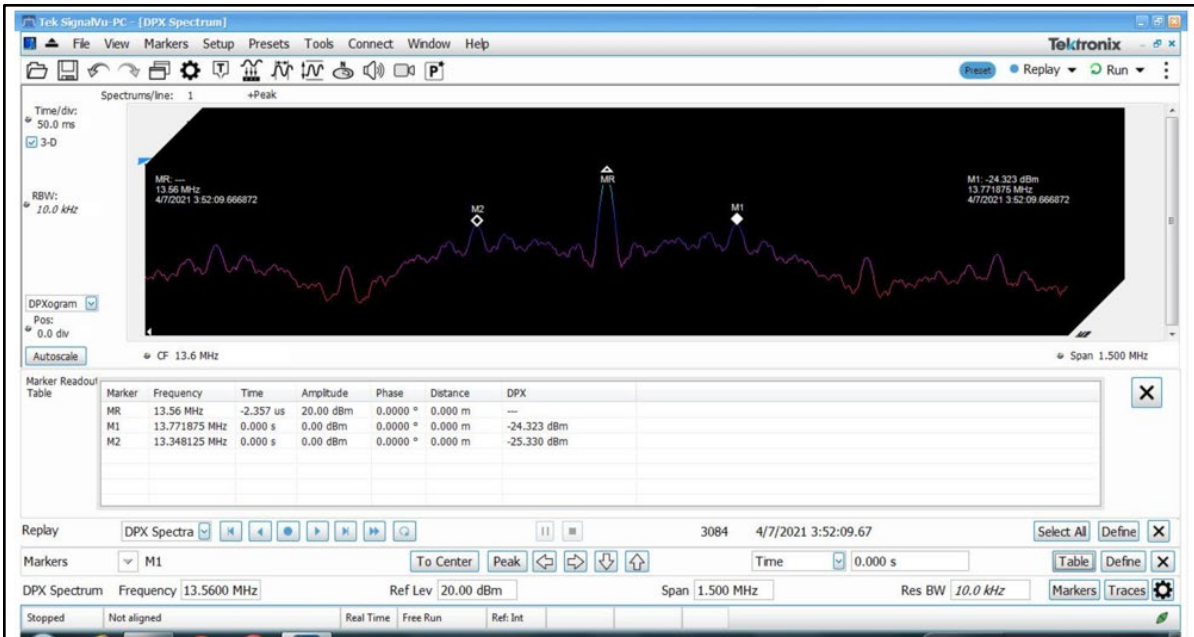
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	 The screenshot displays the Tektronix SignalVu-PC interface. The main plot area shows a DPX Spectrum with three marked peaks: MR at 13.56 MHz, M1 at 13.771875 MHz, and M2 at 13.348125 MHz. The plot includes a grid and various measurement parameters. Below the plot is a "Marker Readout Table" with the following data: <table><thead><tr><th>Marker</th><th>Frequency</th><th>Time</th><th>Amplitude</th><th>Phase</th><th>Distance</th><th>DPX</th></tr></thead><tbody><tr><td>MR</td><td>13.56 MHz</td><td>-2.357 us</td><td>20.00 dBm</td><td>0.0000 °</td><td>0.000 m</td><td>---</td></tr><tr><td>M1</td><td>13.771875 MHz</td><td>0.000 s</td><td>0.00 dBm</td><td>0.0000 °</td><td>0.000 m</td><td>-24.323 dBm</td></tr><tr><td>M2</td><td>13.348125 MHz</td><td>0.000 s</td><td>0.00 dBm</td><td>0.0000 °</td><td>0.000 m</td><td>-25.330 dBm</td></tr></tbody></table> Below the table, there are controls for "Replay", "Markers", and "DPX Spectrum". The "Markers" section shows "M1" selected, and the "DPX Spectrum" section shows "Frequency 13.5600 MHz". The "Replay" section shows "DPX Spectra" and "Time 0.000 s". The "Markers" section shows "M1" and "To Center" button. The "DPX Spectrum" section shows "Frequency 13.5600 MHz", "Ref Lev 20.00 dBm", "Span 1.500 MHz", and "Res BW 10.0 kHz". The "Replay" section shows "Stopped", "Not aligned", "Real Time", "Free Run", and "Ref: Int". <i>Figure 2: Samsung Galaxy S21 5G near Square reader showing actively modulated spikes of power</i>	Marker	Frequency	Time	Amplitude	Phase	Distance	DPX	MR	13.56 MHz	-2.357 us	20.00 dBm	0.0000 °	0.000 m	---	M1	13.771875 MHz	0.000 s	0.00 dBm	0.0000 °	0.000 m	-24.323 dBm	M2	13.348125 MHz	0.000 s	0.00 dBm	0.0000 °	0.000 m	-25.330 dBm
Marker	Frequency	Time	Amplitude	Phase	Distance	DPX																							
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3. The mobile device of claim 1 wherein the smartcard controller is coupled to be powered by the mobile device.	See claim 1, above. The smartcard controller of the Galaxy S21 is coupled to be powered by the mobile device. The Galaxy S21 is a mobile device. The Galaxy S21 battery, shown below, is a power source.																												

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Claim Language	Infringement Evidence
	Teardown image from Samsung Galaxy S21 5G smartphone (emphasis added). As discussed above with respect to claim 1, the SN110U contains a smartcard controller used to complete NFC operations, such as NFC payment transactions via Samsung Pay. The SN110U is coupled to the battery of the Galaxy S21 to receive power.

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	Further, testing indicates that the NFC transmission from the Samsung Galaxy S21 is powered by the device itself. Testing indicates that the Galaxy S21 utilizes ALM functionality. As shown below in Figure 1, which displays the RF signal analysis during an ongoing transaction with an NFC reader, the Galaxy S21 emits two actively modulated sideband spikes plus the carrier frequency (13.56 MHz). This confirms the presence of the load modulation circuitry discussed above. <i>Figure 1: Samsung Galaxy S21 5G with ongoing transaction showing actively modulated spikes of power</i>

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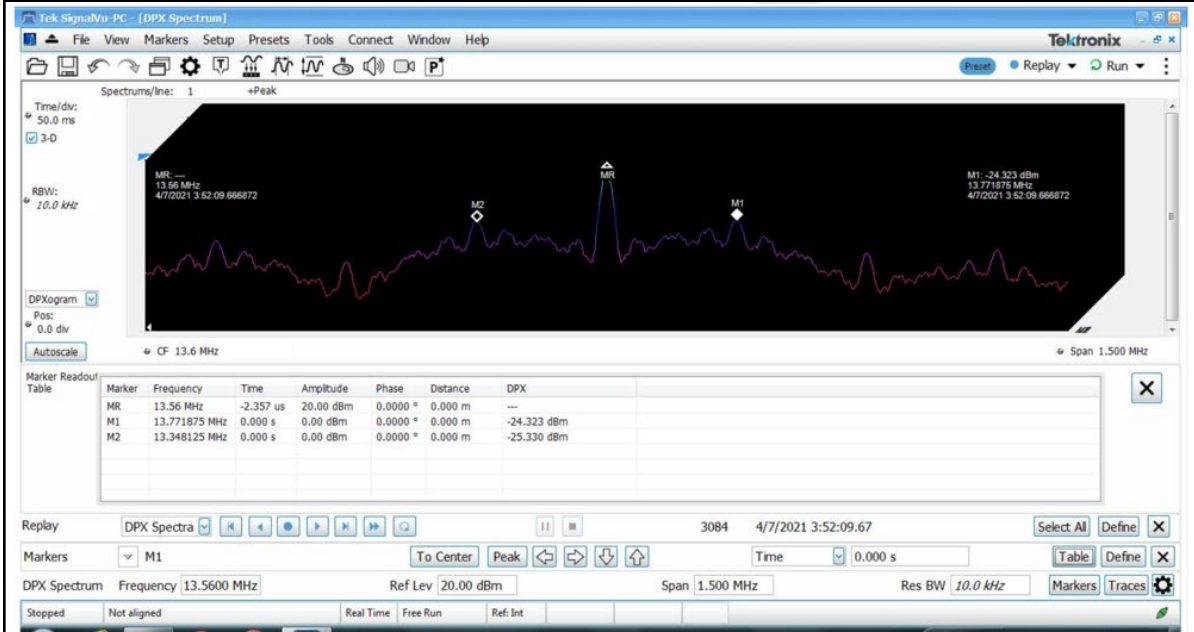
Claim Language	Infringement Evidence																												
	Figure 2 below displays the RF signal analysis of the Galaxy S21 with the Samsung Pay application enabled but with no NFC reader present. Like Figure 1, the figure shows the frequency spectrum enabled by the smartcard circuitry consists of two actively modulated spikes (at $f_c/64$) plus the carrier frequency (13.56 MHz). Thus, Figure 2 demonstrates that the NFC transmission from the Galaxy S21 does not depend on coupling with (or power from) the NFC reader and is therefore the product of an active load modulation circuit. Thus, Figure 2 demonstrates that the SN110U, including the smartcard controller, is powered by the Galaxy S21 battery. <table data-bbox="806 997 1803 1127"><thead><tr><th>Marker</th><th>Frequency</th><th>Time</th><th>Amplitude</th><th>Phase</th><th>Distance</th><th>DPX</th></tr></thead><tbody><tr><td>MR</td><td>13.56 MHz</td><td>-2.357 us</td><td>20.00 dBm</td><td>0.0000 °</td><td>0.000 m</td><td>---</td></tr><tr><td>M1</td><td>13.771875 MHz</td><td>0.000 s</td><td>0.00 dBm</td><td>0.0000 °</td><td>0.000 m</td><td>-24.323 dBm</td></tr><tr><td>M2</td><td>13.348125 MHz</td><td>0.000 s</td><td>0.00 dBm</td><td>0.0000 °</td><td>0.000 m</td><td>-25.330 dBm</td></tr></tbody></table> Figure 2: Samsung Galaxy S21 5G near Square reader showing actively modulated spikes of power	Marker	Frequency	Time	Amplitude	Phase	Distance	DPX	MR	13.56 MHz	-2.357 us	20.00 dBm	0.0000 °	0.000 m	---	M1	13.771875 MHz	0.000 s	0.00 dBm	0.0000 °	0.000 m	-24.323 dBm	M2	13.348125 MHz	0.000 s	0.00 dBm	0.0000 °	0.000 m	-25.330 dBm
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4. The mobile device of claim 1 wherein the mobile device comprises a mobile phone.	See claim 1, above. The Galaxy S21 is a mobile phone.  https://www.samsung.com/us/smartphones/galaxy-s-series/certified-re-newed-store/buy/?modelCode=SM5G991UZAAXAA.

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[5.a] a smartcard controller that includes load modulation circuitry for half duplex communication by creating at least one frequency sideband about a carrier frequency of an interrogating radio frequency (RF) field;	The Galaxy S21 contains a smartcard controller that includes load modulation circuitry for half duplex communication by creating at least one frequency sideband about a carrier frequency of an interrogating radio frequency (RF) field. <i>See</i> limitation 1.a of claim 1, above.
[5.b] an antenna tuned to operate at 13.56 MHz;	The Galaxy S21 contains an antenna tuned to operate at 13.56 MHz. <i>See</i> limitation 1.b of claim 1, above.
[5.c] an amplifier coupled between the smartcard controller and the antenna, wherein the amplifier is coupled to be powered by the mobile device, and wherein the amplifier is coupled to amplify a signal received from the antenna and to provide an amplified signal to the smartcard controller; and	The Galaxy S21 includes an amplifier coupled between the smartcard controller and the antenna, wherein the amplifier is coupled to be powered by the mobile device, and wherein the amplifier is coupled to amplify a signal received from the antenna and to provide an amplified signal to the smartcard controller. <i>See</i> limitation 1.c of claim 1, above. The amplifier is coupled between the antenna and the smartcard controller, so as to amplify signals received at the antenna and provide an amplified signal to the smartcard controller.

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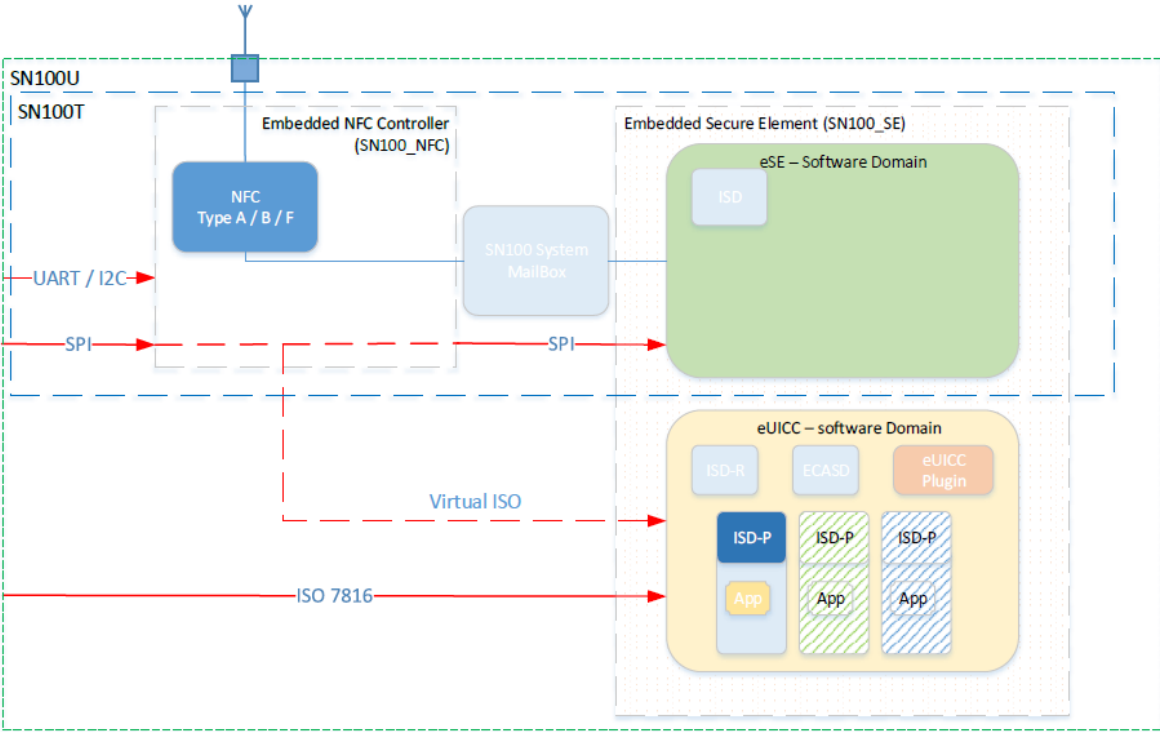
Claim Language	Infringement Evidence
	 Fig. 1.2: SN100x Product Configurations <i>NXP SN100T Secure Element Specification at 5.</i>

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[5.d] a transmit circuit coupled between the smartcard controller and the antenna that in operation forms a signal that mimics the at least one frequency sideband and wherein the signal drives the antenna.	The Galaxy S21 includes a transmit circuit coupled between the smartcard controller and the antenna that in operation forms a signal that mimics the at least one frequency sideband and wherein the signal drives the antenna. <i>See</i> limitation 1.d of claim 1, above.
6. The mobile device of claim 5 wherein the antenna comprises an inductive element too small to draw enough power sufficient to operate the smartcard controller from the interrogating radio frequency (RF) field.	<i>See</i> claim 5, above. The antenna of Galaxy S21 comprises an inductive element too small to draw enough power sufficient to operate the smartcard controller from the interrogating radio frequency (RF) field. <i>See</i> claim 2, above.
7. The mobile device of claim 5 wherein the smartcard controller is coupled to be powered by the mobile device.	<i>See</i> claim 5, above. The smartcard controller of the Galaxy S21 is coupled to be powered by the mobile device. <i>See</i> claim 3, above.
8. The mobile device of claim 5 wherein the transmit circuit comprises a load modulation circuit.	<i>See</i> claim 5, above. The transmit circuit of the Galaxy S21 comprises a load modulation circuit. As described above with respect to claim 5, the SN110U chip of the Galaxy S21 contains a functional block for actively transmitting modulated data to the NFC reader for conducting the transaction.

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	As shown below with respect to the PN7150 chip, the NXP SN110U chip and surrounding circuitry was designed for load modulation, and particularly <i>active</i> load modulation (ALM). 2.2 Single loop antenna dedicated for Active Load Modulation PN7150 was designed for Active Load Modulation (ALM) concept. ALM provides high performances and significant margins to NFC standard criteria. It also allows the use of smaller antenna. <i>PN7150 Antenna Design and Matching Guide</i> at 4.

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iCash's Infringement Contentions for U.S. Patent No. 9,483,722
Samsung Galaxy S21 5G Smartphone

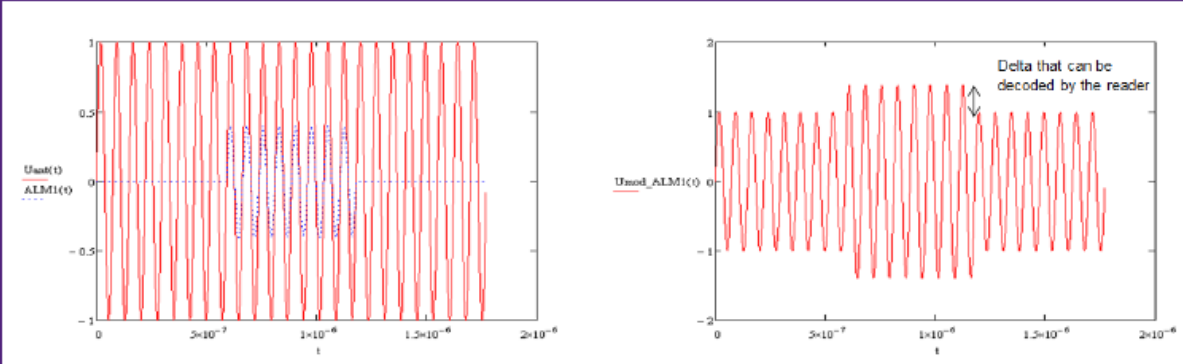
Claim Language	Infringement Evidence
	ALM Mode (SINGLE):  Fig 2. ALM mode SINGLE concept On the left graph the red 13.56MHz signal shows the voltage at the NFC antenna which is induced by the reader field, the blue curve shows the modulation pattern. This modulation pattern is generated by actively driving 13.56MHz with TX1 or TX2 while the other TX pin (TX2 or TX1) is kept silent. On the right we can see the modulated reader field. <i>PN7150 Antenna Design and Matching Guide at 5.</i>

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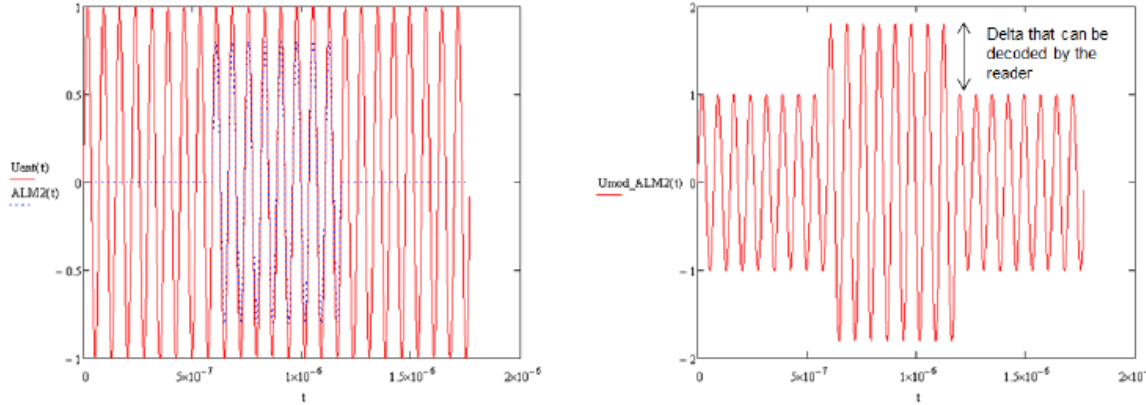
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	ALM Mode (DUAL):  Fig 3. ALM mode DUAL concept In this mode the modulation pattern is generated by actively driving 13.56MHz with TX1 and TX2. The modulation depth observed is twice the modulation depth of mode 1. <i>PN7150 Antenna Design and Matching Guide at 5.</i> As shown below, the PN7150 circuitry produces a modulation signal that is driven to the antenna via TX1 and/or TX2.

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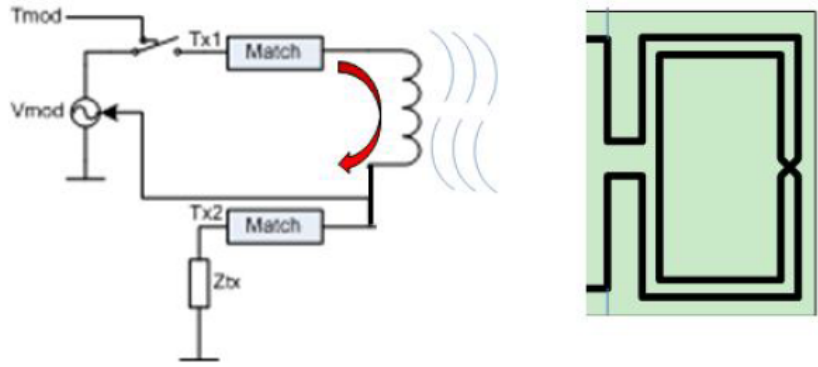
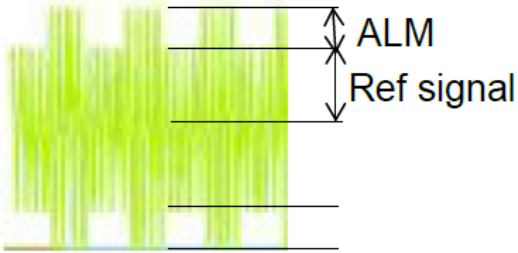
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	 Fig 4. Single Loop Antenna concept (mode 1 illustration) <i>PN7150 Antenna Design and Matching Guide at 6.</i>  Fig 5. Load modulation illustration

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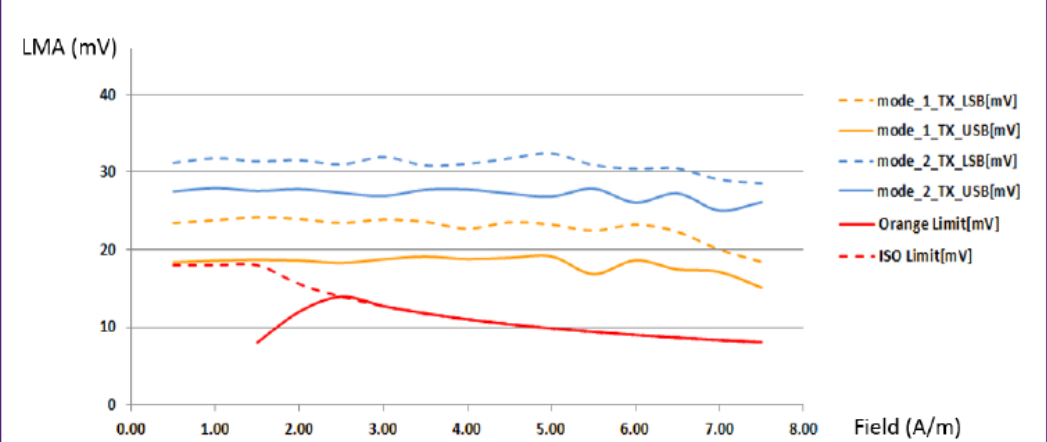
Claim Language	Infringement Evidence
	<i>PN7150 Antenna Design and Matching Guide at 6.</i> The ALM produces upper and lower sidebands with respect to the 13.56 MHz signal. On Fig 6 below LMA result examples are shown for Upper Side Band and Lower Side Band in the 2 modes available for PN7150 (respectively mode 1, mode 2). In order to minimize the phase error, it is necessary to limit the level of the load modulation amplitude (LMA) and to adjust the balance of the Side Bands when the field strength is low.  Fig 6. Example Side Bands ISO results for modes 1, 2 <i>PN7150 Antenna Design and Matching Guide at 7.</i>

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	As additionally discussed with respect to claim 5, the EMVCo certification of the Galaxy S21 as an EMV Contactless Level 1 Mobile Product indicates that the Galaxy S21 has an active load modulation circuit compliant with the requirements of the EMV Level 1 Specifications for Payment Systems. The EMVCo Certification further indicates that the Galaxy S21 supports Type A and Type B communications under the ISO/IEC 14443 standard. Testing of the Galaxy S21 confirms that the SN110U chip of the Galaxy S21 employs active load modulation. As shown below in Figure 1, which displays the RF signal analysis during an ongoing transaction with an NFC reader, the Galaxy S21 emits two actively modulated sideband spikes plus the carrier frequency (13.56 MHz). This confirms the presence of the load modulation circuitry discussed above.

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	<i>Figure 1: Samsung Galaxy S21 5G with ongoing transaction showing actively modulated spikes of power</i> Figure 2 below displays the RF signal analysis of the Galaxy S21 with the Samsung Pay application enabled but with no NFC reader present. Like Figure 1, the figure shows the frequency spectrum enabled by the driver circuit consists of two actively modulated spikes (at $f_c/64$) plus the carrier frequency (13.56 MHz). Thus, Figure 2 demonstrates that the NFC transmission from the Galaxy S21 does not depend on coupling with the NFC reader and is therefore the product of an <i>active</i> load modulation circuit.

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	 The screenshot displays the Tektronix SignalVu-PC interface. The main plot area shows a DPX Spectrum with three marked peaks: MR at 13.56 MHz, M1 at 13.771875 MHz, and M2 at 13.348125 MHz. The plot includes a time axis (50.0 ms) and a frequency axis (10.0 kHz). Below the plot is a 'Marker Readout Table' with the following data: <table><thead><tr><th>Marker</th><th>Frequency</th><th>Time</th><th>Amplitude</th><th>Phase</th><th>Distance</th><th>DPX</th></tr></thead><tbody><tr><td>MR</td><td>13.56 MHz</td><td>-2.357 us</td><td>20.00 dBm</td><td>0.0000 °</td><td>0.000 m</td><td>---</td></tr><tr><td>M1</td><td>13.771875 MHz</td><td>0.000 s</td><td>0.00 dBm</td><td>0.0000 °</td><td>0.000 m</td><td>-24.323 dBm</td></tr><tr><td>M2</td><td>13.348125 MHz</td><td>0.000 s</td><td>0.00 dBm</td><td>0.0000 °</td><td>0.000 m</td><td>-25.330 dBm</td></tr></tbody></table> Below the table, the 'Replay' section shows 'DPX Spectra' and 'Markers' set to M1. The 'DPX Spectrum' section shows 'Frequency 13.5600 MHz', 'Ref Lev 20.00 dBm', 'Span 1.500 MHz', and 'Res BW 10.0 kHz'. The status bar at the bottom indicates 'Stopped', 'Not aligned', 'Real Time', 'Free Run', and 'Ref: Int'. <i>Figure 2: Samsung Galaxy S21 5G near Square reader showing actively modulated spikes of power</i>	Marker	Frequency	Time	Amplitude	Phase	Distance	DPX	MR	13.56 MHz	-2.357 us	20.00 dBm	0.0000 °	0.000 m	---	M1	13.771875 MHz	0.000 s	0.00 dBm	0.0000 °	0.000 m	-24.323 dBm	M2	13.348125 MHz	0.000 s	0.00 dBm	0.0000 °	0.000 m	-25.330 dBm
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9. The mobile device of claim 5 wherein the transmit circuit comprises an active transmit driver.	See claim 5, above. The transmit circuit of the Galaxy S21 comprises an active transmit driver. As described above with respect to claim 5, the SN110U chip of the Galaxy S21 contains a functional block for actively transmitting modulated data to the NFC reader for conducting the transaction.																												

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	As shown below with respect to the PN7150 chip, the NXP SN110U chip and surrounding circuitry was designed for load modulation, and particularly <i>active</i> load modulation (ALM). 2.2 Single loop antenna dedicated for Active Load Modulation PN7150 was designed for Active Load Modulation (ALM) concept. ALM provides high performances and significant margins to NFC standard criteria. It also allows the use of smaller antenna. <i>PN7150 Antenna Design and Matching Guide</i> at 4.

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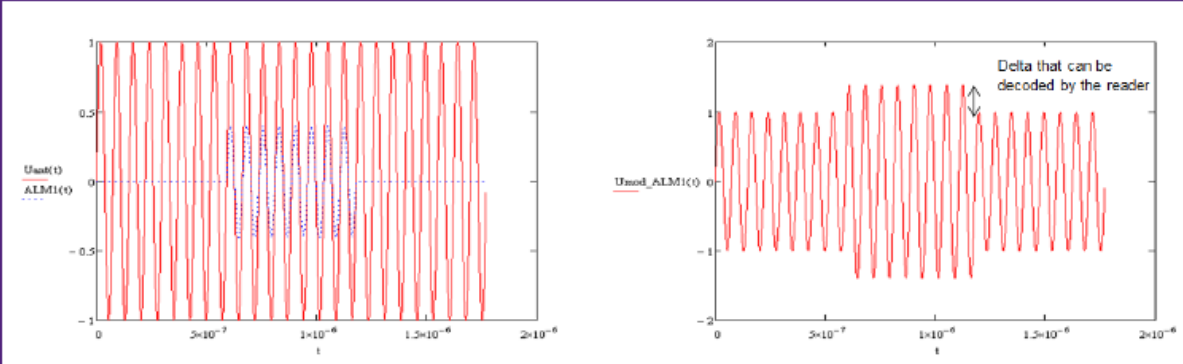
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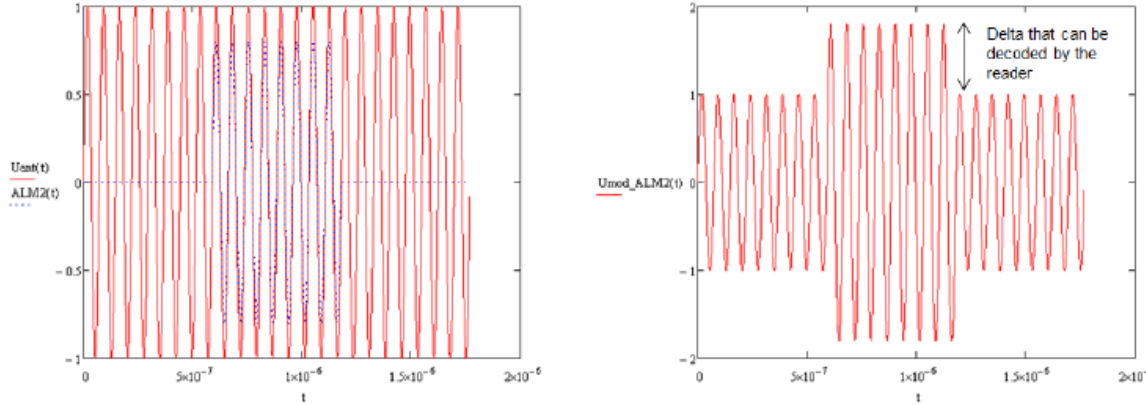
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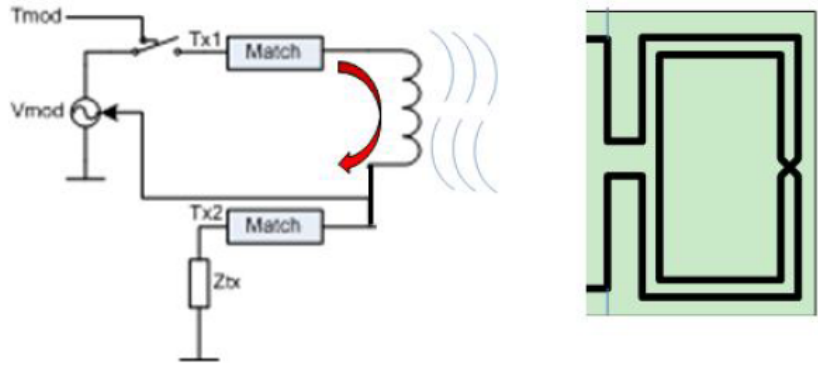
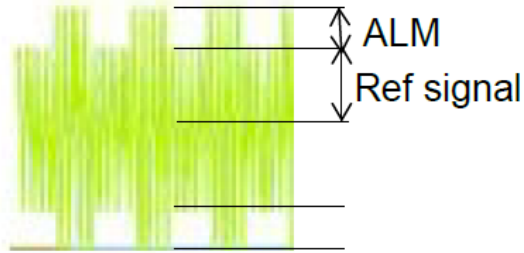
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	 The diagram on the left shows a circuit for a single loop antenna. A voltage source V_{mod} is connected to a switch T_{mod}. The switch can connect to $Tx1$ or $Tx2$. $Tx1$ is connected to a 'Match' block, which is then connected to one terminal of a loop antenna. $Tx2$ is connected to another 'Match' block, which is connected to the other terminal of the loop antenna. The $Tx2$ branch also includes a series impedance Z_{tx} connected to ground. The loop antenna is shown with a red arrow indicating current flow and blue wavy lines representing radiating waves. To the right is a physical layout of the loop antenna on a green substrate, showing a rectangular loop with a small gap. Fig 4. Single Loop Antenna concept (mode 1 illustration) <i>PN7150 Antenna Design and Matching Guide at 6.</i>  The illustration shows a series of vertical green bars of varying heights. On the right side, two horizontal arrows indicate signal levels: the top arrow is labeled 'ALM' and the bottom arrow is labeled 'Ref signal'. Fig 5. Load modulation illustration

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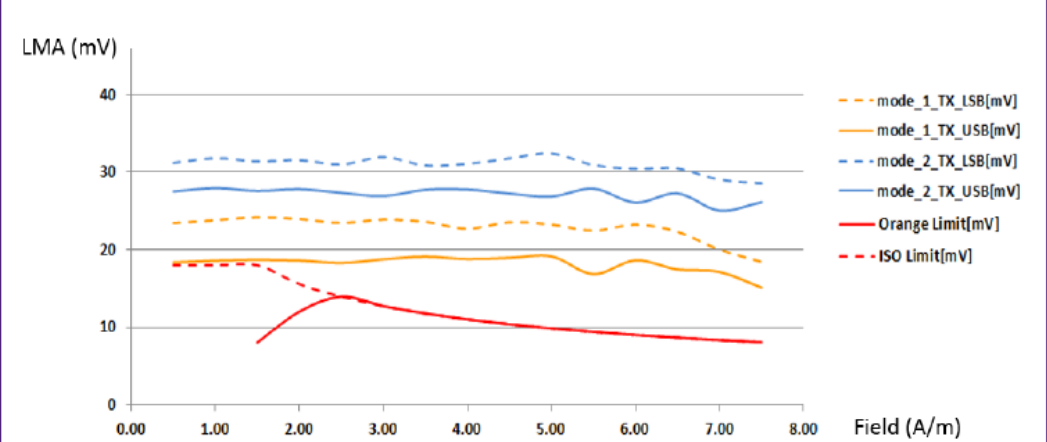
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	<i>PN7150 Antenna Design and Matching Guide at 6.</i> The ALM produces upper and lower sidebands with respect to the 13.56 MHz signal. On Fig 6 below LMA result examples are shown for Upper Side Band and Lower Side Band in the 2 modes available for PN7150 (respectively mode 1, mode 2). In order to minimize the phase error, it is necessary to limit the level of the load modulation amplitude (LMA) and to adjust the balance of the Side Bands when the field strength is low.  Fig 6. Example Side Bands ISO results for modes 1, 2 <i>PN7150 Antenna Design and Matching Guide at 7.</i>

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	The PN7150 CIU contains both a transmitter control block, TxCtrl, and a driver block/circuits, which perform active load modulation for communication to a point-of-sale terminal. The block diagram illustrates the internal architecture of the PN7150 CIU. At the top left is the CLESS INTERFACE UNIT, which contains sub-blocks: RF DETECT, SENSOR, DEMOD, ADC, DRIVER (highlighted in red), TxCtrl (highlighted in red), PLL, BG, and VMID. To its right is the CLESS UART with RX CODEC, TX CODEC, and SIGNAL PROCESSING. Further right is the HOST INTERFACE with an I2C-bus. Below these is a central AHB to APB bridge. To the right of the bridge is the ARM CORTEX M0, connected to DATA MEMORY (SRAM, EEPROM) and CODE MEMORY (ROM, EEPROM) via a MEMORY CONTROL block. Below the bridge are three main functional blocks: POWER MANAGEMENT UNIT (BATTERY MONITOR, 4.5 V TX-LDO, 1.8 V DSLDO), MISCELLANEOUS (TIMERS, CRC COPROCESSOR, RANDOM NUMBER GENERATOR), and CLOCK MANAGEMENT UNIT (OSCILLATOR 380 kHz, OSCILLATOR 40 MHz, FRACN PLL, QUARTZ OSCILLATOR). The diagram is labeled 'aaa-016737' in the bottom right corner. Fig 3. PN7150 block diagram <i>PN7150 Data Sheet</i> at 10 (emphasis added).

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	The <i>PN5180 Data Sheet</i> provides additional detail regarding transmitter circuitry within NXP NFC chips like the PN5180, PN7150, and SN110U. Like the PN7150, the PN5180 also has Tx pins for driving an antenna signal along a Tx path to the NFC reader. iCashe reserves the right to modify its contentions based on non-public information learned through discovery, including, <i>inter alia</i>, any differences in functionality across the SN110U, the PN7150, and the PN5180.

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	Figure 2. PN5180 Block diagram <i>PN5180 Data Sheet at 11 (emphasis added).</i>

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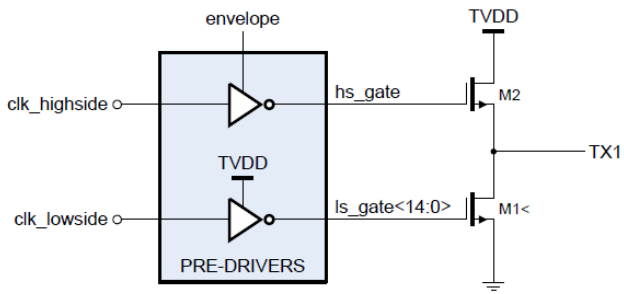
Claim Language	Infringement Evidence
	Transmitter TX The transmitter is able to drive an antenna circuit connected to outputs TX1 and TX2 with a 13.56 MHz carrier signal. The signal delivered on pins TX1 and pin TX2 is the 13.56 MHz carrier modulated by an envelope signal for energy and data transmission. It can be used to drive an antenna directly, using a few passive components for matching and filtering. For a differential antenna configuration, either TX1 or TX2 can be configured to put out an inverted clock. 100 % modulation and several levels of amplitude modulation on the carrier can be performed to support 13.56 MHz carrier-based RF-reader/writer protocols as defined by standards ISO/IEC14443 type A and B, FeliCa and ISO/IEC 18092.  Figure 29. PN5180 Output driver <i>PN5180 Data Sheet at 70.</i> As additionally discussed with respect to claim 5, the EMVCo certification of the Galaxy S21 as an EMV Contactless Level 1 Mobile Product indicates that the Galaxy S21 has an active load modulation circuit compliant with the requirements of the EMV Level 1 Specifications for Payment Systems. The EMVCo Certification further indicates that the Galaxy S21 supports Type A and Type B communications under the ISO/IEC 14443 standard.

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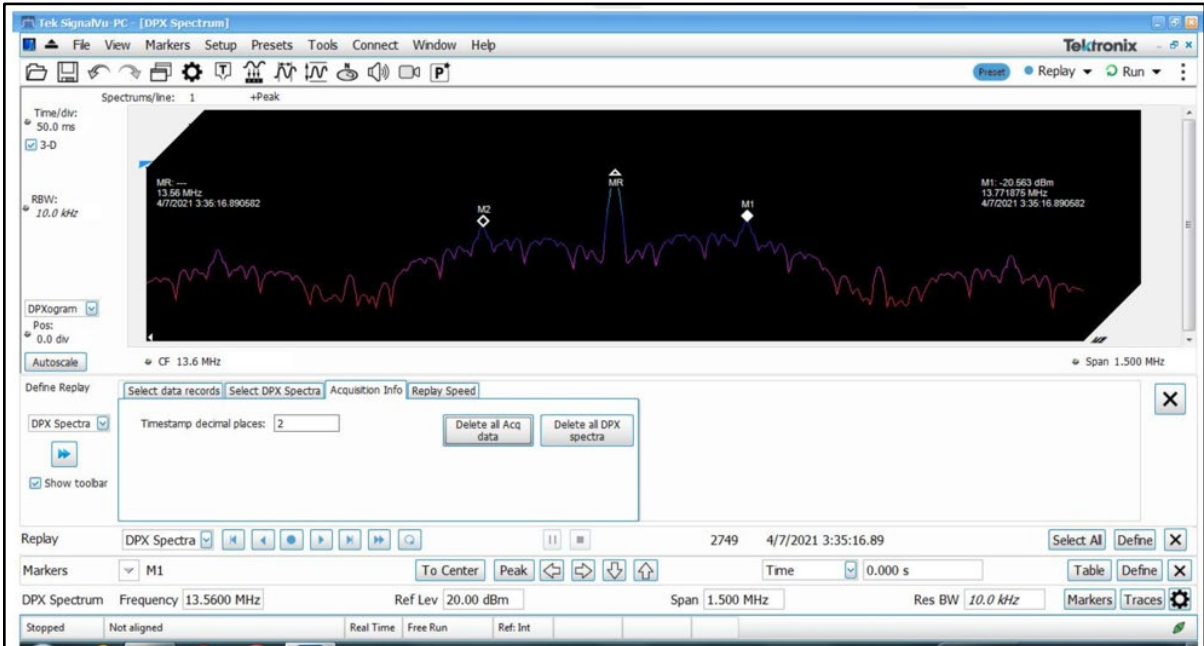
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	Testing of the Galaxy S21 confirms that the SN110U chip of the Galaxy S21 employs active load modulation. As shown below in Figure 1, which displays the RF signal analysis during an ongoing transaction with an NFC reader, the Galaxy S21 emits two actively modulated sideband spikes plus the carrier frequency (13.56 MHz). This confirms the presence of the load modulation circuitry discussed above.  <i>Figure 1: Samsung Galaxy S21 5G with ongoing transaction showing actively modulated spikes of power</i>

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	Figure 2 below displays the RF signal analysis of the Galaxy S21 with the Samsung Pay application enabled but with no NFC reader present. Like Figure 1, the figure shows the frequency spectrum enabled by the driver circuit consists of two actively modulated spikes (at $f_c/64$) plus the carrier frequency (13.56 MHz). Thus, Figure 2 demonstrates that the NFC transmission from the Galaxy S21 does not depend on coupling with the NFC reader and is therefore the product of an <i>active</i> load modulation circuit (including an active transmit driver). <table><thead><tr><th>Marker</th><th>Frequency</th><th>Time</th><th>Amplitude</th><th>Phase</th><th>Distance</th><th>DPX</th></tr></thead><tbody><tr><td>MR</td><td>13.56 MHz</td><td>-2.357 us</td><td>20.00 dBm</td><td>0.0000 °</td><td>0.000 m</td><td>---</td></tr><tr><td>M1</td><td>13.771875 MHz</td><td>0.000 s</td><td>0.00 dBm</td><td>0.0000 °</td><td>0.000 m</td><td>-24.323 dBm</td></tr><tr><td>M2</td><td>13.348125 MHz</td><td>0.000 s</td><td>0.00 dBm</td><td>0.0000 °</td><td>0.000 m</td><td>-25.330 dBm</td></tr></tbody></table> Figure 2: Samsung Galaxy S21 5G near Square reader showing actively modulated spikes of power	Marker	Frequency	Time	Amplitude	Phase	Distance	DPX	MR	13.56 MHz	-2.357 us	20.00 dBm	0.0000 °	0.000 m	---	M1	13.771875 MHz	0.000 s	0.00 dBm	0.0000 °	0.000 m	-24.323 dBm	M2	13.348125 MHz	0.000 s	0.00 dBm	0.0000 °	0.000 m	-25.330 dBm
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10. The mobile device of claim 5 wherein the mobile device comprises a mobile phone.	<i>See</i> claim 5, above. The Galaxy S21 is a mobile phone. <i>See</i> claim 4, above.
11. A mobile device comprising:	The Samsung Galaxy S21 5G smartphone is a mobile device. <i>See</i> claim 1, above.
[11.a] a smartcard controller that includes load modulation circuitry for half duplex communication by creating at least one frequency sideband about a carrier frequency of an interrogating radio frequency (RF) field;	The Galaxy S21 contains a smartcard controller that includes load modulation circuitry for half duplex communication by creating at least one frequency sideband about a carrier frequency of an interrogating radio frequency (RF) field. <i>See</i> limitation 1.a of claim 1, above.
[11.b] an antenna tuned to operate at 13.56 MHz;	The Galaxy S21 contains an antenna tuned to operate at 13.56 MHz. <i>See</i> limitation 1.b of claim 1, above.
[11.c] an amplifier coupled to be powered by the mobile device, wherein the amplifier is coupled to amplify a signal received from the antenna and to provide an amplified signal to the smartcard controller; and	The Galaxy S21 includes an amplifier coupled to be powered by the mobile device, wherein the amplifier is coupled to amplify a signal received from the antenna and to provide an amplified signal to the smartcard controller. <i>See</i> limitation 1.c of claim 1, above.

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[11.d] an active transmit driver circuit coupled between the smartcard controller and the antenna, wherein the active transmit driver circuit is coupled to be powered by the mobile device.	The Galaxy S21 includes an active transmit driver circuit coupled between the smartcard controller and the antenna, wherein the active transmit driver circuit is coupled to be powered by the mobile device. As discussed above with respect to limitation 1.a, the SN110U chip of the Galaxy S21 has a functional block for actively transmitting modulated data to the NFC reader for conducting the transaction. <i>See</i> limitation 1.d of claim 1, above. The active transmit driver circuit is located on the NXP SN110U chip, which is coupled to the power source, i.e., the Galaxy S21 battery, to receive power. Additionally, testing demonstrates that the NFC transmission from the Galaxy S21 does not depend on coupling with the NFC reader, indicating that the SN110U chip is powered. Its power source is the battery of the Galaxy S21. <i>See</i> limitation 1.a of claim 1, above.
12. The mobile device of claim 11 wherein the antenna comprises an inductive element too small to draw enough power sufficient to operate the smartcard controller from the interrogating radio frequency (RF) field.	<i>See</i> claim 11, above. The antenna of Galaxy S21 comprises an inductive element too small to draw enough power sufficient to operate the smartcard controller from the interrogating radio frequency (RF) field. <i>See</i> claim 2, above.
13. The mobile device of claim 11 wherein the smartcard controller is coupled to be powered by the mobile device.	<i>See</i> claim 11, above. The smartcard controller of the Galaxy S21 is coupled to be powered by the mobile device. <i>See</i> claim 3, above.

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14. The mobile device of claim 11 wherein the mobile device comprises a mobile phone.	<i>See</i> claim 11, above. The Galaxy S21 is a mobile phone. <i>See</i> claim 4, above.