

Exhibit 6

Exhibit 6 – Claim Chart Showing Amphenol’s Infringement of U.S. Patent 11,032,111 (“’111 Patent”)

As outlined in the below claim chart, the Accused Products infringe, either literally or under the doctrine of equivalents, at least Claim 8 of the ’111 patent.

’111 Patent	Accused Products
<u>Claim 8</u>	
[8pre] A chip-to-module communications link that comprises a port connector coupling a port transceiver to a pluggable module transceiver, the pluggable module transceiver including:	The Accused Products comprise a chip-to-module communications link that comprises a port connector coupling a port transceiver to a pluggable module transceiver. For example, the Accused Products, on information and belief, comprise a port connector. As can be seen below, each cable terminates on each end in a port connector. These connectors are OSFP (Octal Small Form Factor Pluggable) and QSFP (Quad Small Form Factor Pluggable) cable assemblies.

'111 Patent	Accused Products
Claim 8	
	OSFP (Octal Small Form Factor Pluggable) Copper Cable Assemblies 200G / 400G / 800G / 1.6T SOLUTIONS Amphenol is leading the industry in OSFP cable development. Our Electronics Products 'Product of the Year' award-winning OSFP (Octal Small Form Factor Pluggable) cable assemblies are compatible with 25G/lane channel NRZ up to 224G/lane channel PAM4 signaling protocols that allow the cables to deliver aggregate bandwidths of 200G, 400G, 800G, and 1.6T per cable assembly. Available in both Passive and Active variants. ▪ Comprehensive system integrated interconnect design for copper or optical based cable solutions ▪ Addresses current and future market desired bandwidth port capability requirements ▪ Optimized heat dissipative and airflow features to maximize the heat dissipative properties of the system ▪ Data Rate: 25G NRZ / 56G PAM4 / 112G PAM4 / 224G PAM4 ▪ Cable sizes: 25AWG – 32AWG ▪ 112G Passive cable lengths up to 2 meters ▪ 112G Active cable lengths up to 4 meters ▪ 224G Passive cable lengths up to 1 meter  TARGET MARKETS 

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	Amphenol is leading the industry in OSFP cable development. Our Electronics Products 'Product of the Year' award-winning OSFP (Octal Small Form Factor Pluggable) cable assemblies are compatible with 25G/lane channel NRZ up to 224G/lane channel PAM4 signaling protocols that allow the cables to deliver aggregate bandwidths of 200G, 400G, 800G, and 1.6T per cable assembly. Available in both Passive and Active variants. <i>Amphenol OSFP Copper Cable Assemblies Datasheet</i> (annotated) at https://cdn.amphenol-cs.com/media/wysiwyg/files/documentation/datasheet/cableassemblies/hsio_ca_osfp.pdf.

'111 Patent	Accused Products
Claim 8	
	QSFP DD Cable Assemblies 200G / 400G / 800G SOLUTIONS Amphenol's QSFP DD (Double Density) copper cable assemblies double the number of channels from 4 to 8 lanes when compared to the existing QSFP cabling systems, enabling more bandwidth within the same mechanical envelope. Compatible with 25G/Lane NRZ up to 112G/Lane PAM4 signaling protocols that allow cables to deliver aggregate bandwidths of 200G, 400G, and 800G per cable assembly. Available in both Passive and Active variants. ▪ Addresses current and future market desired bandwidth port capability requirements ▪ Backwards mate compatible with QSFP receptacles ▪ Data Rate: 25G NRZ / 56G PAM4 / 112G PAM4 ▪ Cable sizes: 25AWG – 32AWG ▪ 112G Passive cable lengths up to 2 meters ▪ 112G Active cable lengths up to 4 meters ▪ Ultra-low-power Active Electrical Cable featuring Smart CDR SoC up to 3 meters at only 4.5W per side  TARGET MARKETS  <i>Amphenol QSFP DD Cable Assemblies Datasheet at https://cdn.amphenol-cs.com/media/wysiwyg/files/documentation/datasheet/cableassemblies/hsio_ca_qsfp_dd.pdf.</i>
[8a] one or more transmit filters to each pre-equalize a corresponding serial symbol stream being transmitted to the port transceiver; and	The Accused Products comprise one or more transmit filters to each pre-equalize a corresponding serial symbol stream being transmitted to the port transceiver. For example, on information and belief, the DSP in the Accused Products use one or more transmit filters to pre-equalize a serial symbol stream being transmitted to the port transceiver.

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Claim 8	
	See, e.g.: As explained in the OSFP specification, the high-speed signals of the Accused Products meet the requirements of the IEEE 802.3ck standard. The high-speed signals follow the electrical specifications of IEEE802.3bs, IEEE802.3cd, IEEE 802.3ck and CEI-56G-VSR-PAM4 as defined in OIF-CEI-05.2 for 400GAUI-8 mode and IEEE802.3bj, IEEE802.3bm for CAUI-4 mode. <i>OSFP Specification</i>, https://www.osfpmsa.org/assets/pdf/OSFP_Module_Specification_Rev5_1.pdf at 147. The 802.3ck standard defines the chip-to-module connection, including requiring that the transmitters use a pre-equalized set of filters as part of selecting a short or long channel. 120G.3.2.1 Module output modes The module output shall support two modes: short and long. The means of controlling the module output mode is implementation dependent. For each output mode, the module shall meet the requirements for eye height (min) and VEC (max) in Table 120G-3 for both near-end and far-end measurements (see 120G.3.2.2.1). <i>IEEE 802.3ck Specification</i> at 7275 (annotated). The OSFP standard also requires compliance of the control plane to the CMIS standard.

'111 Patent	Accused Products
Claim 8	
	The OSFP specification defines: • The OSFP module mechanical form factor, including the latching mechanism; • The host cage together with the mating connector; • The electrical interface, including pin-out, data, control, and power and ground signals; • The mechanical interface, including the package outline, front panel, and printed circuit board (PCB) layout requirements; • Thermal requirements and limitations, including heat sink design and airflow; • Electrostatic discharge (ESD) requirements; • The module management interface as contained in the Common Management Interface Specification (CMIS). OSFP Specification (annotated), https://www.osfpmsa.org/assets/pdf/OSFP_Module_Specification_Rev5_1.pdf at 15. The CMIS standard controls the equalization of the port transceiver.

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	6.2.5.2 Rx Output Equalization Control Rx output equalization is defined at an appropriate test point defined by the relevant standard. SCS<k>::OutputEqPreCursorTargetRx<i> and SCS<k>::OutputEqPostCursorTargetRx<i> are four-bit control fields for lane <i> and encoded as shown in Table 6-7. The module advertises support of Rx output equalization control as described in Table 8-48. The module advertises the maximum supported Rx output equalization values as described in Table 8-44. Modules that require only output emphasis utilize the SCS<k>::OutputEqPostCursorTargetRx<i> fields and set the SCS<k>::OutputEqPreCursorTargetRx<i> fields to zero. Table 6-7 Rx Output Equalization Codes <table><tr><th>Code Value</th><th>Bit pattern</th><th>Post-Cursor Equalization</th><th>Pre-Cursor Equalization</th></tr><tr><td>0</td><td>0000b</td><td>0dB (No Equalization)</td><td>0dB (No Equalization)</td></tr><tr><td>1</td><td>0001b</td><td>1 dB</td><td>0.5 dB</td></tr><tr><td>2</td><td>0010b</td><td>2 dB</td><td>1.0 dB</td></tr><tr><td>3</td><td>0011b</td><td>3 dB</td><td>1.5 dB</td></tr><tr><td>4</td><td>0100b</td><td>4 dB</td><td>2.0 dB</td></tr><tr><td>5</td><td>0101b</td><td>5 dB</td><td>2.5 dB</td></tr><tr><td>6</td><td>0110b</td><td>6 dB</td><td>3.0 dB</td></tr><tr><td>7</td><td>0111b</td><td>7 dB</td><td>3.5 dB</td></tr><tr><td>8-10</td><td>1000b-1010b</td><td>Reserved</td><td>Reserved</td></tr><tr><td>11-15</td><td>1011b-1111b</td><td>Custom</td><td>Custom</td></tr></table> Note: The pre-cursor equalizer settings in dB approximates to Pre EQ (dB) = -20·log₁₀(1 - C₋₁ / (C₋₁+C₀+C₁)) (Eq. 6-1) The post-cursor equalizer settings in dB approximates to Post EQ (dB) = -20·log₁₀(1 - C₁ / (C₋₁+C₀+C₁)) (Eq. 6-2) Equalizer coefficients C_n are pre-cursor for n<0 and post-cursor when n>0. CMIS Specification, https://www.oiforum.com/wp-content/uploads/OIF-CMIS-05.2.pdf at 61.	Code Value	Bit pattern	Post-Cursor Equalization	Pre-Cursor Equalization	0	0000b	0dB (No Equalization)	0dB (No Equalization)	1	0001b	1 dB	0.5 dB	2	0010b	2 dB	1.0 dB	3	0011b	3 dB	1.5 dB	4	0100b	4 dB	2.0 dB	5	0101b	5 dB	2.5 dB	6	0110b	6 dB	3.0 dB	7	0111b	7 dB	3.5 dB	8-10	1000b-1010b	Reserved	Reserved	11-15	1011b-1111b	Custom	Custom
Code Value	Bit pattern	Post-Cursor Equalization	Pre-Cursor Equalization																																										
0	0000b	0dB (No Equalization)	0dB (No Equalization)																																										
1	0001b	1 dB	0.5 dB																																										
2	0010b	2 dB	1.0 dB																																										
3	0011b	3 dB	1.5 dB																																										
4	0100b	4 dB	2.0 dB																																										
5	0101b	5 dB	2.5 dB																																										
6	0110b	6 dB	3.0 dB																																										
7	0111b	7 dB	3.5 dB																																										
8-10	1000b-1010b	Reserved	Reserved																																										
11-15	1011b-1111b	Custom	Custom																																										
[8b] a controller having multiple registers, each of the multiple registers containing a set of initial coefficient values corresponding to a different	The Accused Products comprise a controller having multiple registers, each of the multiple registers containing a set of initial coefficient values corresponding to a different channel model, the controller using one of the registers to set initial coefficient values for the one or more transmit filters.																																												

'111 Patent	Accused Products
Claim 8 channel model, the controller using one of the registers to set initial coefficient values for the one or more transmit filters.	For example, on information and belief, the DSP in the Accused Products includes a controller that selects initial coefficient values corresponding to “short” and “long” modes used for 112G C2M transmission, per the IEEE 802.3ck standard, which correspond to different channel models. On information and belief, the DSP has registers that are used to set the initial coefficient values for the transmit filters. See, e.g., 120G.3.2.1 Module output modes The module output shall support two modes: short and long. The means of controlling the module output mode is implementation dependent. For each output mode, the module shall meet the requirements for eye height (min) and VEC (max) in Table 120G–3 for both near-end and far-end measurements (see 120G.3.2.2.1). <i>IEEE 802.3ck Specification</i> at 7275 (annotated). The datasheet for the exemplary Accused Products notes that they support the 802.3ck standard: SPECIFICATIONS ▪ Refer to the latest revision specification of the OSFP octal small form factor pluggable module ▪ Applicable IEEE specifications ▪ IEEE802.3by ▪ IEEE802.3bj ▪ IEEE802.3cd ▪ IEEE802.3ck ▪ The InfiniBand™ architecture specification and annexes <i>Amphenol OSFP Copper Cable Assemblies Datasheet</i> (annotated) at https://cdn.amphenol-cs.com/media/wysiwyg/files/documentation/datasheet/cableassemblies/hsio_ca_osfp.pdf.

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Claim 8	
	SPECIFICATIONS ▪ Refer to the latest revision of the QSFP-DD hardware specification for QSFP double density 8X pluggable transceiver ▪ Applicable IEEE specifications ▪ IEEE802.3by ▪ IEEE802.3bj ▪ IEEE802.3cd ▪ IEEE802.3ck ▪ The InfiniBand™ architecture specification and annexes <i>Amphenol QSFP DD Cable Assemblies Datasheet</i> at https://cdn.amphenol-cs.com/media/wysiwyg/files/documentation/datasheet/cableassemblies/hsio_ca_qsfp_dd.pdf. The datasheet also notes that a nonvolatile memory (an EEPROM) is integrated into the cable assembly, which, on information and belief, contains the registers that is used to set initial coefficient values for the one or more transmit filters. ▪ EEPROM in cable assembly <i>Amphenol OSFP Copper Cable Assemblies Datasheet</i> (annotated) at https://cdn.amphenol-cs.com/media/wysiwyg/files/documentation/datasheet/cableassemblies/hsio_ca_osfp.pdf.

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	▪ EEPROM in cable assembly <i>Amphenol QSFP DD Cable Assemblies Datasheet</i> at https://cdn.amphenol-cs.com/media/wysiwyg/files/documentation/datasheet/cableassemblies/hsio_ca_qsfp_dd.pdf.