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ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

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Assignors	Execution date
UNITED MICROELECTRONICS CORP.	Jun 30, 2025

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Properties (43 total)

Patent	Publication	Application	PCT	International registration
1. MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE Inventors: CHUN-HSIEN LIN, CHAO-CHING HSIEH				PPUBSApp PPUBSPat
7915127 Mar 29, 2011	20110020994 Jan 27, 2011	12509623 Jul 27, 2009		
2. GATE STRUCTURES OF CMOS DEVICE AND METHOD FOR MANUFACTURING THE SAME Inventors: CHUN-HSIEN LIN, CHIN-FU LIN				PPUBSApp PPUBSPat
8067806 Nov 29, 2011	20110062524 Mar 17, 2011	12557535 Sep 11, 2009		
3. SEMICONDUCTOR DEVICE Inventors: CHUN-HSIEN LIN, CHAO-CHING HSIEH				PPUBSApp PPUBSPat
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Patent	Publication	Application	PCT	International registration
4. METAL OXIDE SEMICONDUCTOR TRANSISTOR AND MANUFACTURING METHOD THEREOF Inventors: WEN-TAI CHIANG, CHUN-HSIEN LIN				PPUBSApp PPUBSPat
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5. SEMICONDUCTOR DEVICE WITH ULTRA THIN SILICIDE LAYER Inventors: KUO-CHIH LAI, CHIA CHANG HSU, NIEN-TING HO, BOR-SHYANG LIAO, SHU MIN HUANG, MIN-CHUNG CHENG, YU-RU YANG				PPUBSApp PPUBSPat
8766319 Jul 1, 2014	20130288456 Oct 31, 2013	13456238 Apr 26, 2012		
6. FIELD EFFECT TRANSISTOR AND MANUFACTURING METHOD THEREOF Inventors: TONG-YU CHEN, CHIH-JUNG WANG				PPUBSApp PPUBSPat
9012975 Apr 21, 2015	20130334588 Dec 19, 2013	13517759 Jun 14, 2012		
7. SEMICONDUCTOR STRUCTURE HAVING CONTACT PLUG AND METHOD OF MAKING THE SAME Inventors: CHING-WEN HUNG, CHIH-SEN HUANG, PO-CHAO TSAO, CHIEH-TE CHEN				PPUBSApp PPUBSPat
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8. SEMICONDUCTOR DEVICE AND FABRICATION METHOD THEREOF Inventors: CHING-WEN HUNG, CHIH-SEN HUANG, PO-CHAO TSAO, SHIH-FANG TZOU				PPUBSApp PPUBSPat
9006804 Apr 14, 2015	20140361352 Dec 11, 2014	13912173 Jun 6, 2013		
9. SEMICONDUCTOR STRUCTURE AND MANUFACTURING METHOD OF THE SAME Inventors: HUNG-YI WU, CHIEN-MING LAI, YI-WEN CHEN				PPUBSApp PPUBSPat
9231071 Jan 5, 2016	20150243754 Aug 27, 2015	14187701 Feb 24, 2014		
10. METHOD FOR FABRICATING SEMICONDUCTOR DEVICE Inventors: KUO-CHIH LAI, CHIA CHANG HSU, NIEN-TING HO, BOR-SHYANG LIAO, SHU MIN HUANG, MIN-CHUNG CHENG, YU-RU YANG				PPUBSApp PPUBSPat
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Patent	Publication	Application	PCT	International registration
11. SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME Inventors: YU-CHENG TUNG				PPUBSApp PPUBSPat
9870994 Jan 16, 2018	20160079173 Mar 17, 2016	14489461 Sep 17, 2014		
12. SEMICONDUCTOR DEVICE HAVING CONTACT PLUG IN TWO DIELECTRIC LAYERS AND TWO ETCH STOP LAYERS Inventors: CHING-WEN HUNG, CHIH-SEN HUANG, YI-WEI CHEN, CHIEN-TING LIN, SHIH-FANG TZOU, CHIA-LIN LU, CHUN-LUNG CHEN, KUN-YUAN LIAO, FENG-YI CHANG, CHIEH-TE CHEN				PPUBSApp PPUBSPat
9673100 Jun 6, 2017	20160104645 Apr 14, 2016	14536696 Nov 10, 2014		
13. METAL OXIDE SEMICONDUCTOR TRANSISTOR AND MANUFACTURING METHOD THEREOF Inventors: WEN-TAI CHIANG, CHUN-HSIEN LIN				PPUBSApp PPUBSPat
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14. SEMICONDUCTOR DEVICE Inventors: TIEN-CHEN CHAN, HSIN-CHANG WU, CHUN-YU CHEN, MING-HUA CHANG, SHENG-HSU LIU, CHIEH-LUNG WU, CHUNG-MIN TSAI, NENG-HUI YANG				PPUBSPat
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15. FIELD EFFECT TRANSISTOR AND MANUFACTURING METHOD THEREOF Inventors: TONG-YU CHEN, CHIH-JUNG WANG				PPUBSApp PPUBSPat
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16. METHOD FOR FABRICATING SEMICONDUCTOR DEVICE WITH PATTERNED HARD MASK Inventors: CHING-WEN HUNG, CHIH-SEN HUANG, PO-CHAO TSAO, SHIH-FANG TZOU				PPUBSApp PPUBSPat
9281199 Mar 8, 2016	20150179457 Jun 25, 2015	14639134 Mar 5, 2015		
17. SEMICONDUCTOR STRUCTURE HAVING CONTACT PLUG AND METHOD OF MAKING THE SAME Inventors: CHING-WEN HUNG, PO-CHAO TSAO, CHIH-SEN HUANG, CHIEH-TE CHEN				PPUBSApp PPUBSPat

Patent	Publication	Application	PCT	International registration
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18. STATIC RANDOM ACCESS MEMORY Inventors: CHAO-HUNG LIN, SSU-I FU, CHIH-KAI HSU, YU-HSIANG HUNG, JYH-SHYANG JENQ				PPUBSApp PPUBSPat
9589966 Mar 7, 2017	20160322366 Nov 3, 2016	14724775 May 28, 2015		
19. SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME Inventors: CHING-WEN HUNG, JIA-RONG WU, YI-HUI LEE, YING-CHENG LIU, CHIH-SEN HUANG, CHUN-HSIEN LIN				PPUBSPat
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20. MANUFACTURING METHOD OF METAL OXIDE SEMICONDUCTOR TRANSISTOR Inventors: WEN-TAI CHIANG, CHUN-HSIEN LIN				PPUBSApp PPUBSPat
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21. SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME Inventors: YU-MING HSU, CHUN-LIANG KUO, TSANG-HSUAN WANG, SHENG-HSU LIU, CHIEH-LUNG WU, CHUNG-MIN TSAI, YI-WEI CHEN				PPUBSPat
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22. METHOD OF MAKING SEMICONDUCTOR STRUCTURE HAVING CONTACT PLUG Inventors: CHING-WEN HUNG, CHIH-SEN HUANG, PO-CHAO TSAO, CHIEH-TE CHEN				PPUBSApp PPUBSPat
10049929 Aug 14, 2018	20160141207 May 19, 2016	15008453 Jan 28, 2016		
23. SEMICONDUCTOR DEVICE HAVING A CAP LAYER WITH V-SHAPE Inventors: CHIH-KAI HSU, SSU-I FU, YU-HSIANG HUNG, WEI-CHI CHENG, JYH-SHYANG JENQ, TSUNG-MU YANG				PPUBSApp PPUBSPat
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24. SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF Inventors: EN-CHIUAN LIOU, YU-CHENG TUNG, CHIH-WEI YANG				PPUBSApp PPUBSPat

Patent	Publication	Application	PCT	International registration
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25. METHOD FOR FABRICATING SEMICONDUCTOR DEVICE Inventors: JIA-RONG WU, CHING-WEN HUNG, YI-HUI LEE, CHIH-SEN HUANG, YING-CHENG LIU, CHUN-HSIEN LIN				PPUBSApp PPUBSPat
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