

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

TAIWAN SEMICONDUCTOR MANUFACTURING COMPANY LTD. and
APPLE INC.,
Petitioners,
v.

MARLIN SEMICONDUCTOR LTD.,
Patent Owner.

Case No. IPR2025-00879
U.S. Patent No. 9,093,473

PETITIONERS' UPDATED EXHIBIT LIST

Pursuant to 37 C.F.R. § 42.63(e), Petitioners Taiwan Semiconductor Manufacturing Company Ltd. and Apple Inc. hereby submit a current listing of Petitioner exhibits. Exhibits 1042-1043 are being filed today.

Exhibit	Description
1001	U.S. Patent No. 9,093,473 (“’473 Patent”)
1002	Prosecution History for U.S. Patent No. 9,093,473 (“File History”)
1003	Declaration of Dr. Eugene Fitzgerald, Ph.D.
1004	U.S. Patent Publication No. 2009/0309141 (“Okuno”)
1005	U.S. Patent Publication No. 2008/0251878 (“Mandelman”)
1006	U.S. Patent Publication No. 2006/0088968 (“Shin”)
1007	U.S. Patent Publication No. 2008/0142879 (“Chong”)
1008	S.M. Sze, <i>Physics of Semiconductor Devices</i> . (2d ed. 1981) (excerpted) (“Sze”)
1009	J.D. Plummer et al., <i>Silicon VLSI Technology: Fundamentals, Practice and Modeling</i> (2000) (excerpted) (“Plummer”)
1010	Excerpts from C.Y. Chang & S.M Sze, <i>ULSI Technology</i> (1996) “Chang/Sze”
1011	U.S. Patent Publication No. 2006/0118878 (“Huang”)
1012	U.S. Patent Publication No. 2010/0159685 (“Chuang”)
1013	Excerpts from S. Wolf, <i>Silicon Processing for the VLSI Era, Vol. 1: Process Technology</i> , 2 nd Ed. (2000) (“Wolf1”)
1014	Excerpts from S. Wolf, <i>Silicon Processing for the VLSI Era, Vol. 2: Process Integration</i> (1990) (“Wolf2”)

Exhibit	Description
1015	Excerpts from S. Wolf, <i>Silicon Processing for the VLSI Era, Vol. 3: The Submicron MOSFET</i> (1995) (“Wolf3”)
1016	M. Quirk & J. Serda, <i>Semiconductor Manufacturing Technology</i> , Ch. 12: Metallization (2001) (excerpted) (“Quirk”)
1017	S.E. Thompson et al., “A 90-nm Logic Technology Featuring Strained-Silicon,” <i>IEEE Transactions on Electron Devices</i> , vol. 51. No. 11, pp. 1790-97 (Nov. 2004) (“Thompson2004”)
1018	U.S. Pat. Pub. No. 2006/0148151 (“Murthy151”)
1019	D. James, “2004 – The Year of 90-nm: A Review of 90 nm Devices,” 2005 IEEE/SEMI Advanced Semiconductor Manufacturing Conference, pp. 72-76 (2005) (“James”)
1020	U.S. Patent Publication No. 2006/0292783 (“Lee783”)
1021	U.S. Patent Publication No. 2008/0157119 (“Tsai”)
1022	U.S. Patent Publication No. 2006/0115949 (“Zhang”)
1023	U.S. Patent Publication No. 2004/0262683 (“Bohr”)
1024	B. J. Ginsberg, J. Burghartz, G. B. Bronner and S. R. Mader, “Selective epitaxial growth of silicon and some potential applications,” in <i>IBM Journal of Research and Development</i> , vol. 34, no. 6, pp. 816-827, Nov. 1990 (“Ginsberg”)
1025	P. Bai et al., “A 65nm Logic Technology Featuring 35nm Gate Lengths, Enhanced Channel Strain, 8 Cu Interconnect Layers, Low-k ILD and 0.57 μm^2 SRAM Cell,” <i>Technical Digest of the 2004 IEEE International Electron Devices Meeting (IEDM)</i> , pp. 657-60 (Dec. 2004) (“Bai”)
1026	U.S. Patent No. 6,287,904 (“Lee”)
1027	U.S. Patent Publication No. 2003/0124847 (“Houston”)

Exhibit	Description
1028	U.S. Patent Publication No. 2009/0023258 (“Liang”)
1029	U.S. Patent Publication No. 2009/0176069 (“Yang”)
1030	U.S. Patent No. 6,605,498 (“Murthy”)
1031	U.S. Patent Publication No. 2008/0199999 (“Weijtmans”)
1032	U.S. Patent Publication No. 2008/0128746 (“Wang”)
1033	U.S. Patent Publication No. 2005/0112817 (“Cheng”)
1034	U.S. Patent No. 6,137,126 (“Avanzino”)
1035	U.S. Patent Publication No. 2007/0284615 (“Ku”)
1036	U.S. Patent Publication No. 2005/0260806 (“Chang”)
1037	U.S. Patent Publication No. 2006/0223255 (“Chen”)
1038	U.S. Patent Publication No. 2004/0253776 (“Hoffman”)
1039	U.S. Patent Publication No. 2006/0172556 (“Bather”)
1040	U.S. Patent No. 6,727,558 (“Duane”)
1041	U.S. Patent Publication No. 2011/0039390 (“Nieh”)
1042	Declaration of Jodie W. Cheng in Support of Co-Petitioner TSMC’s Motion for <i>Pro Hac Vice</i> Admission
1043	Declaration of Gyushik (Kevin) Jang in Support of Co-Petitioner TSMC’s Motion for <i>Pro Hac Vice</i> Admission

Respectfully submitted,

Dated: June 27, 2025

/ J. Preston Long /

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Lead Counsel for Petitioners

CERTIFICATE OF SERVICE

The undersigned hereby certifies that a copy of the foregoing **Petitioners' Updated Exhibit List and Exhibits 1042-1043** were served on June 27, 2025, via e-mail directed to counsel of record for Patent Owner and Co-Petitioner at the following:

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U.S. Patent No. 9,093,473

Patent Owner has consented to electronic service by email.

Dated: June 27, 2025

/William Esper/
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