

14/331,229 | NAUP1220USA1: METHOD FOR FABRICATING METAL-OXIDE SEMICONDUCTOR TRANSISTOR

[PUBLIC VIEW](#)

Application #	Confirmation #	Attorney Docket #	Patent #
14/331,229	8157	NAUP1220USA1	9,093,473  Download PDF  Text  Issued - 07/28/2015
Filing or 371 (c) date	Status		
07/15/2014	Patented Case 07/08/2015		

 [Show/hide menu](#)

Continuity

Parent data

Application #	Description	Parent application #	Filing or 371 (c) date	Status	Patent #	AIA
14/331,229	is a Division of	12/837,475	07/15/2010	Patented	8816409	No

Child data

No child data available for selected application