



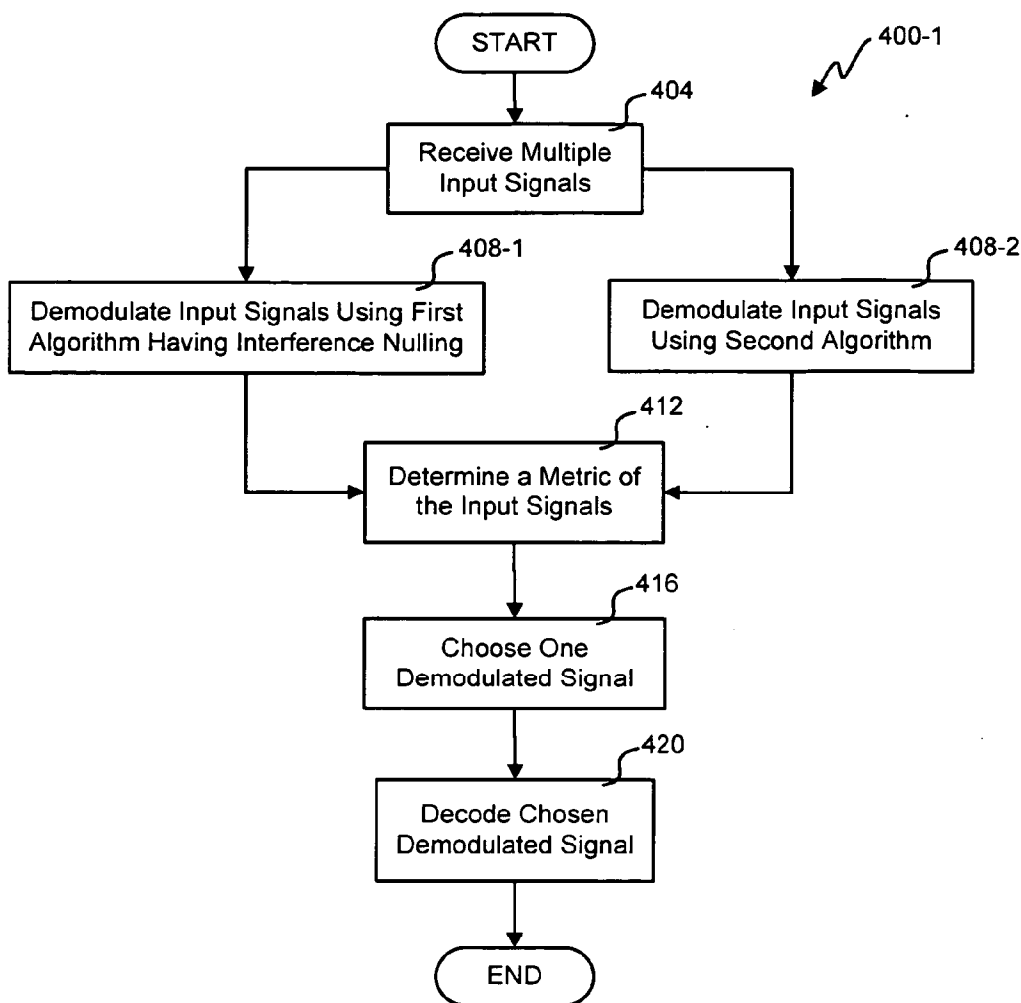
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(19) **United States**(12) **Patent Application Publication**
Sampath(10) **Pub. No.: US 2007/0060061 A1**(43) **Pub. Date: Mar. 15, 2007**(54) **MULTIPLE HYPOTHESIS DECODING****Publication Classification**(76) Inventor: **Hemanth Sampath**, San Diego, CA
(US)(51) **Int. Cl.**
H04B 1/00 (2006.01)(52) **U.S. Cl.** **455/63.1**Correspondence Address:
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SAN DIEGO, CA 92121 (US)(57) **ABSTRACT**

A multiple-input receiver for processing one or more communication signals is disclosed. The receiver includes a first and second demodulators, a decoder and decision logic. The first and second demodulators respectively use a first algorithm and a second algorithm and are both coupled to the receiver. The first algorithm includes interference nulling and is different from the second algorithm. A decoder can be alternatively used with the first or second demodulators to decode one or more signals from the receiver. The decoder produces a decoded signal. The decision logic chooses to use either the first demodulator or the second demodulator to affect the decoded signal.

(21) Appl. No.: **11/501,800**(22) Filed: **Aug. 8, 2006****Related U.S. Application Data**

(60) Provisional application No. 60/753,775, filed on Dec. 23, 2005. Provisional application No. 60/710,407, filed on Aug. 22, 2005.



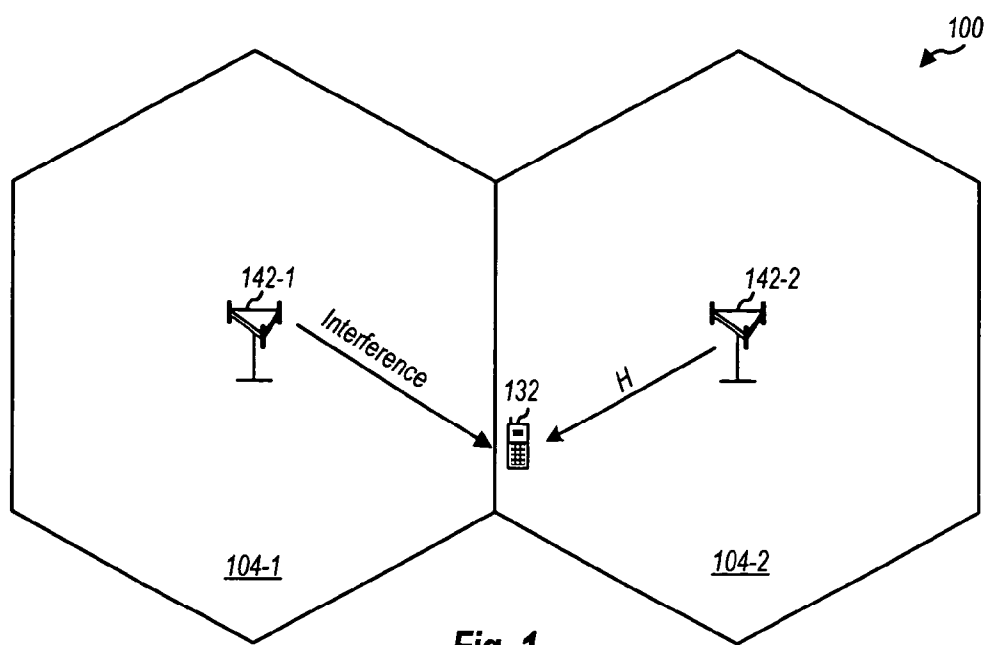


Fig. 1

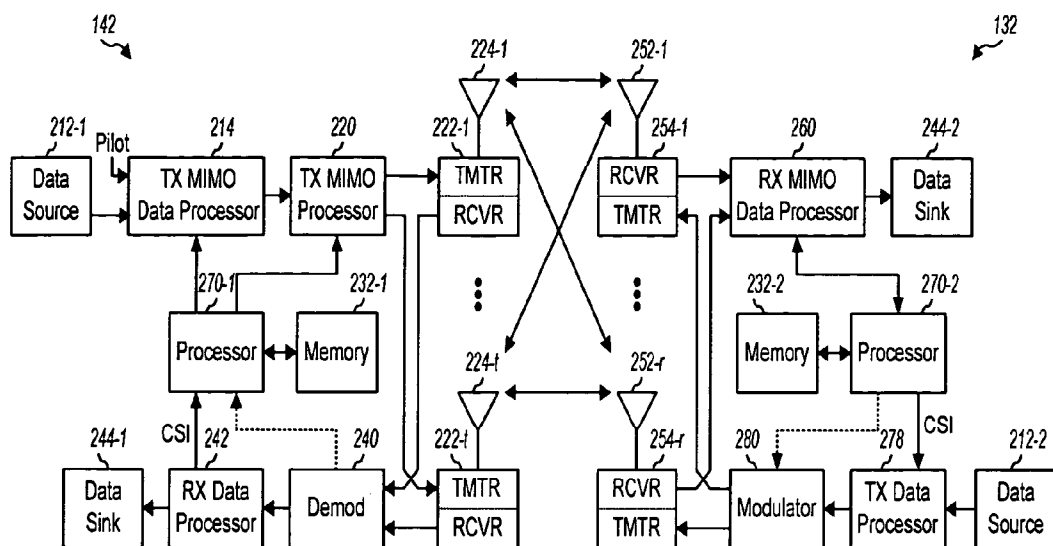


Fig. 2A

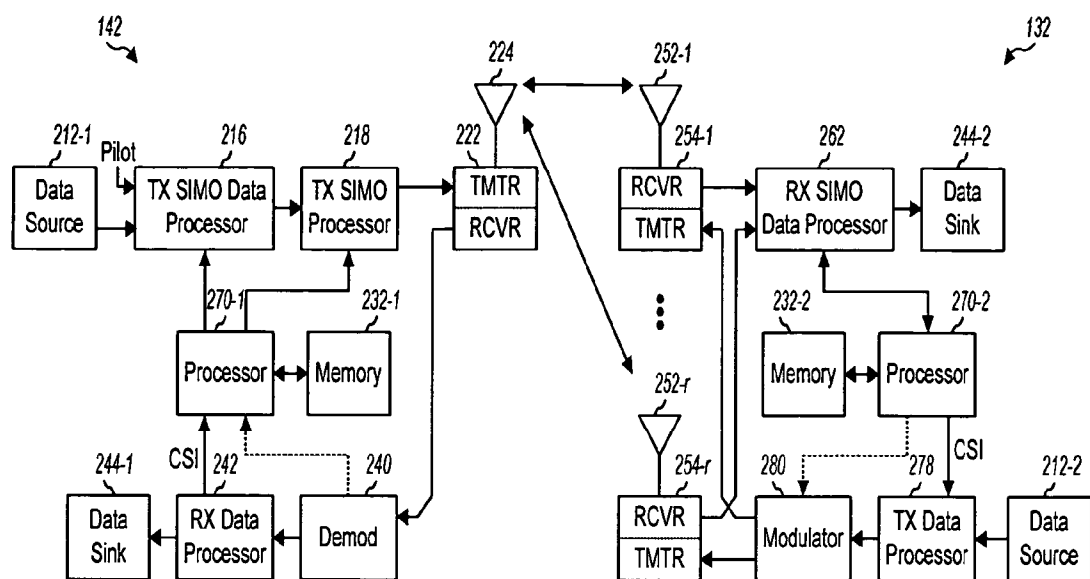


Fig. 2B

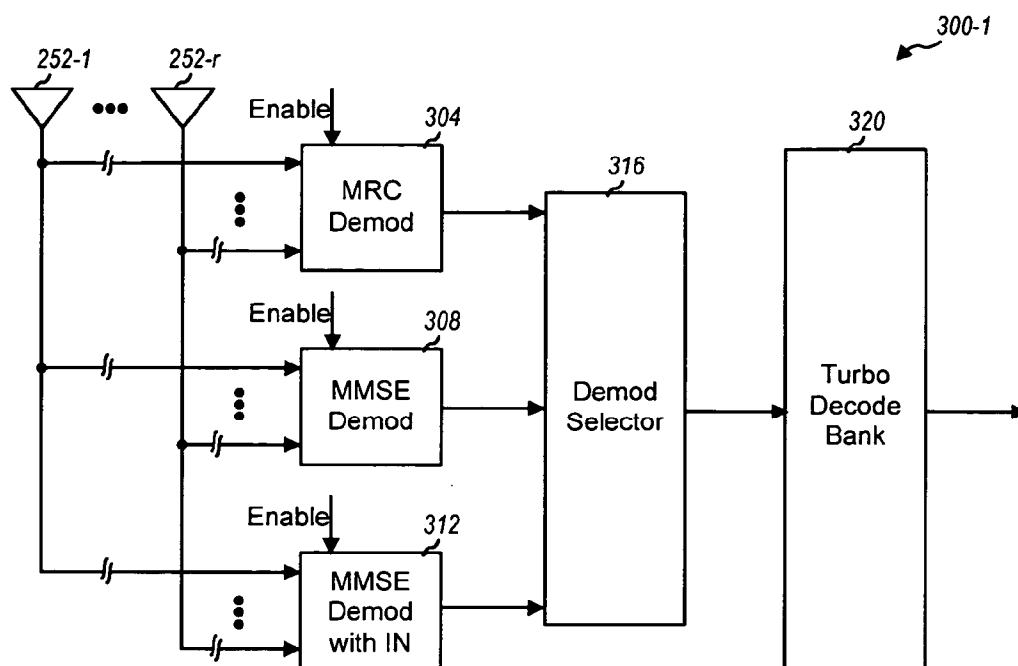


Fig. 3A

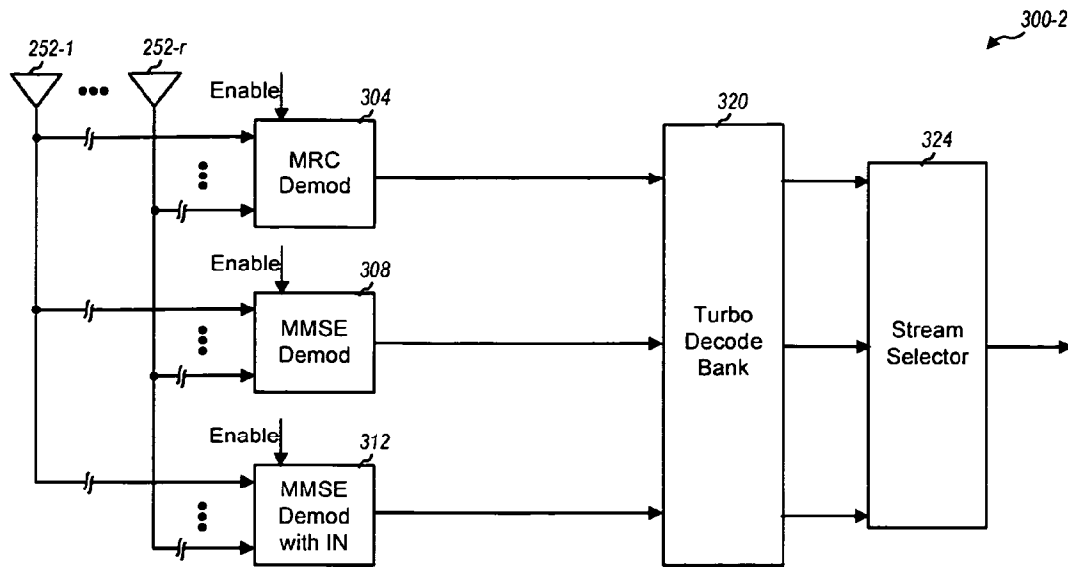


Fig. 3B

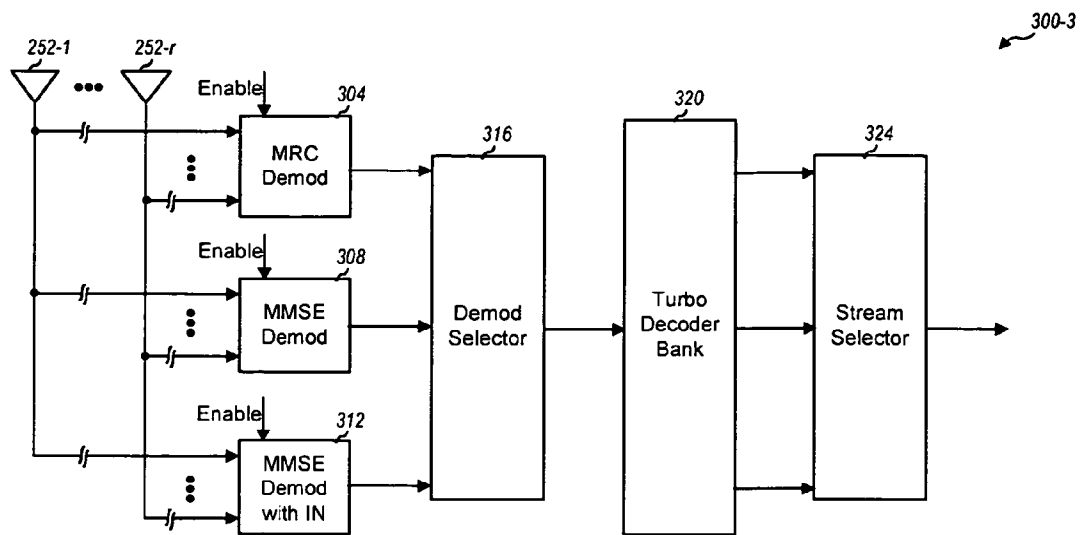


Fig. 3C

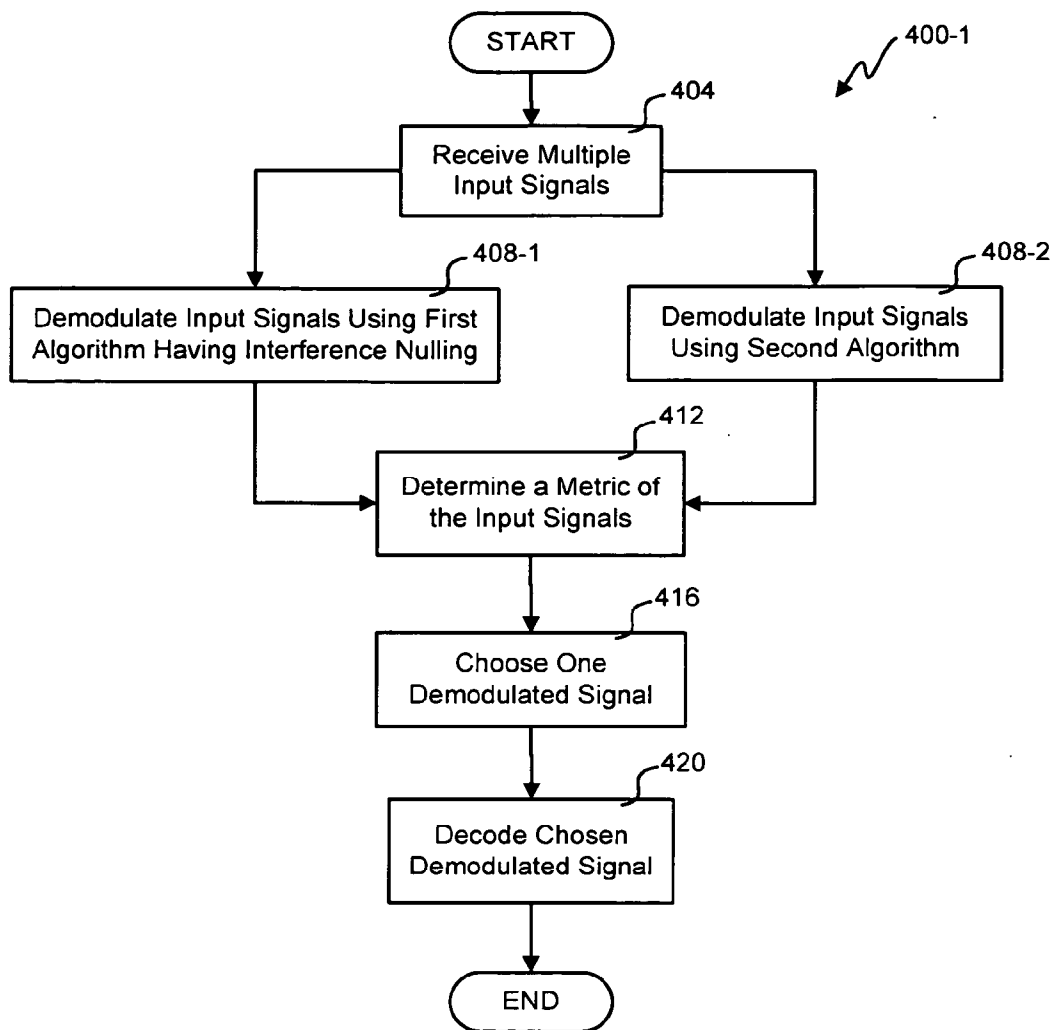


Fig. 4A

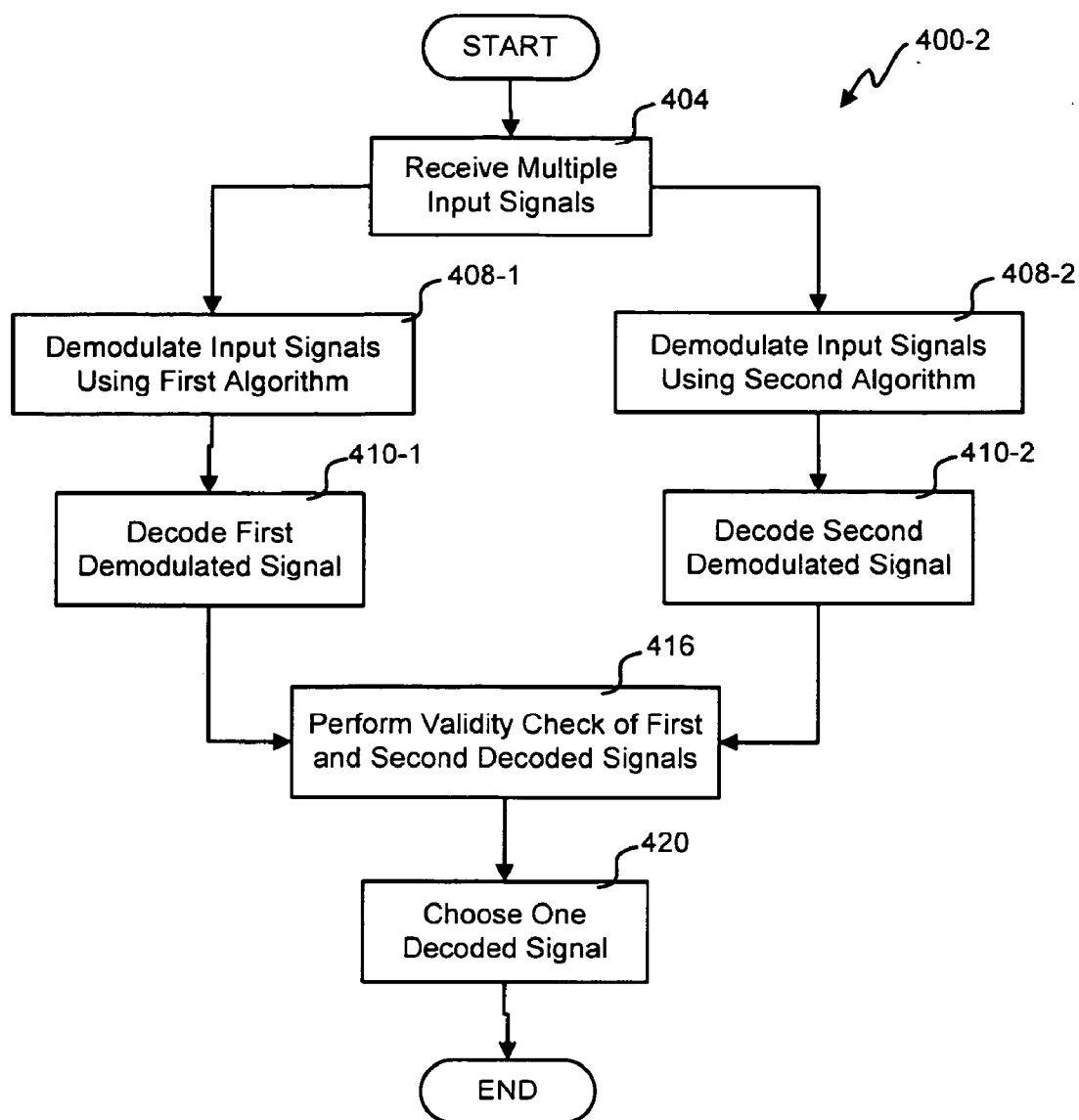


Fig. 4B

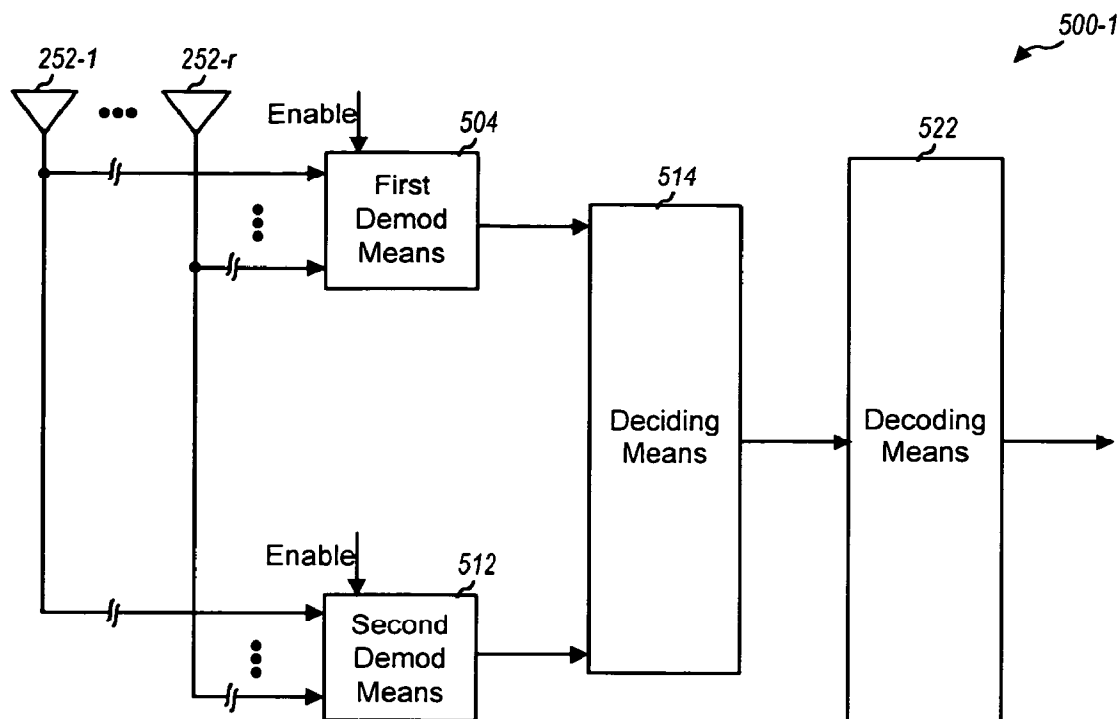


Fig. 5A

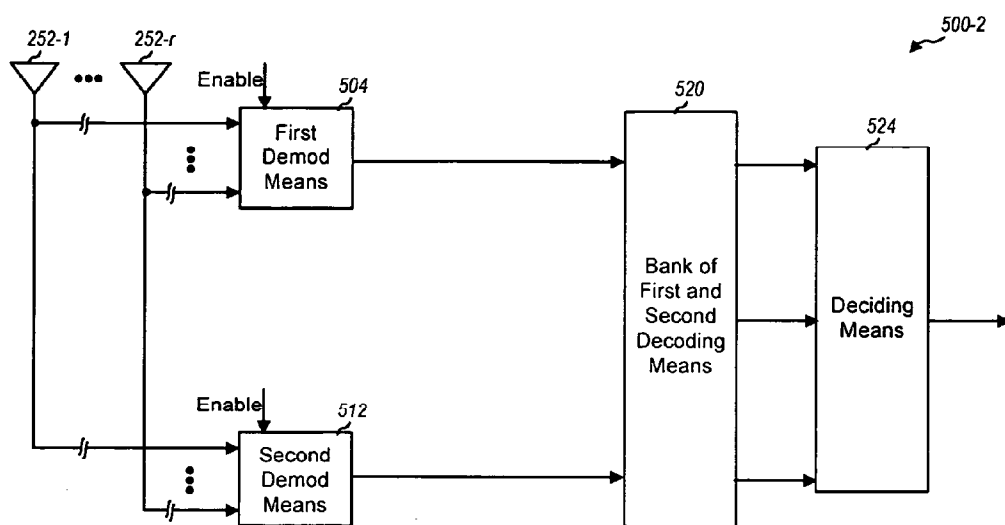


Fig. 5B

MULTIPLE HYPOTHESIS DECODING

CLAIM OF PRIORITY UNDER 35 U.S.C. §119

[0001] The present Application for Patent claims priority to Provisional Application No. 60/710,407 entitled "MULTIPLE HYPOTHESIS DECODING TO SUPPORT INTERFERENCE CANCELLATION" filed Aug. 22, 2005, and Provisional Application No. 60/753,775 entitled "MULTIPLE HYPOTHESIS DECODING" filed Dec. 23, 2005, both assigned to the assignee hereof and hereby expressly incorporated by reference herein.

BACKGROUND

[0002] The present disclosure relates generally to data or voice communication, and more specifically to communication systems that may experience interference.

[0003] Communication systems transport information between two points, for example, wirelessly. An access terminal communicates wirelessly with one or more access terminals to transport data, voice or other information. The term access terminal can be used interchangeably with access point or base station, and the term access terminal can be used interchangeably with wireless communication device, user equipment, handset, or mobile station.

[0004] Multiple-access communication systems allow using multiple access terminals with a single base station. Further, multiple base stations using at least some of the same wireless media can be arranged in cells or just randomly dispersed. For example, cellular telephone systems arrange their base stations in cell patterns to allow communication with an access terminal to any of the base stations in a seamless way even when the access terminal may be moving. WIFI™ can be more randomly dispersed and may not allow seamless movement between base stations.

[0005] Many communication systems today use multiple antennas to increase performance of wireless communication. Such systems are termed single input, multiple output (SIMO) or multiple input, multiple output (MIMO). Each are characterized by transmission and/or reception using multiple antennas that each receive the transmission. Use of multiple antennas can increase the efficiency of the communication system.

SUMMARY

[0006] In one embodiment, the present disclosure provides a multiple-input receiver. The receiver includes a first and second demodulators, a decoder and decision logic. The first and second demodulators respectively use a first algorithm and a second algorithm. The first algorithm includes demodulation with interference nulling, whereas the second algorithm assumes demodulation without interference nulling, for example. The decoder includes decoding portions, where each of the decoding portions can be alternatively used with the first or second demodulators to demodulate one or more signals from the receiver. The decoder produces a decoded signal. The decision logic chooses to use either the first demodulator output or the second demodulator output, as input to the decoded signal.

[0007] In another embodiment, the present disclosure provides a method for processing one or more communication signals. One or more communication signals are demodu-

lated using a first algorithm, where the first algorithm includes interference nulling. The one or more communication signals are also demodulated using a second algorithm, which is different from the first algorithm. A decision is made between the first and second algorithms to produce a demodulated signal. The demodulated signal is decoded to produce a decoded signal.

[0008] In another embodiment, the present disclosure provides a multiple-input receiver. The multiple-input receiver includes first and second means for demodulating, means for deciding, and means for decoding. The first means demodulates the one or more communication signals using a first algorithm that uses interference nulling. The second means demodulates the one or more communication signals using a second algorithm, which is different from the first algorithm. The means for deciding chooses between the first and second algorithm to produce a demodulated signal. The means for decoding the demodulated signal produces a decoded signal.

[0009] The present disclosure also discloses a processor readable medium including instructions thereon that may be used by one or more processors. The instructions include instructions for demodulating one or more communication signals using a first algorithm, where the first algorithm includes interference nulling. Instructions for demodulating the one or more communication signals using a second algorithm, which is different from the first algorithm. Instructions for deciding between the first and second algorithms to produce a demodulated signal and instructions for producing a decoded signal for the demodulated signal.

[0010] Further areas of applicability of the present disclosure will become apparent from the detailed description provided hereinafter. It should be understood that the detailed description and specific examples, while indicating various embodiments, are intended for purposes of illustration only and are not intended to necessarily limit the scope of the disclosure.

BRIEF DESCRIPTION OF THE DRAWINGS

[0011] The present disclosure is described in conjunction with the appended figures:

[0012] FIG. 1 is a block diagram of an embodiment of a multiple-access communication system;

[0013] FIGS. 2A and 2B are block diagrams of embodiments of a base station and an access terminal communicating;

[0014] FIGS. 3A, 3B and 3C are block diagrams that show embodiments of a RX MIMO data processor portion that decodes received channels;

[0015] FIGS. 4A and 4B are flow diagrams that show embodiments of a process for demodulating and decoding one or more signals; and

[0016] FIGS. 5A and 5B are block diagrams that show embodiments of means for demodulating and decoding one or more signals.

[0017] In the appended figures, similar components and/or features may have the same reference label. Further, various components of the same type may be distinguished by following the reference label by a dash and a second label

that distinguishes among the similar components. If only the first reference label is used in the specification, the description is applicable to any one of the similar components having the same first reference label irrespective of the second reference label.

DETAILED DESCRIPTION

[0018] The ensuing description provides preferred exemplary embodiment(s) only, and is not intended to limit the scope, applicability or configuration of the disclosure. Rather, the ensuing description of the preferred exemplary embodiment(s) will provide those skilled in the art with an enabling description for implementing a preferred exemplary embodiment. It being understood that various changes may be made in the function and arrangement of elements without departing from the spirit and scope of the disclosure as set forth in the appended claims.

[0019] Specific details are given in the following description to provide a thorough understanding of the embodiments. However, it will be understood by one of ordinary skill in the art that the embodiments may be practiced without these specific details. For example, circuits may be shown in block diagrams in order not to obscure the embodiments in unnecessary detail. In other instances, well-known circuits, processes, algorithms, structures, and techniques may be shown without unnecessary detail in order to avoid obscuring the embodiments.

[0020] Also, it is noted that the embodiments may be described as a process which is depicted as a flowchart, a flow diagram, a data flow diagram, a structure diagram, or a block diagram. Furthermore, embodiments may be implemented by hardware, software, firmware, middleware, microcode, hardware description languages, or any combination thereof. When implemented in software, firmware, middleware or microcode, the program code or code segments to perform the necessary tasks may be stored in a machine readable medium such as storage medium. Information, arguments, parameters, data, etc. may be passed, forwarded, or transmitted via any suitable means including memory sharing, message passing, token passing, network transmission, etc.

[0021] In one embodiment, the present disclosure provides a multiple-input receiver for processing one or more communication signals. The receiver includes a decoder, which may be implemented as a bank of demodulators and a bank of decoders, and a decision logic that routes the signals from appropriate demodulators to the decoders. Each demodulator can implement a different demodulator algorithm. In one embodiment, the first demodulator algorithm can incorporate interference nulling, whereas the second demodulator algorithm can assume no knowledge of interference. The decoder may include multiple decoders, for example, turbo-decoders. The turbo-decoders can be used to decode symbols from one or more demodulators. The decision logic determines which demodulator outputs need to be routed to turbo-decoders, based upon one or more metrics. These metrics can be for example, the available turbo-decoder resources and/or power consumption considerations.

[0022] One embodiment is used for robust demodulation and decoding of small packets. This is explained as follows for one embodiment. Typically, the modem turbo-decoder resources (i.e., the number of turbo-decoders) is sized to

decode peak-data rates. However, several applications (e.g., VoIP, control messages) have small packets that need only to use some of the turbo-decoder resources. In this scenario, there are turbo-decoders available for multiple hypothesis decoding. Specifically, symbols from multiple demodulators can be routed to independent turbo-decoders, for multiple-hypothesis decoding. One demodulator can be based on interference nulling, whereas a second demodulator can be based on a vanilla MMSE receiver without interference nulling. If interference estimates are not accurate, the latter algorithm can in-fact perform better than the former algorithm. In this sense, multiple-hypothesis decoder ensures that the optimum demodulator is selected.

[0023] In another application where an embodiment is used, the multiple input multiple output (MIMO) transmitter may chose to change the forward link (FL) MIMO rank due to changing channel scenario or packet-sizes. Typically, the transmitter indicates the change of rank using a FL assignment control message. In a fully loaded system, this may not be possible due to lack of FL control resources. To enable the receiver to demodulate the FL transmitted signal, the demodulator may consist of multiple demodulators, where each demodulator can be a minimum mean squared error (MMSE) receiver assuming a different rank (e.g., ranks 1, 2, 3, or 4). The signals from each demodulator can be sent to independent decoders, for multiple hypothesis decoding.

[0024] Referring initially to FIG. 1, a block diagram of an embodiment of a multiple-access communication system 100 is shown. In this embodiment, cells 104 divide up the geography for multiple base stations 142. One or more access terminals 132 can move between cells and still communicate using the multiple-access communication system 100. Although only two cells 104 are shown in this embodiment, there could be many more cells 104. Some cells could be for base stations not associated with the multiple-access system 100 and use overlapping frequencies, coding and/or other common elements of the transmission media.

[0025] In the depicted example, an access terminal is approaching the cell boundary of a second cell 104-2 and is receiving signals from a first cell 104-1. As the access terminal is currently associated with a second base station 142-2, the signal from a first base station 142-1 is considered interference. Interference can reduce the efficiency of the transmission media, among other things.

[0026] Interference is a common situation in wireless communication. Interference can occur when two different systems or base stations use the same or overlapping wireless media. In the depicted system 100, two base stations 142 are in range of the same access terminal 132 such that the intended channel, H, receives some interference from an unintended channel, for example from neighboring cell 104-1. Other sources of interference (not shown) could be from systems that use overlapping frequencies, coding and other transmission media components. Similar problems can be encountered at the base station 142, such that interference nulling (IN) can be helpful in both the base station 142 and the access terminal 132.

[0027] Interference nulling can give improved performance in most situations where there are multiple receive antennas, but can perform worse where there is a poor estimate of interference statistics. IN uses an estimation of

the interference covariance matrix (R_{nn}) and/or the received covariance matrix (R_{yy}). There can be a loss in performance for a MMSE demodulator when R_{nn} and/or R_{yy} are inaccurate. For example, in the case of single input single output (SISO) transmission, maximum ratio combining (MRC) demodulator without IN can outperform a MMSE demodulator with interference nulling, when the interference statistics are inaccurately estimated.

[0028] With reference to FIG. 2A, a block diagram of an embodiment of a base station 142 and an access terminal 132 having a MIMO configuration is shown. The base station 142 communicates with multiple antennas 224. Similarly, the access terminal 132 communicates with multiple antennas 252. At the base station 142, traffic data for a number of data streams is provided from a data source 212-1 to a transmit (TX) MIMO data processor 214. In one embodiment, each data stream is transmitted over a respective base station antenna 224.

[0029] The TX MIMO data processor 214 formats, codes and interleaves the traffic data for each data stream based upon a particular coding scheme selected for that data stream to provide coded data. In some embodiments, the TX MIMO data processor 214 applies beamforming weights to the symbols of the data streams based upon the access terminal 132 to which the symbols are being transmitted and the antenna 224 from which the symbol is being transmitted. Although, this may not be utilized and may be transmission dependent.

[0030] The coded data for each data stream may be multiplexed with pilot data using orthogonal frequency division multiplexing (OFDM) techniques. The pilot data is typically a known data pattern that is processed in a known manner and may be used at the access terminal 132 to estimate the channel response. The multiplexed pilot and coded data for each data stream is then modulated (i.e., symbol mapped) based on a particular modulation scheme (e.g., BPSK, QPSK, M-PSK or M-QAM) selected for that data stream to provide modulation symbols. The data rate, coding and modulation for each data stream may be determined by instructions performed on a processor 270-1. In some embodiments, the number of parallel spatial streams may be varied according to the rank information that is transmitted from the access terminal 132.

[0031] The modulation symbols for all data streams are then provided to a TX MIMO processor 220 that may further process the modulation symbols (e.g., for OFDM). The TX MIMO processor 220 then provides N_T symbol streams to N_T transmitters/receivers (TMTR/RCVR) 222. Each TMTR 222 receives and processes a respective symbol stream to provide one or more analog signals, and further conditions (e.g., amplifies, filters, upconverts, etc.) the analog signals to provide a modulated signal suitable for transmission of the MIMO channel, H . N_T modulated signals from the N_T TMTRs 222 are then transmitted from N_T antennas 224, respectively.

[0032] At the access terminal 132, the transmitted modulated signals are received by N_R antennas 252 and the received signal from each antenna 252 is provided to a respective receiver/transmitter (RCVR/TMTR) 254. Each RCVR 254 conditions (e.g., filters, amplifies, downconverts, etc.) a respective received signal, digitizes the conditioned signal to provide samples, and further processes the samples to provide a corresponding received symbol stream.

[0033] An RX MIMO processor 260 then receives and processes the N_R received symbol streams from N_R receivers 254 based upon a particular receiver processing technique to provide the rank number of detected symbol streams. Each detected symbol stream includes symbols that are estimates of the modulation symbols transmitted for the corresponding data stream. The RX MIMO processor 260 then demodulates, deinterleaves and decodes each detected symbol stream to recover the traffic data for the data stream. Demodulating can be done with varying redundant demodulating algorithms to use the best algorithm for a particular set of conditions. The processing of the RX MIMO processor 260 is generally complementary to that performed by the TX MIMO data processor 214 and the TX MIMO processor 220 at the base station 142.

[0034] At the access terminal 132, various processing techniques may be used to process the N_R received signals to detect the N_T transmitted symbol streams. The access terminal processing techniques may be grouped into two primary demodulating categories: (i) spatial and space-time receiver processing techniques (i.e., equalization techniques), and (ii) "successive nulling/equalization and interference cancellation" receiver processing techniques (i.e., "successive interference cancellation" or "successive cancellation" receiver processing techniques).

[0035] A MIMO channel formed by the N_T transmit and N_R receive antennas may be decomposed into N_S independent channels, with $N_S \leq \min \{N_T, N_R\}$. Each of the N_S independent channels may also be referred to as a spatial subchannel (or a transmission channel) of the MIMO channel and corresponds to a dimension.

[0036] If the rank equals one (i.e., a SISO transmission), the access terminal 132 implements both a maximum ratio combining MRC receiver and a MMSE with IN receiver. The soft information generated from both RCVRs 254 is sent to independent turbo-decoders operated by or part of the processor 270. The transmitted bits are obtained from the turbo-decoder upon successful decoding and passing a cyclic redundancy check (CRC) or other validity check. Note that if the interference nulling gains are obtained, then the turbo-decoder corresponding to interference nulling receiver will result in a "earlier decode" and vice-versa. The same concept can be extended to MIMO receivers with/without IN gains.

[0037] Other functions are performed by the base station 142 and access terminal 132. A data source 212-2 of the access terminal 132 passes through a TX data processor 278, a modulator 280, and a TMTR 254 before passing over the wireless media to the base station 142. Upon passing the wireless media over the channel matrix, H , the signal passes antennas 224, RCVRs 222, a demodulator 240, and a receiver (RX) data processor 242 to a data sink 244-1. A first path from the access terminal data source 212-2 to the base station data sink 244-1 can use the same multiple-hypothesis decoding as a second path from the base station data source 212-1 to the access terminal data sink 244-2.

[0038] Referring next to FIG. 2B, a block diagram of an embodiment of a base station 142 and an access terminal 132 having a SIMO configuration is shown. The base station in this embodiment uses TX SIMO data processor 216 and a TX SIMO processor 218 to drive a single TMTR 222 and antenna 224. The access terminal 132 has multiple antennas

252 and RCVRs 254 that are processed by a RX SIMO data processor 262. The forward link from the base station 142 to the access terminal 132 has N_s independent channels that are equal in number to the N_r receive antennas 252. The access terminal 132 in this embodiment may be operable in both SIMO and MIMO configurations.

[0039] With reference to FIG. 3A, a block diagram of an embodiment of a portion 300-1 of the access terminal 132 that shows demodulation and decoding of received channels is depicted. The N_r received signals (corresponding to the receive antennas 252) are passed to three different demodulators 304, 308, 312 in this embodiment, which each demodulate the received signals in parallel. A demodulator selector 316 uses a metric to decide which of the demodulators 304, 308, 312 to pass to a turbo decoder 320, here depicted as a bank of decoders, for decoding.

[0040] This embodiment uses MRC and MMSE demodulators 304, 308 that use an equalization technique and a MMSE demodulator with IN 312 that uses a successive cancellation technique. Some embodiments could use only one equalization technique and one successive cancellation technique, but other embodiments could use any number of equalization and successive cancellation techniques. The equalizer function for the MRC demodulator 304, $G=H^*$, and the MMSE demodulator 308, $G=H^*(HH^*+\sigma^2I)^{-1}$, and the successive cancellation function for MMSE with interference nulling 312, $G=H^*(HH^*+R_{nn})^{-1}$, are all performed in parallel on the same received signals.

[0041] The demodulator selector 316 in this embodiment determines the demodulator 304, 308, 312 that is likely to produce the most accurate demodulated signal. A metric is used by the demodulator selector 316 in the selection, for example, power consumption and turbo-decoder resources available. The turbo-decoder resources available is a function of FL data rate—for small data rates more resources are available, and vice-versa for large data rates. After selection, one or more outputs from the demodulators 304, 308, 312 is passed to the turbo decoding 320 for decoding.

[0042] The decoding is performed with a turbo decoding 320. Other embodiments could use another convolutional decoder or any decoder once the initial demodulating is performed. The turbo decoder 320 can be used to decode a single signal or multiple signals depending on loading of the turbo decoder 320. For example, the turbo decoder 320 may have additional resources available when frame size is small.

[0043] In some cases, another metric can be used to determine before processing by the demodulators 304, 308, 312 the demodulator that is likely to produce the best result. The demodulator 304, 308, 312 unlikely to produce the best result can be disabled and powered-down for those situations whether this metric is determined to be accurate. For those situations where the favored demodulator 304, 308, 312 cannot be predetermined, the demodulator selector 316 makes that determination with all demodulators activated. A mix of culling the demodulators 304, 308, 312 used with the enable signal and choosing from the culled demodulators with the demodulator selector 316 can be done in some embodiments.

[0044] Referring next to FIG. 3B, a block diagram of an embodiment of a portion 300-2 of the access terminal 132 that shows demodulation and decoding of received channels

is depicted. In this embodiment, all three demodulators 304, 308, 312 perform their demodulating before passing their results to the turbo decoder 320. Resources are available in the turbo decoder 320 to handle all three streams in parallel.

[0045] After decoding in the turbo decoder 320, all three alternatives are presented to a stream selector 324. A validity check of some sort (e.g., CRC, parity, decryption, etc.) is performed in the stream selector to choose which stream is used. Where all streams pass the validity check, a voting algorithm finds the stream that was presented most often and that is chosen by the stream selector 324.

[0046] The validity check may be binary for a packet such that one decision is made per packet, but could be performed on sub-packet portions such that portions independently pass or fail the validity check. The packet could be reformulated from portions of one stream or another based upon the piecemeal validity checking.

[0047] With reference to FIG. 3A, a block diagram of an embodiment of a portion 300-1 of the access terminal 132 that shows demodulation and decoding of received channels is depicted. In this embodiment, three different methods can be used to choose the demodulator 304, 308, 312 to use. Different demodulators 304, 308, 312 can be deactivated with the enable signal. The demodulator selector 316 can choose from the remaining ones or not. Where there are adequate resources in the turbo decoder 320, the most likely to be used streams are decoded in the turbo decoder 320. The stream selector 324 chooses from any alternatives using a validity check.

[0048] An example can illustrate the interaction of the three different choosing methods. It could be determined that interference is unlikely such that the MMSE demodulator with IN 312 is deactivated. The MRC and MMSE demodulators 304, 308 produce a demodulated stream to the demodulator selector 316 who determines that adequate resources exist in the turbo decoder 320 such that no culling is performed by the demodulator selector 316. After turbo decoding, the stream selector may find that one or both pass the validity check. One that passed is chosen for use.

[0049] Referring next to FIG. 4A, the present disclosure provides an embodiment a method 400-1 for demodulating and decoding one or more communication signals that are received in block 404. The one or more communication signals are demodulated using a first algorithm in block 408-1, where the first algorithm includes interference nulling. The one or more communication signals are also demodulated using a second algorithm in block 408-2, which is different from the first algorithm. A decision is made between the first and second algorithms in block 416 based upon a metric determined in block 412 to produce a demodulated signal. The demodulated signal is decoded in block 420 to produce a decoded signal.

[0050] With reference to FIG. 4B, the present disclosure further discloses another embodiment of a method 400-2 for demodulating and decoding one or more communication signals received in block 404. The one or more communication signals is demodulated using a first algorithm to produce a first demodulated signal in block 408-1. The one or more communication signals is also demodulated using a second algorithm to produce a second demodulated signal in block 408-2, where the first algorithm is different from the

second algorithm. The first demodulated signal is decoded to produce a first decoded signal in block **410-1**, and the second demodulated signal is decoded to produce a second decoded signal in block **410-2**. A decision is made in block **420** between the first and second decoded signal based upon validity checks of the first and second decoded signals in block **416**.

[0051] Referring next to FIG. 5A, a block diagram illustrates an embodiment of a multiple-input receiver **500-1** for demodulating and decoding one or more signals. The multiple-input receiver **500-1** includes first and second means for demodulating **504**, **512**, means for deciding **514**, and means for decoding **522**. The first means **504** demodulates the one or more communication signals using a first algorithm that uses interference nulling. The second means **512** demodulates the one or more communication signals using a second algorithm, which is different from the first algorithm. The means for deciding **514** chooses between the first and second algorithm based upon a metric to produce a demodulated signal. The means for decoding **522** the demodulated signal produces a decoded signal.

[0052] Referring next to FIG. 5B, a block diagram illustrates an embodiment of a multiple-input receiver **500-2** for demodulating and decoding one or more signals. The multiple-input receiver **500-2** includes first and second means for demodulating **504**, **512**, first and second means for decoding **520**, and means for deciding **524**. The first means for decoding **504** the one or more communication signals uses a first algorithm to produce a first demodulated signal. The second means for demodulating **512** the one or more communication signals uses a second algorithm to produce a second demodulated signal, where the first algorithm is different from the second algorithm. The first means for decoding **520** the first demodulated signal produces a first decoded signal. The second means for decoding **520** the second demodulated signal produces a second decoded signal. The means for deciding **524** between the first and second decoded signal chooses based upon validity checks of the first and second decoded signals.

[0053] The techniques, blocks, steps and means described herein may be implemented by various means. For example, these techniques, blocks, steps and means may be implemented in hardware, software, or a combination thereof. For a hardware implementation, the processing units within an access point or an access terminal may be implemented within one or more application specific integrated circuits (ASICs), digital signal processors (DSPs), digital signal processing devices (DSPDs), programmable logic devices (PLDs), field programmable gate arrays (FPGAs), processors, controllers, micro-controllers, microprocessors, other electronic units designed to perform the functions described herein, or a combination thereof.

[0054] For a software implementation, the techniques described herein may be implemented with instructions (e.g., procedures, functions, modules, and so on) that perform the functions described herein. The instructions may be stored in memory units, or other memory, and executed by processors. The memory may be implemented within the processor or external to the processor, in which case it can be communicatively coupled to the processor via various means as is known in the art.

[0055] While the principles of the disclosure have been described above in connection with specific apparatuses and

methods, it is to be clearly understood that this description is made only by way of example and not as limitation on the scope of the disclosure.

What is claimed is:

1. A multiple-input receiver comprising:

- a first demodulator configured to use a first algorithm, including interference nulling;
- a second demodulator, wherein the second demodulator uses a second algorithm, which is different from the first algorithm;
- a decoder configured to be alternatively used with the first or second demodulator to decode one or more signals from the receiver; and

decision logic configured to choose the first demodulator or the second demodulator.

2. The multiple-input receiver as recited in claim 1, wherein:

the decoder is configured to produce the decoded signal using the first demodulator,

the decoder is configured to produce a second decoded signal using the second demodulator, and

the decision logic is configured to choose either the decoded signal or the second decoded signal based upon validity tests of decoded bits of the decoded signal and second decoded bits of the second decoded signal.

3. The multiple-input receiver as recited in claim 1, wherein the decoder uses an algorithm chosen from a group consisting of a convolutional, low density parity check (LDPC) or turbo-decoding algorithm.

4. The multiple-input receiver as recited in claim 1, wherein the first and second algorithms are chosen from the group consisting of maximum ratio combining (MRC) demodulating and minimum mean squared error (MMSE) demodulating.

5. The multiple-input receiver as recited in claim 1, wherein the decoder is configured to use a turbo decoding algorithm.

6. The multiple-input receiver as recited in claim 1, further comprising a plurality of antenna inputs that each receive the one or more communication signals.

7. The multiple-input receiver as recited in claim 1, wherein interference nulling is a process that removes unwanted information from the one or more communication signals.

8. The multiple-input receiver as recited in claim 1, wherein the decision logic is configured to choose based upon a metric.

9. The multiple-input receiver as recited in claim 8, wherein the metric is a signal to interference-plus-noise ratio (SINR) for the first or second demodulator.

10. A multiple-input receiver comprising:

first means for demodulating one or more communication signals using a first algorithm, wherein the first algorithm includes interference nulling;

second means for demodulating the one or more communication signals using a second algorithm, which is different from the first algorithm;

means for deciding between the first and second algorithm to produce a demodulated signal; and

means for decoding the demodulated signal to produce a decoded signal.

11. The multiple-input receiver as recited in claim 10, wherein the means for deciding comprises means for deciding between a decoded signal and a second decoded signal, wherein the decoded signal uses the first algorithm and the second decoded signal uses the second algorithm.

12. The multiple-input receiver as recited in claim 10, wherein the second algorithm is chosen from the group consisting of maximum ratio combining (MRC) demodulation and minimum mean squared error (MMSE) demodulation.

13. The multiple-input receiver as recited in claim 10, wherein the means for deciding comprises means for deciding based upon a metric.

14. The multiple-input receiver as recited in claim 13, wherein the metric is a signal to interference-plus-noise ratio (SINR).

15. The multiple-input receiver as recited in claim 10, wherein interference nulling removes signals from the one or more communication signals.

16. The multiple-input receiver as recited in claim 10, wherein the first algorithm includes a successive cancellation receiver processing technique.

17. The multiple-input receiver as recited in claim 10, wherein means for deciding comprises means for deciding using a validity check

18. A method for processing one or more communication signals, the method comprising:

demodulating the one or more communication signals using a first algorithm, wherein the first algorithm includes interference nulling;

demodulating the one or more communication signals using a second algorithm, which is different from the first algorithm;

deciding between the first and second algorithm to produce a demodulated signal; and

decoding the demodulated signal to produce a decoded signal.

19. The method for processing the one or more communication signals as recited in claim 18, further comprising determining to not demodulate the one or more communication signals using a third algorithm.

20. The method for processing the one or more communication signals as recited in claim 18, wherein the second algorithm does not perform interference nulling.

21. The method for processing the one or more communication signals as recited in claim 18, wherein the first algorithm includes a successive cancellation receiver processing technique.

22. The method for processing the one or more communication signals as recited in claim 18, wherein deciding comprises deciding using a validity check.

23. The method for processing the one or more communication signals as recited in claim 18, wherein deciding comprises deciding based upon a metric.

24. The method for processing the one or more communication signals as recited in claim 23, wherein the metric is a signal to interference-plus-noise ratio (SINR).

25. A processor readable medium comprising instructions thereon that may be utilized by one or more processors, the instructions comprising:

instructions for demodulating one or more communication signals using a first algorithm, wherein the first algorithm includes interference nulling;

instructions for demodulating the one or more communication signals using a second algorithm, which is different from the first algorithm;

instructions for deciding between the first and second algorithm based upon a metric to produce a demodulated signal; and

instructions for decoding the demodulated signal to produce a decoded signal.

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