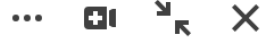


New message



Jonathan Charles X



Jonathan Charles ✓ · 1st

VC @ Samsung Catalyst Fund | Kauffman Fellow | X-Apple

APR 27, 2019



Shih-Yuan (SY) Wang · 12:27 PM

Dear JC,

I was referred to connect with you from James Schuessler.

W&Wsens Devices Inc. based in Los Altos California, has developed Silicon and Germanium on Silicon Photodetectors compatible with CMOS technology for high speed, high efficiency optical data communications. Our technology is



New message



Jonathan Charles X



efficiency optical data communications. Our technology is also applicable to monolithically integrated LiDAR solutions (time-of-flight photo-sensor / 3D imaging). We are currently engaging in M&A discussions and are being represented by Bank of America Merrill Lynch headed by Marco Chisari. Please see the attached deck for an outline of our technology and intellectual property portfolio. Feel free to reach out to any of the members listed below to schedule an initial phone conference.

Best Regards,

SY Wang
CTO & Co-Founder
W&Wsens Devices Inc.
4546 El Camino, Suite 215
Los Altos, CA 94022



New message



Jonathan Charles X



sywang@me.com
www.wwsensdevcies.com

Marco Chisari
Global Co-Head, Semiconductor Investment Banking
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3075B Hansen Way
Palo Alto, CA 94304
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M 408.212.1441

Lihan Wang
Vice President Strategy & Business Development
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4546 El Camino, Suite 215
Los Altos CA 94022
lihanw@gmail.com



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W&Wsens_intro_deck_li...

3 MB

APR 28, 2019



Jonathan Charles • 7:05 PM

SY, thanks for the info. I'll share it with the corp dev team that works on automotive related things. They will reach out if there is any interest.

Regards,

JC

AUG 14, 2019



New message



Jonathan Charles X



AUG 14, 2019



Shih-Yuan (SY) Wang • 5:09 PM



Attached below are 3 decks (DCI, LiDAR, Integration) outlining W&Wsens Devices break through technology of high speed AND high efficiency Silicon PD / APD using light trapping. Our thin Si PD / APD is fully compatible with standard CMOS / BiCMOS manufacturing enabling single chip integration to reduce costs and improve performance. We would like to meet with you to discuss W&W break through technology further.

Best,

SY Wang
CTO & Co-Founder
W&Wsens Devices Inc.



- Dear JC, I was referred to connect with you from James Schuessler. W&Wsens Devices Inc. based in Los Altos California, has developed Silicon and Germanium on Silicon Photodetectors compatible with CMOS technology for high speed, high efficiency optical data communications. Our technology is also applicable to monolithically integrated LiDAR solutions (time-of-flight photo-sensor / 3D imaging). We are currently engaging in M&A discussions and are being represented by Bank of America Merrill Lynch headed by Marco Chisari. Please see the attached deck for an outline of our technology and intellectual property portfolio. Feel free to reach out to any of the members listed below to schedule an initial phone conference. Best Regards, SY Wang CTO & Co-Founder W&Wsens Devices Inc. 4546 El Camino, Suite 215 Los Altos, CA 94022 sywang@me.com www.wwsensdevcies.com Marco Chisari Global Co-Head, Semiconductor Investment Banking Bank of America Merrill Lynch 3075B Hansen Way Palo Alto, CA 94304 marco.chisari@baml.com M 408.212.1441 Lihan Wang Vice President Strategy & Business Development W&Wsens Devices Inc. 4546 El Camino, Suite 215 Los Altos CA 94022 lihanw@gmail.com M 650.704.1273

[W&Wsens intro deck lidar 3 28 compressed.pdf](#)

[3 MB](#)

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- APR 28, 2019 Jonathan Charles sent the following message at 7:05 PM



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[Jonathan Charles](#) 7:05 PM

SY, thanks for the info. I'll share it with the corp dev team that works on automotive related things. They will reach out if there is any interest. Regards, JC



- AUG 14, 2019 Shih-Yuan (SY) Wang sent the following messages at 5:09 PM



[View Shih-Yuan \(SY\)'s profile](#)

[Shih-Yuan \(SY\) Wang](#) 5:09 PM

Attached below are 3 decks (DCI, LiDAR, Integration) outlining W&Wsens Devices break through technology of high speed AND high efficiency Silicon PD / APD using light trapping. Our thin Si PD / APD is fully compatible with standard CMOS / BiCMOS manufacturing enabling single chip integration to reduce costs and improve performance. We would like to meet with you to discuss W&W break through technology further. Best, SY Wang CTO & Co-Founder W&Wsens Devices Inc. 4546 El Camino, Suite 215 Los Altos, CA 94022 sywang@me.com www.wwsensdevices.com Lihan Wang Vice President Strategy & Business Development W&Wsens Devices Inc. 4546 El Camino, Suite 215 Los Altos CA 94022 lihanw@gmail.com M 650.704.1273

[**presentation deck LiDAR 7 10 compressed.pdf**](#)

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WWsens CIS-ToF deck

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To: joncharles01@gmail.com, Cc: Shih-Yuan Wang

Jonathan,

W&W is the first to demonstrate high efficiency and low jitter in thin silicon photosensors using photon / light trapping technology that is fully compatible with current state of the art CMOS manufacturing.

Extremely low jitter in the 10s of pico seconds allows for high depth resolution in the millimeter range for applications such as 3D imaging and AR/VR and robotics.

W&W has developed an extensive global IP portfolio for light trapping technology in thin silicon photosensors such as PD / APD / SPAD – please see page 6 of our presentation deck.

Lastly, our light trapping technology to enhance IR sensitivity in thin silicon has been corroborated by Sony – paper attached.

Thanks, and we look forward to further discussions with your team.

Best,
Lihan



presentation_de
ck_CIS...F_c.pdf



IR sensitivity
enhanc...s 2.pdf

Lihan Wang
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