

I. AGREED CONSTRUCTION

Term No.	Term	Agreed Construction
1	Whether the preamble of claim 1 of U.S. Patent No. 7,930,575 is limiting. “microcontroller”	The preamble of claim 1 is limiting.

II. DISPUTED CONSTRUCTIONS

Term No.	Term	Plaintiff's Proposed Construction	Defendant's Proposed Construction	Court's Final Construction
1	“microcontroller” U.S. Patent No. 7,930,575, claim 1	“An integrated circuit”	“plain and ordinary meaning, a chip used in control applications rather than data-processing that has a complete CPU, semiconductor memory sufficient for control applications, and input/output capability[, that chip includes]” OR “a system on a chip designed for control applications rather than data-processing” (Alternative based on past <i>Markman</i> hearing)	Plain and ordinary meaning
2	“CPU” U.S. Patent No. 7,930,575, claim 1	Plain and ordinary meaning, “processing units”	“Processing units and associated temporary storage”	Plain and ordinary meaning, wherein a “CPU” includes “processing units”
3	“power supply device” U.S. Patent No. 7,930,575, claim 1	“Power supply”	“External power supply”	“Power supply”

4	“power supply unit” U.S. Patent No. 7,930,575, claim 1, 16	Not subject to 35 U.S.C. § 112, para. 6. “Power switch switching power supplied by the power supply device.”	Subject to 35 U.S.C. § 112, para. 6. <u>Function:</u> supplying power <u>Structure:</u> voltage regulator	Not subject to 35 U.S.C. § 112, para. 6; plain and ordinary meaning, wherein the power switch is not a part of the power supply unit
5	“power supply control unit” U.S. Patent No. 7,930,575, claim 1, 3, 16	Not indefinite. Not subject to 35 U.S.C. § 112, para. 6. “power supply control unit” describes structure to a POSITA. Plain and ordinary meaning, no construction required.	Subject to 35 U.S.C. § 112, para. 6. <u>Function:</u> power supply control <u>Structure:</u> indefinite	Not subject to 35 U.S.C. § 112, para. 6; plain and ordinary meaning
6	“the power supply control unit limits a period in which shutdown is possible” U.S. Patent No. 7,930,575, claim 3	Not indefinite. Not subject to 35 U.S.C. § 112, para. 6. “power supply control unit” describes structure to a POSITA. The power supply control unit sets the time in which shutdown is possible.	Subject to 35 U.S.C. § 112, para. 6. <u>Function:</u> power supply control that limits a period in which shutdown is possible <u>Structure:</u> indefinite	Not subject to 35 U.S.C. § 112, para. 6; plain and ordinary meaning
7	“information holding unit” U.S. Patent No. 7,930,575, claim 1	Not subject to 35 U.S.C. § 112, para. 6. Plain and ordinary meaning, no construction necessary.	Subject to 35 U.S.C. § 112, para. 6. <u>Function:</u> holding information <u>Structure:</u> built-in RAM external to the CPU	Not subject to 35 U.S.C. § 112, para. 6; plain and ordinary meaning

8	“restores the information necessary in proceeding with the program and evacuated in the information holding unit in time of power shutdown” U.S. Patent No. 7,930,575, claim 1	“Restores the information necessary to proceed with the program, the information was evacuated from the CPU in the information holding unit in the power shut down process”	“Restores from the information holding unit the information necessary to proceed with the program, the information was stored in the information holding unit during power shut down”	“Restores the information necessary to proceed with the program, the information was evacuated from the CPU in the information holding unit in the power shut down process”
9	“the plurality of microprograms” U.S. Patent No. 7,930,575, claim 7	Not indefinite. The antecedent basis for the plurality of microprograms is: “a microprogram for restoring the evacuated information necessary in proceeding with the program and proceeding with the program; a microprogram for restoring a set value without using the evacuated information necessary in proceeding with the program;” No construction necessary.	Indefinite	Indefinite

10	“a program for resetting processes, and the CPU selects one of the plurality of microprograms in an alternative way” U.S. Patent No. 7,930,575, claim 7	Not indefinite. Plain and ordinary meaning: “a program for resetting processes, and the CPU selects one of [a microprogram for restoring the evacuated information necessary in proceeding with the program and proceeding with the program or a microprogram for restoring a set value without using the evacuated information necessary in proceeding with the program] in an alternative way.”	Indefinite	Indefinite
11	“a circuit for loading an inverted value to a register in a circuit portion in a power shutdown range” U.S. Patent No. 7,930,575, claim 12	Not indefinite “a circuit for loading opposite Boolean values to a register in a circuit portion, the opposite Boolean values being in a range for power shutdown”	Indefinite	Not indefinite; plain and ordinary meaning