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**Re: *United Microelectronics Corp. and UMC Group (USA) v. Advanced Integrated Circuit Process LLC*, IPR2025-01079 for US Patent No. 7,923,764;
Advanced Integrated Circuit Process LLC v. United Microelectronics Corp., Civil Action No. 2:24-cv-00730 (E.D. Tex.) (Lead Case);
Advanced Integrated Circuit Process LLC v. Taiwan Semiconductor Manufacturing Co. Ltd., Civil Action No. 2:24-cv-00623 (E.D. Tex.) (Consolidated Member Case)
TPL Ref: 632329.000004**

Dear Counsel:

We write regarding a petition for *inter partes* review ("IPR") filed June 2, 2025 (Paper 1) with the United States Patent and Trademark Office, Patent Trial and Appeal Board ("PTAB") in IPR2025-01079 (the "Petition") to challenge claims 1-6 and 11-19 (the "Challenged Claims") of U.S. Patent No. 7,923,764. The table below lists the grounds asserted by United

Microelectronics Corporation and UMC Group (USA) (collectively, “Petitioner”) in the Petition, along with the Challenged Claims against which the grounds are asserted. Petitioner hereby stipulates that in the event (a) the PTAB institutes an IPR in response to the Petition, and does not subsequently vacate institution or (b) in response to the petition filed by Taiwan Semiconductor Manufacturing Co. Ltd. (“TSMC”) against U.S. Patent No. 7,923,764 (IPR2025-00829), the PTAB institutes an IPR and the instituted IPR is not subsequently vacated or terminated before Final Written Decision, Petitioner will not pursue in the subject parallel litigation in U.S. District Court (the “Litigation”) invalidity of the Challenged Claims based on:

1. the grounds listed in the below table against the corresponding claims listed in the below table for those grounds, as identified in the Petition,
2. any other grounds that could have reasonably been raised before the PTAB in IPR2025-01079 (i.e., any ground that could have reasonably been raised under pre-AIA §102 or §103 on the basis of prior art patent(s) or prior art printed publication(s)), or
3. any other invalidity ground under pre-AIA §102 or §103 involving any type of legally recognized prior art, including patent, printed publication, or system art.

Ground	Basis	Claims	Reference(s) in IPR2025-01079
I	§ 103	1, 4, 5, and 14-19	U.S. Patent Application Publication No. 2003/0025135 A1 (“Matsumoto”) (Ex. 1009)
II	§ 103	2, 3, 6, and 11	U.S. Patent Application Publication No. 2003/0025135 A1 (“Matsumoto”) (Ex. 1009) and U.S. Patent Application Publication No. 2006/0131672 A1 (“Wang”) (Ex. 1012)
III	§ 103	11	U.S. Patent Application Publication No. 2003/0025135 A1 (“Matsumoto”) (Ex. 1009), U.S. Patent Application Publication No. 2006/0131672 A1 (“Wang”) (Ex. 1012), and (“Sim”) (Ex. 1024)
IV	§ 103	2 and 3	U.S. Patent Application Publication No. 2003/0025135 A1 (“Matsumoto”) (Ex. 1009) and U.S. Patent Application Publication No. 2005/0045938 A1 (“Mutou”) (Ex. 1019)
V	§ 103	6, 12, and 13	U.S. Patent Application Publication No. 2003/0025135 A1 (“Matsumoto”) (Ex. 1009) and U.S. Patent Application Publication No. 2005/0051856 A1 (“Ono”) (Ex. 1013)
VI	§ 103	15	U.S. Patent Application Publication No. 2003/0025135 A1 (“Matsumoto”) (Ex. 1009) and U.S. Patent Application Publication No. 2004/0110352 (“Bu”) (Ex. 1025)
VII	§ 103	18 and 19	U.S. Patent Application Publication No. 2003/0025135 A1 (“Matsumoto”) (Ex. 1009) and Wilk, G.D., et al. “High-k gate

Ground	Basis	Claims	Reference(s) in IPR2025-01079
			dielectrics: Current status and materials properties considerations" ("Wilk") (Ex. 1018)
VIII	§ 102	1, 5, 16 and 17	JP2003-258241 ("Kajiyama") (Ex. 1010/1011)
IX	§ 103	4, 6, 18, and 19	JP2003-258241 ("Kajiyama") (Ex. 1010/1011)
X	§ 103	11	JP2003-258241 ("Kajiyama") (Ex. 1010/1011) and Wang (Ex. 1012)
XI	§ 103	11	JP2003-258241 ("Kajiyama") (Ex. 1010/1011) and Sim, J.H., et al., "Effects of ALD Hfo2 thickness on charge trapping and mobility," Microelectronic Engineering, Vol. 80, pp. 218-221, June 17, 2005 ("Sim") (Ex. 1024)
XII	§ 103	12 and 13	JP2003-258241 ("Kajiyama") (Ex. 1010/1011) and Ono (Ex. 1013)
XIII	§ 103	14 and 15	JP2003-258241 ("Kajiyama") (Ex. 1010/1011) and Bu (Ex. 1025)
XIV	§ 103	18 and 19	JP2003-258241 ("Kajiyama") (Ex. 1010/1011) and Wilk (Ex. 1018)

For the sake of clarity, and to avoid any doubt, if the PTAB declines to institute the grounds identified in the above table for whatever reason, Petitioner reserves the right to assert such grounds and all other available arguments for invalidity in the Litigation, including without limitation, under §112.

If institution of the Petition is granted, Petitioner further stipulates that it will submit a parallel stipulation in the Litigation as well as seek a stay of that Litigation pending resolution of the instituted IPR.

Best regards,

/s/ Tim Tingkang Xia

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Mark Hannemann (Defendant Counsel of Record)
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cc: Counsel of Record via email

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