

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

ASUSTEK COMPUTER INC.
Petitioner,

v.

NOKIA TECHNOLOGIES OY
Patent Owner.

Case IPR2025-01153
Patent 10,536,714

**PETITIONER’S UNOPPOSED MOTION FOR *PRO HAC VICE*
ADMISSION OF KYLE J. FLEMING UNDER 37 C.F.R. § 42.10(c)**

PETITIONER'S UPDATED EXHIBIT LIST

Exhibit No.	Description
ASUS-1001	U.S. Patent No. 10,536,714 to Bici, et al. (“the ’714 patent”)
ASUS-1002	Prosecution File History for the ’714 patent (“Prosecution History”)
ASUS-1003	Declaration of Joseph Havlicek (“Havlicek Declaration”)
ASUS-1004	U.S. Patent Application Publication No. 2011/0194609 to Rusert et al (“Rusert”)
ASUS-1005	U.S. Patent Application Publication No. 2011/0249721 to Karczewicz et al (“Karczewicz”)
ASUS-1006	U.S. Patent Application Publication No. 2014/0092981 to Lin et al (“Lin”)
ASUS-1007	Nakamura et al., “Unification of derivation process for merge mode and MVP,” Joint Collaborative Team on Video Coding (JCT-VC) of ITU-T SG16 WP3 and ISO/IEC JTC1/SC29/WG11, 6th Meeting, Torino, Italy, Jul. 14-22, 2011, Document JCTVC-F419 (“Nakamura Document”)
ASUS-1008-1011	[RESERVED]
ASUS-1012	U.S. Provisional Application 61/301,649 (“Rusert Provisional”)
ASUS-1013	U.S. Provisional Application 61/500,903 (“Lin Provisional”)
ASUS-1014	[RESERVED]
ASUS-1015	Prosecution File History for U.S. Patent No. 9,571,833 (“’833 Prosecution History”)
ASUS-1016	[RESERVED]
ASUS-1017	European Prosecution File History for 12845839 (“European Prosecution History”)
ASUS-1018	U.S. Patent Application Publication No. 2012/0128067 to Liu (“Liu”)
ASUS-1019	Gary J. Sullivan, Recent Developments in Standardization of High Efficiency Video Coding (HEVC) (“Sullivan”)
ASUS-1020	Frank Bossen, HEVC Complexity and Implementation Analysis (“Bossen”)
ASUS-1021	Jian-Wei Chen et al, Introduction to H.264 advanced video coding. In: Proceedings of the 2006 Asia and South Pacific Design Automation Conference vol. 2006, pp. 736–741 (2006)
ASUS-1022	Won, Kwanghyun et al, Motion vector coding using

Exhibit No.	Description
	decoder-side estimation of motion vector. In 2009 IEEE International Symposium on Broadband Multimedia Systems and Broadcasting (BMSB). IEEE, 2009
ASUS-1023	Gary J. Sullivan et al, The H.264/AVC Advanced Video Coding Standard: Overview and Introduction to the Fidelity Range Extensions, SPIE Conference on Applications of Digital Image Processing XXVII, 2004
ASUS-1024-1051	[RESERVED]
ASUS-1052	Institution Decision IPR2024-00604, Paper 11, November 7, 2024
ASUS-1053	Institution Decision IPR2024-00605, Paper 11, November 7, 2024
ASUS-1054	August 1, 2025 ASUS Stipulation
ASUS-1055	U.S. Patent No. 9,571,833 to Bici, et al. (“the ’833 patent”)
ASUS-1056	Office Action of August 16, 2021 in U.S. Application No. 16/741,156
ASUS-1057	Amendment of February 16, 2022 in U.S. Application No. 16/741,156
ASUS-1058	Declaration of Kyle J. Fleming in support of Pro Hac Vice motion

Pursuant to 37 C.F.R. § 42.10(c), Petitioner, Asustek Computer Inc. and Asus Computer International (“Petitioner” or “Asus”) respectfully requests that the Board recognize Kyle J. Fleming as counsel *pro hac vice* in this proceeding. Petitioner seeks the counsel of Mr. Fleming due to his experience in representing Asus in this patent-related matter and particularly due to his familiarity with the substantive and technical issues involved in this proceeding. This motion is authorized by the Notice of Filing Date Accorded to Petition and Time for Filing Patent Owner Preliminary Response that was mailed on July 7, 2025.

Patent Owner does not oppose this motion.

Statement of Facts

Mr. Fleming is a patent litigation attorney with more than 5 years of experience representing clients in cases involving, *inter alia*, computer software, consumer products, consumer electronics, semiconductors, and oil and gas equipment. Mr. Fleming regularly litigates patent cases before federal district courts and the International Trade Commission (“the ITC”). Through his practice in such cases, Mr. Fleming has gained substantial experience in the various phases of patent litigation and patent law. Mr. Fleming’s biography is attached as Appendix A of Exhibit 1058.

Mr. Fleming also has particular experience and familiarity with the substantive and technical issues involved in this *inter partes* review proceeding.

For instance, Mr. Fleming serves as trial counsel for Asus in litigation that involves the '714 patent, and, as a result, has gained familiarity with the patent, the prior art references at issue, and general technology related to the '714 patent. Patent Owner has invested significant financial resources in these proceedings, and Mr. Fleming will be taking a leading role with respect to the patent that is the subject of the present proceeding. Through this experience, Asus has developed a particular relationship with Mr. Fleming such that Asus desires to continue the relationship with Mr. Fleming for the purpose of this proceeding.

Declaration of Individual Seeking to Appear

This Motion for *Pro Hac Vice* Admission is accompanied by a Declaration of Kyle J. Fleming. Accordingly, Asus submits that there is good cause under 37 C.F.R. § 42.10(c) for the Board to recognize Mr. Fleming as counsel *pro hac vice* during this proceeding.

Respectfully submitted,

Date: January 28, 2026

/Jeremy J. Monaldo/

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CERTIFICATE OF SERVICE

Pursuant to 37 CFR § 42.6(e)(4), the undersigned certifies that on January 28, 2026, a complete and entire copy of this Petitioner's Motion for Pro Hac Vice and Exhibit 1058 were provided via email, to the Patent Owner by serving the email correspondence addresses of record as follows:

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