

EXHIBIT 37

Exhibit 37 – Claim Chart Showing Molex’s Infringement of U.S. Patent 10,877,233 (“’233 Patent”)

The following chart is based on information known to date. Credo reserves the right to amend and update this chart as additional information is obtained and analyzed. The exemplary claim mappings below rely on images and data from Molex and its suppliers’ public-facing materials. The following chart is representative of Molex’s infringement by its unlawful importation into the United States, sale for importation into the United States, and/or sale within the United States after importation of all the Accused Molex Products.

As outlined in the below claim chart, the Accused Molex Products infringe, either literally or under the doctrine of equivalents, at least Claims 1, 8, and 15 of the ’233 patent.

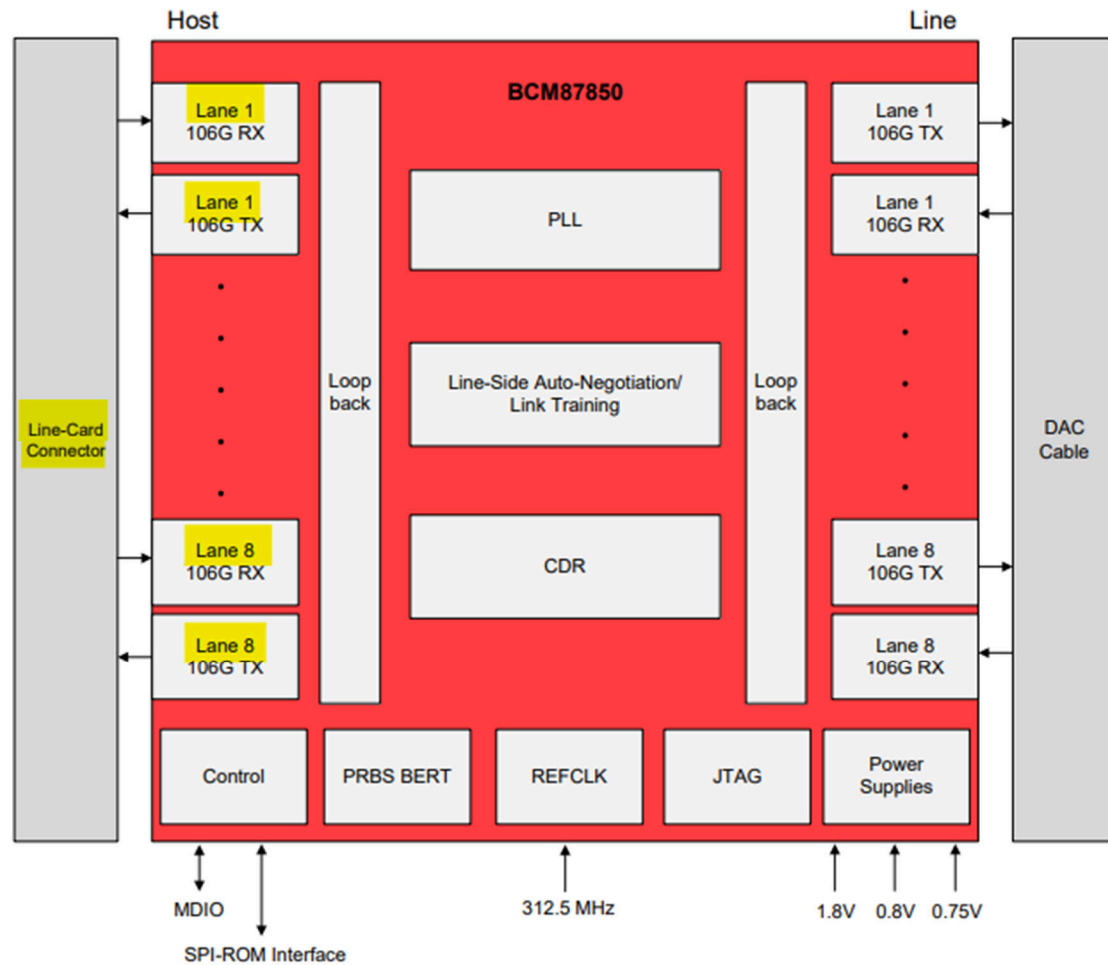
<u>’233 Patent</u>	<u>Accused Molex Products</u>
<u>Claim 1</u>	
[1pre] A cable that comprises:	The Accused Molex Products comprise a cable. See, e.g.: Active Electrical Cable (AEC) OSFP to OSFP Straight Cable with Broadcom BCM 87854 Re-Timer ➤ AECs use re-timers to efficiently extend the reach of copper cables. They deliver superior performance and improved design flexibility while simultaneously reducing costs. Ex. 56, <i>Molex Active Electrical Cable (AEC) OSFP to OSFP Straight Cable with Broadcom BCM 87854 Re-Timer Product Brief</i>. 
[1a] a first data recovery and re-modulation (DRR) device that exchanges inbound and outbound multi-lane data	The Accused Molex Products comprise a first data recovery and re-modulation (DRR) device that exchanges inbound and outbound multi-lane data streams with a first host interface port via a first end connector plug.

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<u>Claim 1</u> streams with a first host interface port via a first end connector plug;	For example, the first DRR device is a first digital signal processor (“DSP”) at one end of the active cable. See, e.g.: Active Electrical Cable (AEC) OSFP to OSFP Straight Cable with Broadcom BCM 87854 Re-Timer ➤ AECs use re-timers to efficiently extend the reach of copper cables. They deliver superior performance and improved design flexibility while simultaneously reducing costs.  Ex. 56, <i>Molex Active Electrical Cable (AEC) OSFP to OSFP Straight Cable with Broadcom BCM 87854 Re-Timer Datasheet</i>. For example, the DSPs in the Accused Molex Products are manufactured by Broadcom and others. The first DSP in the Accused Molex Products is a retimer that exchanges inbound and outbound multi-lane data streams with a first host interface port via a first end connector plug. See, e.g.:

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Claim 1	
	Active Electrical Cable (AEC) OSFP to OSFP Straight Cable with Broadcom BCM 87854 Re-Timer ➤ AECs use re-timers to efficiently extend the reach of copper cables. They deliver superior performance and improved design flexibility while simultaneously reducing costs.  Ex. 56, <i>Molex Active Electrical Cable (AEC) OSFP to OSFP Straight Cable with Broadcom BCM 87854 Re-Timer Datasheet.</i> Active electrical cables powered by Marvell Alaska A PAM4 DSPs are sampling now from Amphenol, Molex and TE Connectivity. Ex. 23, <i>Marvell Announces Availability Of Active Electrical Cables Powered By Its Industry-Leading PAM4 DSP Technology Article, Oct. 20, 2022.</i>

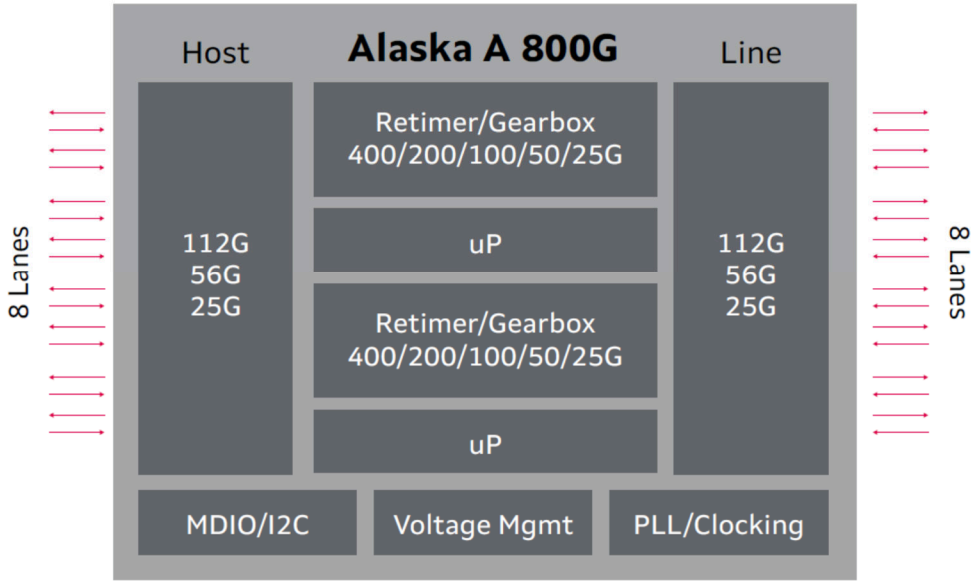
'233 Patent	Accused Molex Products
Claim 1	
	Overview The Broadcom® BCM87850 is a single-chip, eight-lane, ultra-low power, ultra-low latency PHY that integrates retimer and equalizer to support active cable applications. The BCM87850 is capable of equalizing 22 dB of loss on both the client-side and line-side interfaces. Each lane is capable of multiple data rates, including 106.25 Gb/s. The on-chip clock synthesis is performed by a low-cost 312.5-MHz reference clock through high-frequency, low jitter phase-locked loops (PLLs). The BCM87850 is fabricated in low-power 7-nm CMOS technology and is available in a 12 mm × 12 mm, 0.5-mm pitch, 485-ball BGA, RoHS-compliant package. Ex. 50, <i>Broadcom BCM87850 Product Brief</i> (annotated).

Claim 1



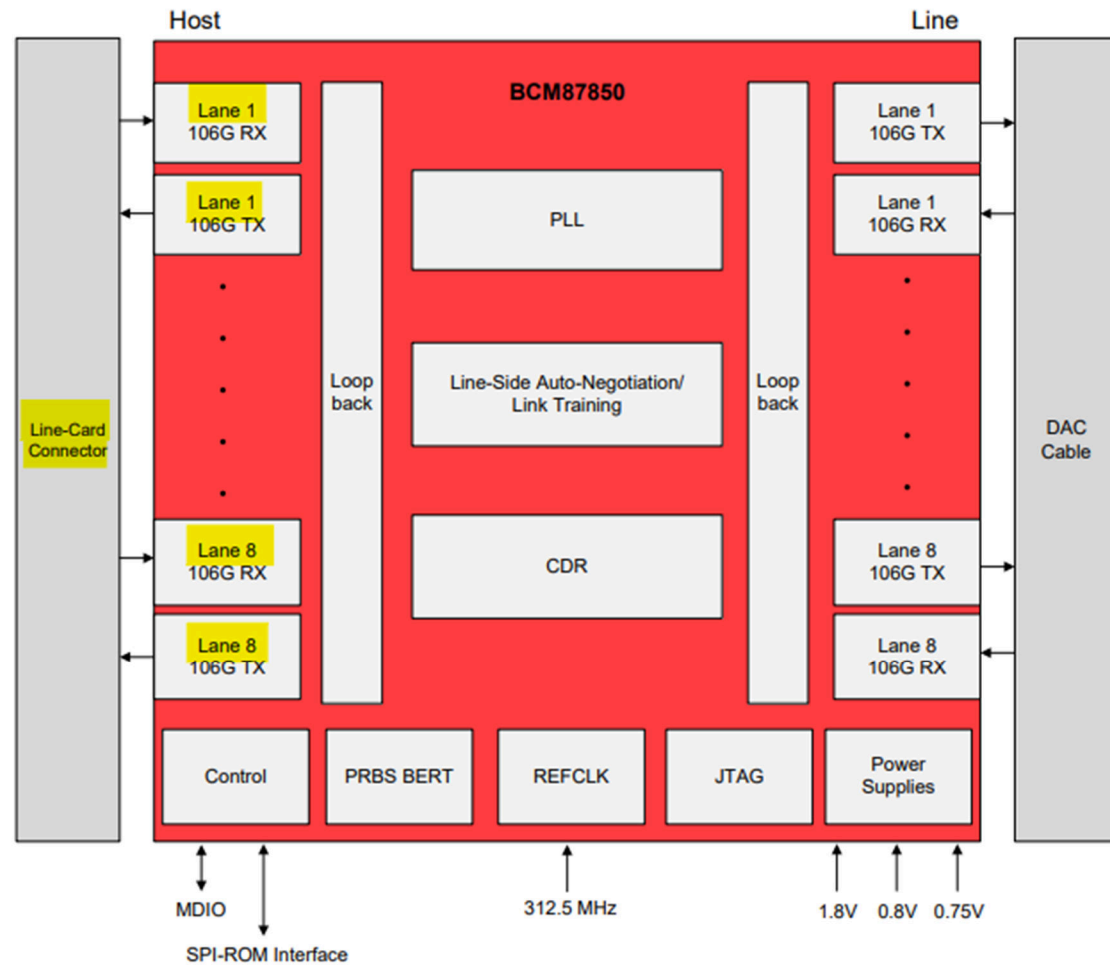
Ex. 50, *Broadcom BCM87850 Product Brief* (annotated).

'233 Patent	Accused Molex Products
Claim 1	
	Overview The Marvell Alaska A MV-CHA180C0C 800G is a PAM4 DSP retimer for 800G Active Electrical Cable (AEC) application, optimized for Switch to Switch and Switch to Server connectivity inside next generation cloud data center, high-performance computing and AI systems. Alaska A 800G is a retimer device which utilizes a 112G Gbps PAM4 DSP SERDES. There are 8-host and 8-line ports with each receiver port being able to recover 112Gbps PAM-4 signals and transmit to partnered TX. It can provide up to 800G (8 x 112G) full duplex mission mode traffic.

'233 Patent	Accused Molex Products
Claim 1	
	 Ex. 51, <i>Marvell Alaska Product Brief</i> (annotated).
[1b] a second DRR device that exchanges inbound and outbound multi-lane data streams with a second host interface port via a second end connector plug; and	The Accused Molex Products comprise a second DRR device that exchanges inbound and outbound multi-lane data streams with a second host interface port via a second end connector plug. For example, the second DRR device is a second DSP retimer at the other end of the cable. As shown below, each end of the cable has a symmetrical connector containing a DSP. See, e.g.:

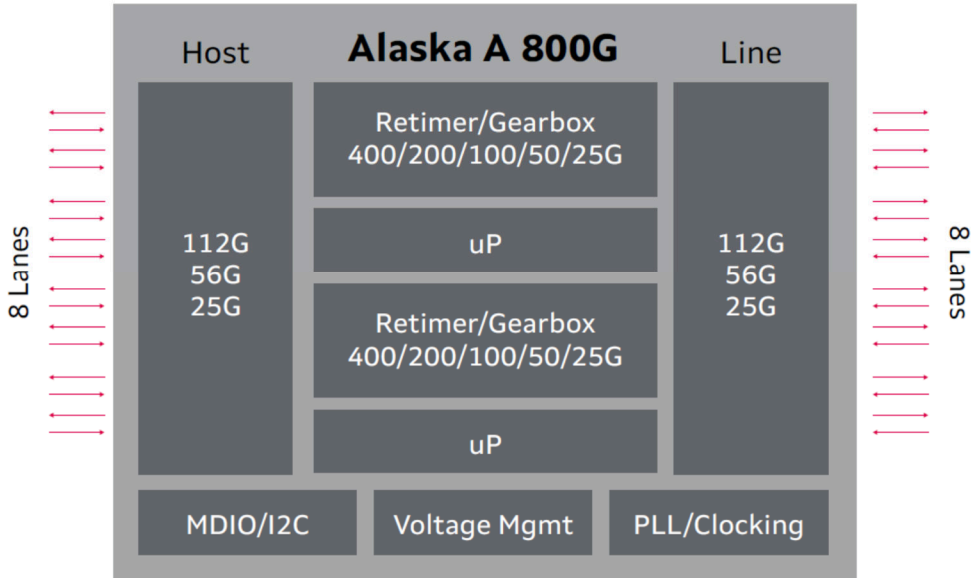
'233 Patent	Accused Molex Products
Claim 1	
	Active Electrical Cable (AEC) OSFP to OSFP Straight Cable with Broadcom BCM 87854 Re-Timer ➤ AECs use re-timers to efficiently extend the reach of copper cables. They deliver superior performance and improved design flexibility while simultaneously reducing costs. <i>Ex. 56, Molex Active Electrical Cable (AEC) OSFP to OSFP Straight Cable with Broadcom BCM 87854 Re-Timer Datasheet.</i> The second DSP exchanges inbound and outbound multi-lane data streams with a second host interface port via a second end connector plug: 

Claim 1



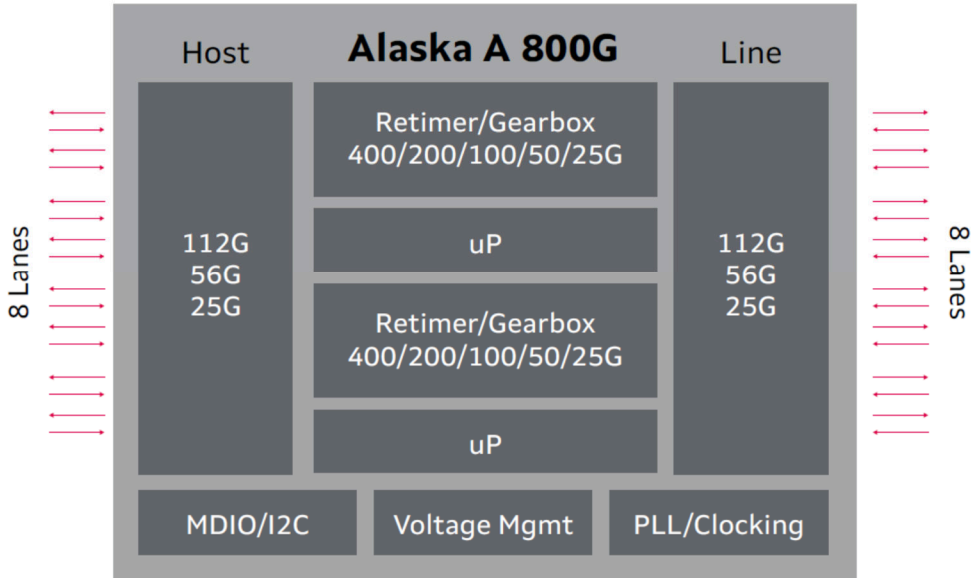
Ex. 50, *Broadcom BCM87850 Product Brief* (annotated).

'233 Patent	Accused Molex Products
Claim 1	
	Overview The Marvell Alaska A MV-CHA180C0C 800G is a PAM4 DSP retimer for 800G Active Electrical Cable (AEC) application, optimized for Switch to Switch and Switch to Server connectivity inside next generation cloud data center, high-performance computing and AI systems. Alaska A 800G is a retimer device which utilizes a 112G Gbps PAM4 DSP SERDES. There are 8-host and 8-line ports with each receiver port being able to recover 112Gbps PAM-4 signals and transmit to partnered TX. It can provide up to 800G (8 x 112G) full duplex mission mode traffic.

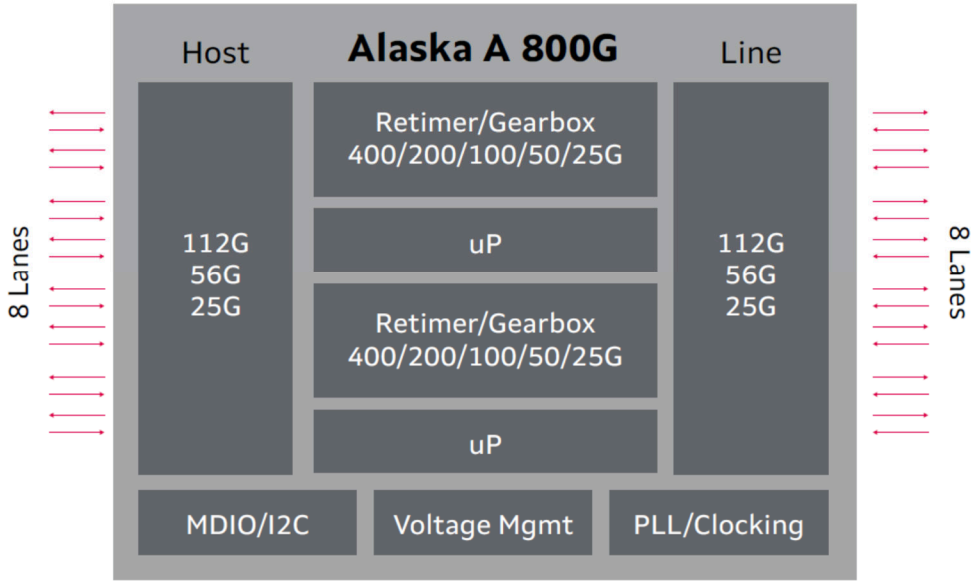
'233 Patent	Accused Molex Products
Claim 1	
	 Ex. 51, <i>Marvell Alaska Product Brief</i> (annotated). See also [1pre] and [1a] above.
[1c] electrical conductors connecting the first and second DRR devices to convey electrical transit signals therebetween,	The Accused Molex Products comprise electrical conductors connecting the first and second DRR devices to convey electrical transit signals therebetween. For example, the electrical conductors are discrete twin-axial copper wires that connect the first and second DSPs to convey electrical transit signals therebetween. See, e.g.:

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Claim 1	
	Cable Construction Expando over discrete twinax Ex. 56, <i>Molex Active Electrical Cable (AEC) OSFP to OSFP Straight Cable with Broadcom BCM 87854 Re-Timer Datasheet</i>.
[1d] the first DRR device converting between said electrical transit signals and said inbound and outbound multi-lane data streams for the first host interface port, and	The first DRR device in the Accused Molex Products converts between said electrical transit signals and said inbound and outbound multi-lane data streams for the first host interface port. For example, the first DSP receives electrical transit signals from the cable and converts them (via the retimer) into multi-lane data streams that are sent to the host. The first DSP also receives multi-lane data streams from the host and converts them (via the retimer) into electrical transit signals that are sent to the cable. See, e.g.: Overview The Broadcom® BCM87850 is a single-chip, eight-lane, ultra-low power, ultra-low latency PHY that integrates retimer and equalizer to support active cable applications. The BCM87850 is capable of equalizing 22 dB of loss on both the client-side and line-side interfaces. Each lane is capable of multiple data rates, including 106.25 Gb/s. The on-chip clock synthesis is performed by a low-cost 312.5-MHz reference clock through high-frequency, low jitter phase-locked loops (PLLs). The BCM87850 is fabricated in low-power 7-nm CMOS technology and is available in a 12 mm × 12 mm, 0.5-mm pitch, 485-ball BGA, RoHS-compliant package. Ex. 50, <i>Broadcom BCM87850 Product Brief</i> (annotated).

'233 Patent	Accused Molex Products
<u>Claim 1</u>	
	Overview The Marvell Alaska A MV-CHA180C0C 800G is a PAM4 DSP retimer for 800G Active Electrical Cable (AEC) application, optimized for Switch to Switch and Switch to Server connectivity inside next generation cloud data center, high-performance computing and AI systems. Alaska A 800G is a retimer device which utilizes a 112G Gbps PAM4 DSP SERDES. There are 8-host and 8-line ports with each receiver port being able to recover 112Gbps PAM-4 signals and transmit to partnered TX. It can provide up to 800G (8 x 112G) full duplex mission mode traffic.

'233 Patent	Accused Molex Products
Claim 1	
	 Ex. 51, <i>Marvell Alaska Product Brief</i> (annotated).
[1e] the second DRR device converting between said electrical transit signals and said inbound and outbound multi-lane data streams for the second host interface port,	The second DRR device in the Accused Molex Products converts between said electrical transit signals and said inbound and outbound multi-lane data streams for the second host interface port. For example, the second DSP at the other end of the cable also converts between said electrical transit signals and said inbound and outbound multi-lane data streams for the first host interface port via the retimer.

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Claim 1	
	Overview The Broadcom® BCM87850 is a single-chip, eight-lane, ultra-low power, ultra-low latency PHY that integrates retimer and equalizer to support active cable applications. The BCM87850 is capable of equalizing 22 dB of loss on both the client-side and line-side interfaces. Each lane is capable of multiple data rates, including 106.25 Gb/s. The on-chip clock synthesis is performed by a low-cost 312.5-MHz reference clock through high-frequency, low jitter phase-locked loops (PLLs). The BCM87850 is fabricated in low-power 7-nm CMOS technology and is available in a 12 mm × 12 mm, 0.5-mm pitch, 485-ball BGA, RoHS-compliant package. Ex. 50, <i>Broadcom BCM87850 Product Brief</i> (annotated). Overview The Marvell Alaska A MV-CHA180C0C 800G is a PAM4 DSP retimer for 800G Active Electrical Cable (AEC) application, optimized for Switch to Switch and Switch to Server connectivity inside next generation cloud data center, high-performance computing and AI systems. Alaska A 800G is a retimer device which utilizes a 112G Gbps PAM4 DSP SERDES. There are 8-host and 8-line ports with each receiver port being able to recover 112Gbps PAM-4 signals and transmit to partnered TX. It can provide up to 800G (8 x 112G) full duplex mission mode traffic.

'233 Patent	Accused Molex Products
Claim 1	
	 Ex. 51, <i>Marvell Alaska Product Brief</i> (annotated). See also 1[b] and 1[d] above.
[1f] the first and second DRR devices providing pre-equalization of the electrical transit signals using transmit filter coefficient values stored in nonvolatile memories.	The first and second DRR devices in the Accused Molex Products provide pre-equalization of the electrical transit signals using transmit filter coefficient values stored in nonvolatile memories. For example, the first and second DSPs provide different levels of pre-equalization corresponding to “short” and “long” modes used for 112G-per-lane chip-to-module (C2M) transmission, per the IEEE 802.3ck standard. On information and belief, the transmit filter coefficient values used for short-channel and long-channel pre-equalization are stored in

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Claim 1	
	nonvolatile memories so they are available for C2M communications when the cable is connected to the host. See, e.g.: 120G.3.2.1 Module output modes The module output shall support two modes: short and long. The means of controlling the module output mode is implementation dependent. For each output mode, the module shall meet the requirements for eye height (min) and VEC (max) in Table 120G-3 for both near-end and far-end measurements (see 120G.3.2.2.1). Ex. 52, <i>IEEE 802.3ck Specification</i> at 7275 (annotated). The website for the exemplary Accused Molex Products notes that they support the IEEE 802.3ck standard:¹

¹ The Accused Molex Products that use a QSFP form factor likewise comply with the IEEE 802.3ck standard. See Ex. 77, *QSFP-DD Specification* at 34; Ex. 78, *QSFP112 Specification* at 10.

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Claim 1	
	OSFP Connector System The Octal Small Form Factor Pluggable (OSFP) Connector System and Cable Assemblies provide 56 or 112 Gbps PAM-4 per lane, single-port, 8-lane I/O connectivity. These solutions support aggregate data rates of 400 and 800 Gbps, improve thermal management and meet IEEE and OSFP industry standards. Compact DAC, AOC, ACC and optical modules offer flexibility and support high-density switch applications. Meets IEEE 802.3ck channel compliance and OSFP 112G multi-source agreement (MSA) Complies with industry standards for easy implementation Ex. 57, <i>Molex OSFP Connector System Website</i>. On information and belief, a nonvolatile memory is integrated into the cable assembly, which contains the registers that is selected to specify initial pre-equalizer coefficient values, thus implementing the IEEE 802.3ck standard.

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Claim 8	
[8pre] A cable manufacturing method that comprises:	The Accused Molex Products are produced using a cable manufacturing method. <i>See [1pre] above.</i>
[8a] connecting a first connector plug to a first data recovery and re-modulation (DRR) device that exchanges multi-lane data streams with a first host interface port via the first connector plug;	The Accused Molex Products are produced using a cable manufacturing method, including connecting a first connector plug in the Accused Molex Products to a first data recovery and re-modulation (DRR) device that exchanges multi-lane data streams with a first host interface port via the first connector plug. <i>See [1a] above.</i>
[8b] connecting a second connector plug to a second DRR device that exchanges multi-lane data streams with a second host interface port via the second connector plug;	The Accused Molex Products are produced using a cable manufacturing method, including connecting a second connector plug in the Accused Molex Products to a second DRR device that exchanges multi-lane data streams with a second host interface port via the second connector plug. <i>See [1b] above.</i>
[8c] connecting electrical conductors to the first and second DRR devices to convey electrical transit signals therebetween,	The Accused Molex Products are produced using a cable manufacturing method, including connecting electrical conductors in the Accused Molex Products to the first and second DRR devices to convey electrical transit signals therebetween. <i>See [1c] above.</i>
[8d] the first DRR device converting between said electrical transit signals and said multi-lane data streams for the first host interface port, and	The Accused Molex Products are produced using a cable manufacturing method, wherein the first DRR device in the Accused Molex Products converts between said electrical transit signals and said multi-lane data streams for the first host interface port. <i>See [1d] above.</i>

'233 Patent	Accused Molex Products
<u>Claim 8</u>	
[8e] the second DRR device converting between said electrical transit signals and said multi-lane data streams for the second host interface port,	The Accused Molex Products are produced using a cable manufacturing method, wherein the second DRR device in the Accused Molex Products converts between said electrical transit signals and said multi-lane data streams for the second host interface port. <i>See [1e] above.</i>
[8f] the first and second DRR devices providing pre-equalization of the electrical transit signals using transmit filter coefficient values stored in nonvolatile memories.	The Accused Molex Products are produced using a cable manufacturing method, wherein the first and second DRR devices in the Accused Molex Products provide pre-equalization of the electrical transit signals using transmit filter coefficient values stored in nonvolatile memories. <i>See [1f] above.</i>

'233 Patent	Accused Molex Products
Claim 15	
[15pre] A communications method that comprises:	Molex performs a communication method. <i>See [1pre] above.</i>
[15a] inserting a first end connector plug of a cable into a first host interface port, the first end connector plug being connected to a first data recovery and re-modulation (DRR) device that converts a multi-lane data stream from the first host interface port into electrical transit signals conveyed by electrical conductors to a second DRR device connected to a second end connector plug of the cable; and	Molex inserts a first end connector plug of a cable into a first host interface port, the first end connector plug being connected to a first data recovery and re-modulation (DRR) device that converts a multi-lane data stream from the first host interface port into electrical transit signals conveyed by electrical conductors to a second DRR device connected to a second end connector plug of the cable. <i>See [1a], [1c], [1d] above.</i>
[15b] inserting the second end connector plug of the cable into a second host interface port, the second DRR device converting a multi-lane data stream from the second host interface port into electrical transit signals conveyed by electrical conductors to the first DRR device,	Molex inserts the second end connector plug of the cable into a second host interface port, the second DRR device converting a multi-lane data stream from the second host interface port into electrical transit signals conveyed by electrical conductors to the first DRR device. <i>See [1b], [1c], [1e] above.</i>
[15c] the first and second DRR devices providing pre-	The first and second DRR devices of the Accused Molex Products provide pre-equalization of the electrical transit signals using transmit filter coefficient values stored in nonvolatile memories.

'233 Patent	Accused Molex Products
Claim 15	
equalization of the electrical transit signals using transmit filter coefficient values stored in nonvolatile memories.	<i>See [1f] above.</i>