## Contact

www.linkedin.com/in/haoli-qian-1a50628 (LinkedIn)

## Top Skills

ASIC

## Languages

Chinese

English

# Haoli Qian

Chief R&D Officer

Fremont, California, United States

## Summary

Design the state of art architecture optimized for mixed-signal PHY's
Lead the team to implement and develop the next gen super high speed I/O technology

## Experience

Credo Semiconductor

Chief R&D Officer (CRDO)

January 2010 - Present (15 years 11 months)

Positions held at Credo Semiconductor: Director/Senior Director of Engineering/VP of Engineering

Advice for Contacting Me

We are currently seeking passionate and talented analog design, firmware, and application engineers to support super high speed I/O transceiver products.

Qualifications: PhD or MS

Please send me your resume if you are interested.

Marvell

Design Manager

September 2004 - June 2009 (4 years 10 months)

## Education

University at Buffalo

Tsinghua University



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