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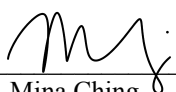

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Product Description

The material provides the reader with an understanding of the features and functions typically found on HSS devices. It explains how these HSS devices are used in protocol applications and the analysis which must be performed to use such HSS devices. The book is an assimilation of various topics with a focus on what chip designers need to understand in order to design chips using HSS cores.

The reader is first introduced to the basic concepts and the resulting features and functions typical of HSS devices. HSS devices are used in the context of a protocol application. The reader is then introduced to the basic concepts used by protocols, and overviews are provided for several protocol standards in which HSS devices are commonly used. Additional chapters describe the features, functions, and considerations associated with designing and analyzing a reference clock distribution network, as well as testing HSS hardware, analyzing signal integrity, and analyzing power consumption. Finally, any HSS core is not complete without a set of design kit models to facilitate integration within the chip design, and special topics uniquely related to design kits for HSS cores are discussed in the final chapter.

From the Back Cover

High Speed Serdes Devices and Applications provides a broad understanding of High Speed Serdes (HSS) device usage. The material focuses on HSS devices, and the consolidation of related topics into a single text. Chip and system designers using HSS devices must have detailed knowledge of both the features and functions of the HSS device, and the applications in which they are used. Also designers must have a working knowledge of related subjects, including: reference clock architectures, signal integrity, power dissipation, and test features and functions. The authors consolidate these topics with a specific focus on HSS devices. This approach provides the chip designer sufficient background information for using HSS devices on their chips.

The chapters can be viewed as four distinct sections. The first section relates to the features, functions, and design of HSS devices. Second are chapters that describe the features and functions of protocol logic used to implement various network protocol interface standards. The third section covers specialized topics related to HSS cores.

Finally, characteristics of typical design kit models to facilitate integration within the chip design are discussed.

High Speed Serdes Devices and Applications is a useful resource for chip designers using HSS devices in their chip design.

Product Details

Paperback: 504 pages
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