

EXHIBIT 42

Exhibit 42 – Claim Chart Showing TE Connectivity’s Infringement of U.S. Patent 11,032,111 (“’111 Patent”)

The following chart is based on information known to date. Credo reserves the right to amend and update this chart as additional information is obtained and analyzed. The exemplary claim mappings below rely on images and data from TE Connectivity and its suppliers’ public-facing materials. The following chart is representative of TE Connectivity’s infringement by its unlawful importation into the United States, sale for importation into the United States, and/or sale within the United States after importation of all the Accused TE Connectivity Products.

As outlined in the below claim chart, the Accused TE Connectivity Products infringe, either literally or under the doctrine of equivalents, at least Claims 1, 8, 11, and 16 of the ’111 patent.

<u>’111 Patent</u>	<u>Accused TE Connectivity Products</u>
<u>Claim 1</u>	
[1pre] A SerDes communications method that comprises, in a transceiver:	TE Connectivity performs a SerDes communications method in a transceiver. For example, the Accused TE Connectivity Products contain digital signal processors (“DSPs”) at the cable ends. See, e.g.:

'111 Patent	Accused TE Connectivity Products
Claim 1	
	OSFP Cable Assembly, 32 Positions, 3 m [9.84 ft], 56 Gb/s, Black Cable, Shielded, 26 AWG, OSFP Plug End A, OSFP Plug End B, VW-1, 16 Pairs  TE Internal #: 2336065-3 TE Internal Description: OSFP - OSFP, 3 METE R, 26AWG Cable Assembly Type : OSFP Number of Positions : 32 Cable Assembly Length : 3 m [9.84 ft] Data Rate (Gb/s): 56 Cable Color : Black Add to List ▾ Similar Products Digital Datasheet Ex. 58, <i>TE Connectivity OSFP Cable Assembly Website</i>. • 112G Product Support for SFP, SFP-DD, QSFP, QSFP-DD, OSFP, and OSFP-XD Interconnects - Direct Attached Copper Cables (DAC) - Active Copper Cables (ACC) - Active Electrical Cable (AEC) - Active Copper Cables (AOC) - I/O Connectors and Cages Ex. 59, <i>TE Connectivity 112G Portfolio External Cabling Solutions Datasheet</i>.

'111 Patent	Accused TE Connectivity Products
Claim 1	
	For example, the DSPs in the Accused Products are manufactured by Broadcom and others. The DSP in the Accused TE Connectivity Products is a retimer that includes a transceiver that transmits and receives host and line communications. See, e.g.: TE's live and static displays will demonstrate how TE is paving the way to meet the technology demands for next-generation products and architectures. TE will showcase its innovative solutions in the following booths: • TE Connectivity – Booth #6035 • Broadcom – Booth #6425 • Ethernet Alliance – Booth #5417 • Intel – Booth #2901 • MACOM – Booth #3927 • Marvell – Booth #4326 • Optical Internetworking Forum (OIF) – Booth #5101 • SEMTECH – Booth #5417 • Spirent – Booth #5304

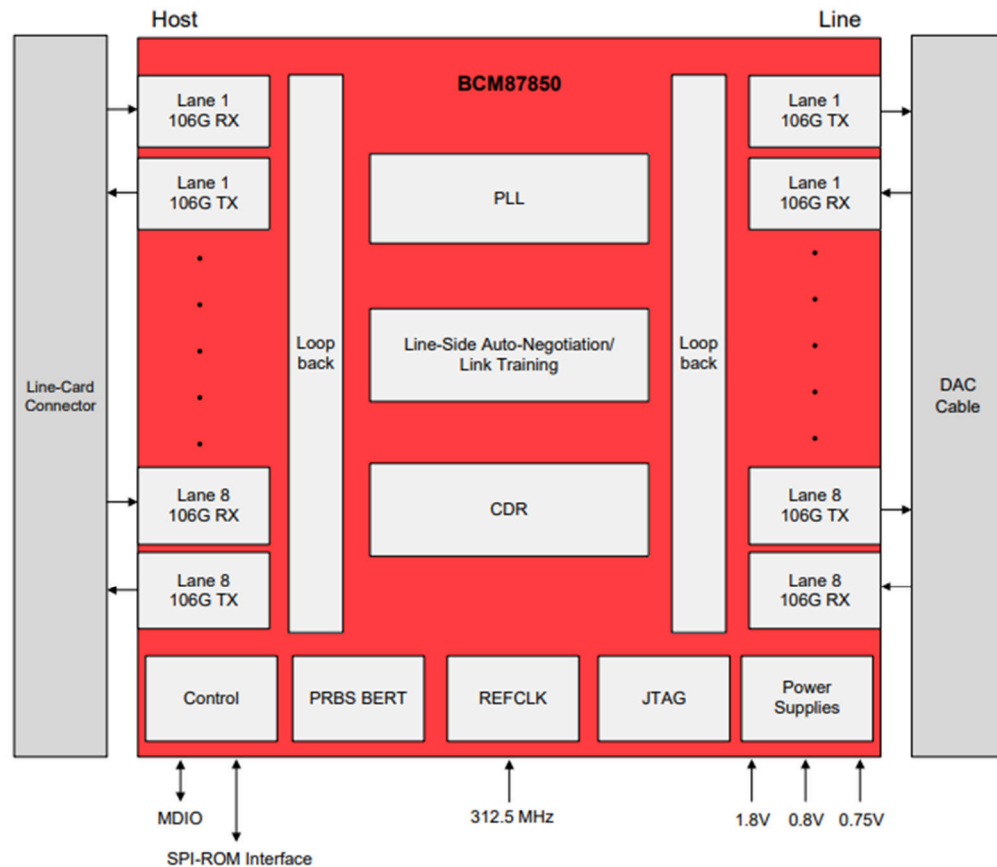
'111 Patent	Accused TE Connectivity Products
Claim 1	
	TE's featured live demos will include: 800G QSFP-DD active optical cables (AOC) The 800G quad small form-factor pluggable double density (QSFP-DD) AOC live demo showcases TE's 800 Gbps optical data link using an IEEE 802.3df 800GBASE-SR8 AOC in QSFP-DD form factor running live 8x112G traffic end-to-end. The AOC uses low-cost multimode optics and low power electronics, resulting in a per end power consumption below 14W. Initial beta product sampling is available now. 800G active cable technologies The 800G active copper cable (ACC) and active electrical cable (AEC) demonstrate the latest in TE's bulk cable and termination technology. A retimer or redriver embedded in the module extends the reach of copper cables while consuming less power than equivalent optical cables. AEC/ACC technology also enables fine wire gauges for short links to ease routing challenges in dense next-gen racks. This live demo shows eight lanes of 112G PAM-4 data running through TE's AEC/ACC with a host trace and connector on each side. TE's innovative solutions will also be displayed across its partner booths. Broadcom (booth #6425) will showcase TE's 800G AEC technology. Intel (booth #2901) will demonstrate TE's 1.6Tb 224 Gbps OSFP connector and DAC technology. MACOM (booth #3927) will display TE's 800G ACC technology. Marvell's exhibit (booth #4326) will include TE's 800G AEC technology. SEMTECH (booth #5417) will feature TE's 800G ACC technology. And Spirent (booth #5304) will highlight TE's 800G DAC technology in their demonstration. <i>Ex. 60, TE Connectivity OFC 2023 Press Release.</i> Active electrical cables powered by Marvell Alaska A PAM4 DSPs are sampling now from Amphenol, Molex and TE Connectivity.

'111 Patent

Accused TE Connectivity Products

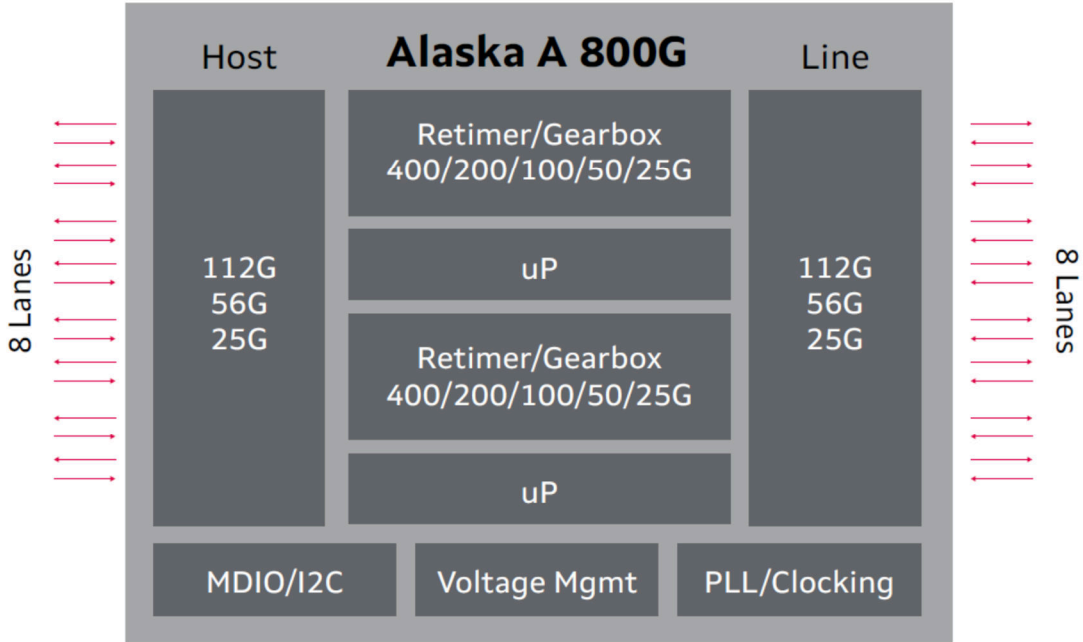
Claim 1

Ex. 23, *Marvell Announces Availability Of Active Electrical Cables Powered By Its Industry-Leading PAM4 DSP Technology Article*, Oct. 20, 2022.



Ex. 50, *Broadcom BCM87850 Product Brief*.

'111 Patent	Accused TE Connectivity Products
Claim 1	
	Overview The Marvell Alaska A MV-CHA180C0C 800G is a PAM4 DSP retimer for 800G Active Electrical Cable (AEC) application, optimized for Switch to Switch and Switch to Server connectivity inside next generation cloud data center, high-performance computing and AI systems. Alaska A 800G is a retimer device which utilizes a 112G Gbps PAM4 DSP SERDES. There are 8-host and 8-line ports with each receiver port being able to recover 112Gbps PAM-4 signals and transmit to partnered TX. It can provide up to 800G (8 x 112G) full duplex mission mode traffic.

'111 Patent	Accused TE Connectivity Products
Claim 1	
	 Ex. 51, <i>Marvell Alaska Product Brief</i> (annotated).
[1a] selecting one of multiple registers to specify initial pre-equalizer coefficient values, each of the multiple registers corresponding to a different channel model;	TE Connectivity selects one of multiple registers to specify initial pre-equalizer coefficient values, each of the multiple registers corresponding to a different channel model. For example, the DSP in the Accused TE Connectivity Products selects different levels of pre-equalization corresponding to “short” and “long” modes used for 112G chip-to-module (C2M) transmission, per the IEEE 802.3ck standard, which correspond to different channel models. On information and belief, the pre-equalizer coefficient values used for short-channel and long-channel pre-equalization are stored in selectable registers so they are

'111 Patent	Accused TE Connectivity Products
Claim 1	
	available for both short and long C2M communications when the cable is connected to the host. For example, the Accused TE Connectivity Products use OSFP form factor pluggable modules.¹ See, e.g.: OSFP Cable Assembly, 32 Positions, 3 m [9.84 ft], 56 Gb/s, Black Cable, Shielded, 26 AWG, OSFP Plug End A, OSFP Plug End B, VW-1, 16 Pairs  TE Internal #: 2336065-3 TE Internal Description: OSFP - OSFP, 3 METE R, 26AWG Cable Assembly Type : OSFP Number of Positions : 32 Cable Assembly Length : 3 m [9.84 ft] Data Rate (Gb/s): 56 Cable Color : Black Add to List Similar Products Digital Datasheet Ex. 58, TE Connectivity OSFP Cable Assembly Website.

¹ The Accused TE Connectivity Products that use a QSFP form factor likewise comply with the IEEE 802.3ck standard and the the Common Management Interface Specification (CMIS) standard. See Ex. 77, QSFP-DD Specification at 34; Ex. 78, *QSFP112 Specification* at 10.

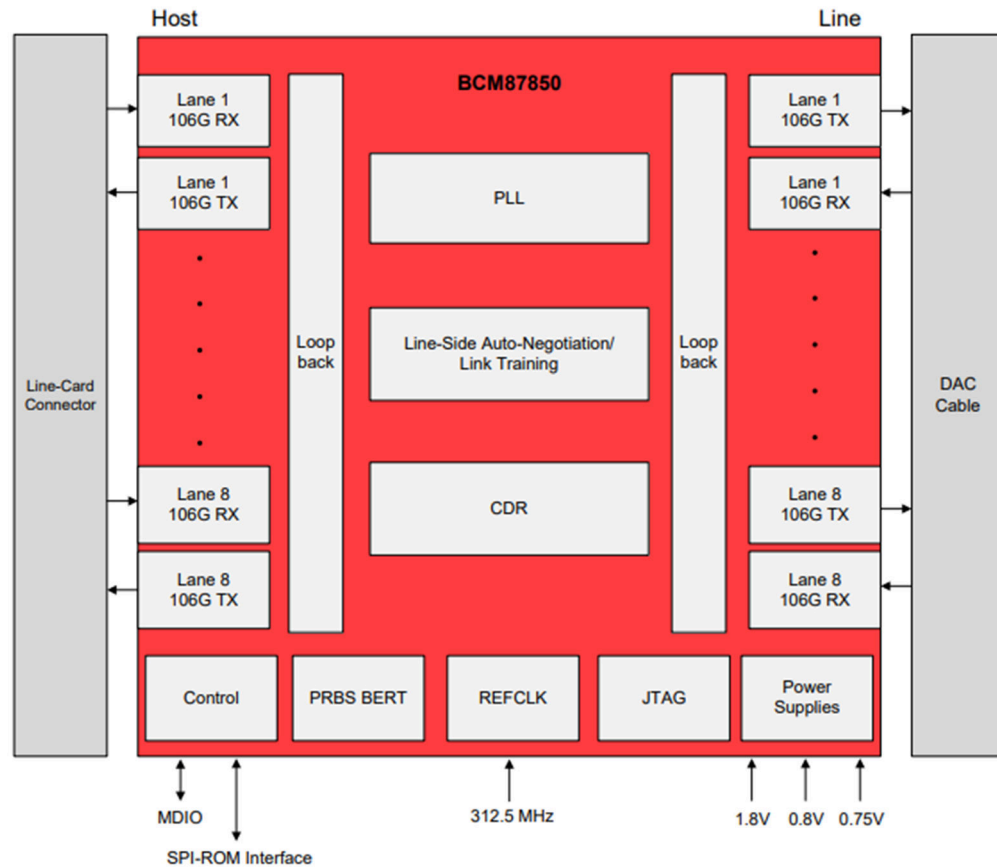
'111 Patent	Accused TE Connectivity Products
<u>Claim 1</u>	
	The OSFP standard requires compliance with the IEEE 802.3ck standard, including the use of standard attachment unit interfaces (AUI). The high-speed signals follow the electrical specifications of IEEE802.3bs, IEEE802.3cd, IEEE 802.3ck and CEI-56G-VSR-PAM4 as defined in OIF-CEI-05.2 for 400GAUI-8 mode and IEEE802.3bj, IEEE802.3bm for CAUI-4 mode. Ex. 54, <i>OSFP Specification</i> at 147(annotated). The 802.3ck standard defines the chip-to-module connection, including the support for short and long channel modes. 120G.3.2.1 Module output modes The module output shall support two modes: short and long. The means of controlling the module output mode is implementation dependent. For each output mode, the module shall meet the requirements for eye height (min) and VEC (max) in Table 120G-3 for both near-end and far-end measurements (see 120G.3.2.2.1). Ex. 52, <i>IEEE 802.3ck Specification</i> at 7275 (annotated). The OSFP standard also requires compliance of the control plane to the Common Management Interface Specification (CMIS) standard, which affects the register module used for the interface to select a short or long channel.

'111 Patent	Accused TE Connectivity Products										
Claim 1											
	The OSFP specification defines: • The OSFP module mechanical form factor, including the latching mechanism; • The host cage together with the mating connector; • The electrical interface, including pin-out, data, control, and power and ground signals; • The mechanical interface, including the package outline, front panel, and printed circuit board (PCB) layout requirements; • Thermal requirements and limitations, including heat sink design and airflow; • Electrostatic discharge (ESD) requirements; • The module management interface as contained in the Common Management Interface Specification (CMIS). Ex. 54, OSFP Specification at 15(annotated). The CMIS standard enables selection of the short or long channel models by writing the correct value to the CMIS register. <table><tr><th>Byte</th><th>Bits</th><th>Field Name</th><th>Field Description</th><th>Type</th></tr><tr><td>0</td><td>7-0</td><td>SFF8024Identifier</td><td> SFF8024Identifier is an SFF-8024 module type Identifier from the Identifier Values table in [5] which allows to infer both physical form factor and management protocol of the module. <i>Note: The CMIS interpretation of all other registers or fields is valid only when the fundamental SFF8024Identifier indicates that the module uses the CMIS management protocol.</i> </td><td>RO Rqd.</td></tr></table> Ex. 55, CMIS Specification at 127. The website for the exemplary Accused TE Connectivity Products notes that they support the 802.3ck standard:	Byte	Bits	Field Name	Field Description	Type	0	7-0	SFF8024Identifier	SFF8024Identifier is an SFF-8024 module type Identifier from the Identifier Values table in [5] which allows to infer both physical form factor and management protocol of the module. <i>Note: The CMIS interpretation of all other registers or fields is valid only when the fundamental SFF8024Identifier indicates that the module uses the CMIS management protocol.</i>	RO Rqd.
Byte	Bits	Field Name	Field Description	Type							
0	7-0	SFF8024Identifier	SFF8024Identifier is an SFF-8024 module type Identifier from the Identifier Values table in [5] which allows to infer both physical form factor and management protocol of the module. <i>Note: The CMIS interpretation of all other registers or fields is valid only when the fundamental SFF8024Identifier indicates that the module uses the CMIS management protocol.</i>	RO Rqd.							

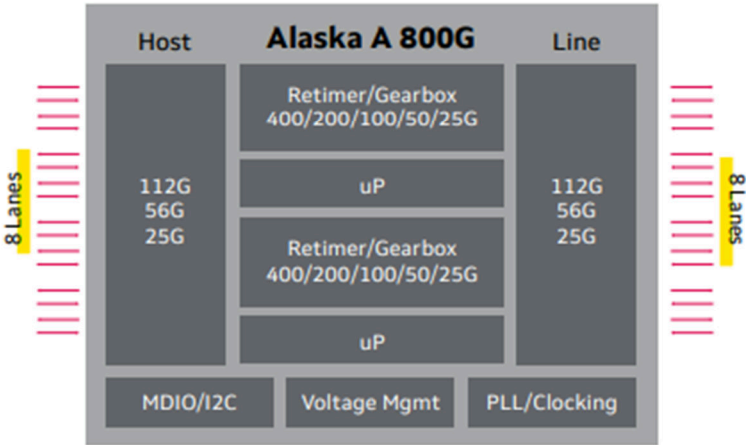
'111 Patent	Accused TE Connectivity Products
Claim 1	
	TE Connectivity's (TE) next generation octal small form-factor pluggable 224G DAC/AEC/ACC portfolio offers industry standard operations at 1600 Gbps complying with IEEE 802.3df and IEEE 802.3ck. The OSFP specification is designed to support high density applications, up to 32 1.6T ports per 1RU and TE offers a wide variety of configurations to support a range of power levels and cooling solutions. TE's OSFP-224G product line is well suited for artificial intelligence (AI) and machine learning (ML) architectures. OSFP 1.6T AEC (Active Electrical Cable) 1. Industry Standard - Up to 1600 Gbps aggregate throughput, compliant to IEEE 802.3ck. 2. Low Power – Lower power compared to optics; targeting <20W per module. 3. Active cables optimized for heat dissipation and system airflow. Ex. 61, <i>TE Connectivity OSFP Connectors and Cable Assemblies Website</i> (annotated). On information and belief, a nonvolatile memory is integrated into the cable assembly, which contains the registers that is selected to specify initial pre-equalizer coefficient values, thus implementing the IEEE 802.3ck standard.
[1b] updating the initial pre-equalizer coefficient values during a training phase; and	TE Connectivity updates the initial pre-equalizer coefficient values during a training phase. For example, the Accused TE Connectivity Products use continuous auto-adaptive equalizers and/or automatic tuning of the host link to update the initial pre-equalizer coefficient values during a training phase. See, e.g.: • Continuous auto-adaptive equalizers

'111 Patent	Accused TE Connectivity Products
Claim 1	
	Ex. 50, <i>Broadcom BCM87850 Product Brief</i>. Alaska A supports multiple industry standard protocols up to 112Gbs for both host and lines sides (IEEE and CEI). It includes IEEE compliant Auto Negotiation and Link Training, for automatic tuning of the host link between the Switch/Server ASIC to the AEC, to maximize reach and link margins for high density switch designs. Support for IEEE Auto Negotiation and Training protocol • Automatic tuning of Serdes parameters to maximize link margins Ex. 51, <i>Marvell Alaska Product Brief</i>.
[1c] using the updated pre-equalizer coefficient values to convey a transmit data stream.	TE Connectivity uses the updated pre-equalizer coefficient values to convey a transmit data stream. For example, the Accused TE Connectivity Products use the updated pre-equalizer coefficient values (via, for example, the continuous auto-adaptive equalizers) to convey a transmit data stream from the DSP to the host over 8 lanes.

Claim 1



Ex. 50, Broadcom BCM87850 Product Brief (see 8 outgoing lanes on left of chip).

'111 Patent	Accused TE Connectivity Products
Claim 1	
	Alaska A 800G is a retimer device which utilizes a 112G Gbps PAM4 DSP SERDES. There are 8-host and 8-line ports with each receiver port being able to recover 112Gbps PAM-4 signals and transmit to partnered TX. It can provide up to 800G (8 x 112G) full duplex mission mode traffic.  Ex. 51, <i>Marvell Alaska Product Brief</i> (annotated).

'111 Patent	Accused TE Connectivity Products
Claim 8	
[8pre] A chip-to-module communications link that comprises a port connector coupling a port transceiver to a pluggable module transceiver, the pluggable module transceiver including:	The Accused TE Connectivity Products comprise a chip-to-module communications link that comprises a port connector coupling a port transceiver to a pluggable module transceiver. For example, the Accused TE Connectivity Products, on information and belief, comprise a port connector. As can be seen below, each cable terminates on each end in a port connector. These connectors are OSFP (Octal Small Form Factor Pluggable) cable assemblies. OSFP Cable Assembly, 32 Positions, 3 m [9.84 ft], 56 Gb/s, Black Cable, Shielded, 26 AWG, OSFP Plug End A, OSFP Plug End B, VW-1, 16 Pairs  TE Internal #: 2336065-3 TE Internal Description: OSFP - OSFP, 3 METE R, 26AWG Cable Assembly Type : OSFP Number of Positions : 32 Cable Assembly Length : 3 m [9.84 ft] Data Rate (Gb/s): 56 Cable Color : Black Add to List Similar Products Digital Datasheet Ex. 58, TE Connectivity OSFP Cable Assembly Website. See [1pre] above.
[8a] one or more transmit filters to each pre-equalize a corresponding	The Accused TE Connectivity Products comprise one or more transmit filters to each pre-equalize a corresponding serial symbol stream being transmitted to the port transceiver.

'111 Patent	Accused TE Connectivity Products
Claim 8	
serial symbol stream being transmitted to the port transceiver; and	For example, on information and belief, the DSP in the Accused TE Connectivity Products use one or more transmit filters to pre-equalize a serial symbol stream being transmitted to the port transceiver. See, e.g.: As explained in the OSFP specification, the high-speed signals of the Accused TE Connectivity Products meet the requirements of the IEEE 802.3ck standard. The high-speed signals follow the electrical specifications of IEEE802.3bs, IEEE802.3cd, IEEE 802.3ck and CEI-56G-VSR-PAM4 as defined in OIF-CEI-05.2 for 400GAUI-8 mode and IEEE802.3bj, IEEE802.3bm for CAUI-4 mode. Ex. 54, <i>OSFP Specification</i> at 147. The 802.3ck standard defines the chip-to-module connection, including requiring that the transmitters use a pre-equalized set of filters as part of selecting a short or long channel. 120G.3.2.1 Module output modes The module output shall support two modes: short and long. The means of controlling the module output mode is implementation dependent. For each output mode, the module shall meet the requirements for eye height (min) and VEC (max) in Table 120G-3 for both near-end and far-end measurements (see 120G.3.2.2.1). Ex. 52, <i>IEEE 802.3ck Specification</i> at 7275 (annotated). The OSFP standard also requires compliance of the control plane to the CMIS standard.

'111 Patent	Accused TE Connectivity Products
Claim 8	
	The OSFP specification defines: • The OSFP module mechanical form factor, including the latching mechanism; • The host cage together with the mating connector; • The electrical interface, including pin-out, data, control, and power and ground signals; • The mechanical interface, including the package outline, front panel, and printed circuit board (PCB) layout requirements; • Thermal requirements and limitations, including heat sink design and airflow; • Electrostatic discharge (ESD) requirements; • The module management interface as contained in the Common Management Interface Specification (CMIS). Ex. 54, <i>OSFP Specification</i> at 15(annotated). The CMIS standard controls the equalization of the port transceiver.

'111 Patent	Accused TE Connectivity Products																																												
Claim 8																																													
	6.2.5.2 Rx Output Equalization Control Rx output equalization is defined at an appropriate test point defined by the relevant standard. SCS<k>::OutputEqPreCursorTargetRx<i> and SCS<k>::OutputEqPostCursorTargetRx<i> are four-bit control fields for lane <i> and encoded as shown in Table 6-7. The module advertises support of Rx output equalization control as described in Table 8-48. The module advertises the maximum supported Rx output equalization values as described in Table 8-44. Modules that require only output emphasis utilize the SCS<k>::OutputEqPostCursorTargetRx<i> fields and set the SCS<k>::OutputEqPreCursorTargetRx<i> fields to zero. Table 6-7 Rx Output Equalization Codes <table><tr><th>Code Value</th><th>Bit pattern</th><th>Post-Cursor Equalization</th><th>Pre-Cursor Equalization</th></tr><tr><td>0</td><td>0000b</td><td>0dB (No Equalization)</td><td>0dB (No Equalization)</td></tr><tr><td>1</td><td>0001b</td><td>1 dB</td><td>0.5 dB</td></tr><tr><td>2</td><td>0010b</td><td>2 dB</td><td>1.0 dB</td></tr><tr><td>3</td><td>0011b</td><td>3 dB</td><td>1.5 dB</td></tr><tr><td>4</td><td>0100b</td><td>4 dB</td><td>2.0 dB</td></tr><tr><td>5</td><td>0101b</td><td>5 dB</td><td>2.5 dB</td></tr><tr><td>6</td><td>0110b</td><td>6 dB</td><td>3.0 dB</td></tr><tr><td>7</td><td>0111b</td><td>7 dB</td><td>3.5 dB</td></tr><tr><td>8-10</td><td>1000b-1010b</td><td>Reserved</td><td>Reserved</td></tr><tr><td>11-15</td><td>1011b-1111b</td><td>Custom</td><td>Custom</td></tr></table> Note: The pre-cursor equalizer settings in dB approximates to Pre EQ (dB) = -20·log₁₀(1 - C₋₁ / (C₋₁+C₀+C₁)) (Eq. 6-1) The post-cursor equalizer settings in dB approximates to Post EQ (dB) = -20·log₁₀(1 - C₁ / (C₋₁+C₀+C₁)) (Eq. 6-2) Equalizer coefficients C_n are pre-cursor for n<0 and post-cursor when n>0. Ex. 55, CMIS Specification at 61.	Code Value	Bit pattern	Post-Cursor Equalization	Pre-Cursor Equalization	0	0000b	0dB (No Equalization)	0dB (No Equalization)	1	0001b	1 dB	0.5 dB	2	0010b	2 dB	1.0 dB	3	0011b	3 dB	1.5 dB	4	0100b	4 dB	2.0 dB	5	0101b	5 dB	2.5 dB	6	0110b	6 dB	3.0 dB	7	0111b	7 dB	3.5 dB	8-10	1000b-1010b	Reserved	Reserved	11-15	1011b-1111b	Custom	Custom
Code Value	Bit pattern	Post-Cursor Equalization	Pre-Cursor Equalization																																										
0	0000b	0dB (No Equalization)	0dB (No Equalization)																																										
1	0001b	1 dB	0.5 dB																																										
2	0010b	2 dB	1.0 dB																																										
3	0011b	3 dB	1.5 dB																																										
4	0100b	4 dB	2.0 dB																																										
5	0101b	5 dB	2.5 dB																																										
6	0110b	6 dB	3.0 dB																																										
7	0111b	7 dB	3.5 dB																																										
8-10	1000b-1010b	Reserved	Reserved																																										
11-15	1011b-1111b	Custom	Custom																																										
[8b] a controller having multiple registers, each of the multiple registers containing a set of initial coefficient values corresponding to a different channel model, the controller using one of the registers	The Accused TE Connectivity Products comprise a controller having multiple registers, each of the multiple registers containing a set of initial coefficient values corresponding to a different channel model, the controller using one of the registers to set initial coefficient values for the one or more transmit filters.																																												

'111 Patent	Accused TE Connectivity Products
Claim 8 to set initial coefficient values for the one or more transmit filters.	For example, on information and belief, the DSP in the Accused TE Connectivity Products includes a controller that selects initial coefficient values corresponding to “short” and “long” modes used for 112G C2M transmission, per the IEEE 802.3ck standard, which correspond to different channel models. On information and belief, the DSP has registers that are used to set the initial coefficient values for the transmit filters. See, e.g., 120G.3.2.1 Module output modes The module output shall support two modes: short and long. The means of controlling the module output mode is implementation dependent. For each output mode, the module shall meet the requirements for eye height (min) and VEC (max) in Table 120G–3 for both near-end and far-end measurements (see 120G.3.2.2.1). Ex. 52, <i>IEEE 802.3ck Specification</i> at 7275 (annotated). The website for the exemplary TE Connectivity products notes that they support the 802.3ck standard: TE Connectivity’s (TE) next generation octal small form-factor pluggable 224G DAC/AEC/ACC portfolio offers industry standard operations at 1600 Gbps complying with IEEE 802.3df and IEEE 802.3ck. The OSFP specification is designed to support high density applications, up to 32 1.6T ports per 1RU and TE offers a wide variety of configurations to support a range of power levels and cooling solutions. TE’s OSFP-224G product line is well suited for artificial intelligence (AI) and machine learning (ML) architectures.

'111 Patent	Accused TE Connectivity Products
Claim 8	
	OSFP 1.6T AEC (Active Electrical Cable) 1. Industry Standard - Up to 1600 Gbps aggregate throughput, compliant to IEEE 802.3ck . 2. Low Power – Lower power compared to optics; targeting <20W per module. 3. Active cables optimized for heat dissipation and system airflow. Ex. 61, <i>TE Connectivity OSFP Connectors and Cable Assemblies Website</i> (annotated). On information and belief, a nonvolatile memory is integrated into the cable assembly, which contains the registers that is selected to specify initial pre-equalizer coefficient values, thus implementing the IEEE 802.3ck standard.

'111 Patent	Accused TE Connectivity Products
<u>Claim 11</u>	
[11pre] A chip-to-module communications link that comprises a port connector coupling a port transceiver to a pluggable module transceiver, the pluggable module transceiver including:	The Accused TE Connectivity Products have a chip-to-module communications link that comprises a port connector coupling a port transceiver to a pluggable module transceiver. <i>See [8pre] above.</i>
[11a] one or more transmit filters to each pre-equalize a corresponding serial symbol stream being transmitted to the port transceiver; and	The Accused TE Connectivity Products have a pluggable module transceiver including one or more transmit filters to each pre-equalize a corresponding serial symbol stream being transmitted to the port transceiver. <i>See [8a] above.</i>
[11b] a controller having multiple registers, each of the multiple registers containing a set of initial coefficient values for which the port transceiver determines a performance characteristic,	The Accused TE Connectivity Products have a pluggable module transceiver including a controller having multiple registers, each of the multiple registers containing a set of initial coefficient values for which the port transceiver determines a performance characteristic. <i>See [8b] above.</i>
[11c] the controller using one of the registers selected by the port transceiver to specify the initial coefficient values for the one or more transmit filters.	The controller of the Accused TE Connectivity Products uses one of the registers selected by the port transceiver to specify the initial coefficient values for the one or more transmit filters <i>See [8b] above.</i>

'111 Patent	Accused TE Connectivity Products
<u>Claim 16</u>	
[16pre] A pluggable module transceiver including:	The Accused TE Connectivity Products have a module transceiver. <i>See [8pre] above.</i>
[16a] one or more transmit filters to each pre-equalize a corresponding serial symbol stream being transmitted to a port transceiver; and	The Accused TE Connectivity Products have a module transceiver including one or more transmit filters to each pre-equalize a corresponding serial symbol stream being transmitted to a port transceiver. <i>See [8a] above.</i>
[16b] a controller having multiple registers, each of the multiple registers containing a set of initial coefficient values corresponding to a different channel model, the controller using one of the registers to set initial coefficient values for the one or more transmit filters.	The Accused TE Connectivity Products have a module transceiver including a controller having multiple registers, each of the multiple registers containing a set of initial coefficient values corresponding to a different channel model, the controller using one of the registers to set initial coefficient values for the one or more transmit filters. <i>See [8b] above.</i>