

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

ASUSTEK COMPUTER INC.
Petitioner,

v.

NOKIA TECHNOLOGIES OY
Patent Owner.

Case IPR2025-01279
Patent 8,050,321

PETITIONER'S UPDATED EXHIBIT LIST

EXHIBITS

ASUS Exhibit No.	Description
ASUS-1001	U.S. Patent No. 8,050,321 (“the ’321 patent”)
ASUS-1002	Prosecution File History for the ’321 patent
ASUS-1003	Declaration of Dr. Eli Saber
ASUS-1004	Curriculum Vitae of Dr. Eli Saber
ASUS-1005	ISO/IEC 11172-2 (1st ed.) Information technology - Coding of moving pictures and associated audio for digital storage media at up to about 1,5 Mbit/s - Part 2: Video (“MPEG-1”)
ASUS-1006	U.S. Patent No. 6,912,351 (“Kim”)
ASUS-1007	CL480PC MPEG-1 System Decoder Specification, <i>available at</i> https://web.archive.org/web/19970128081644/http://www.ccube.com/prdctlst/cl480pc.pdf .
ASUS-1008	U.S. Patent No. 7,263,270
ASUS-1009	Ruby B. Lee et al., <i>Real-Time Software MPEG Video Decoder on Multimedia-Enhanced PA 7100LC Processors</i> , Hewlett-Packard J., Apr. 1995, <i>available at</i> https://archive.org/details/Hewlett-Packard_Journal_Vol._46_No._2_1995-04_Hewlett-Packard
ASUS-1010	Ketan Patel, et al., <i>Performance of a Software MPEG Video Decoder</i> , Multimedia ‘93: Proceedings of the First ACM Int’l Conf. on Multimedia, August 2-6, 1993
ASUS-1011	RESERVED
ASUS-1012	RESERVED

ASUS-1013	Declaration of June Ann Munford
ASUS-1014	U.S. Patent No. 7,894,521
ASUS-1015	U.S. Patent No. 5,786,858 (“Yagasaki”)
ASUS-1016	August 1, 2025 ASUS Stipulation

Respectfully submitted,

Dated August 1, 2025

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CERTIFICATE OF SERVICE

Pursuant to 37 CFR §§ 42.6(e)(4)(i) *et seq.* and 42.105(b), the undersigned certifies that on August 1, 2025, a complete and entire copy of this Petitioner's Updated Exhibit List and the associated stipulation were provided via Federal Express, to the Patent Owner by serving the correspondence address of record as follows:

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