

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

TAIWAN SEMICONDUCTOR MANUFACTURING COMPANY LTD.,

Petitioner,

v.

ADVANCED INTEGRATED CIRCUIT PROCESS LLC,

Patent Owner.

IPR2025-01302

U.S. Patent 8,884,373

PETITIONER'S UPDATED EXHIBIT LIST

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Patent Trial and Appeal Board
U.S. Patent & Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

EXHIBIT LIST

Pursuant to 37 C.F.R. § 42.63(e), Petitioner Taiwan Semiconductor Manufacturing Company Ltd. hereby submits a current listing of exhibits. Exhibit TSMC-1019 is being filed today.

Exhibit	Description
1001	U.S. Patent 8,884,373 to Sato et al. (“the ’373 patent”)
1002	File History of U.S. Patent 8,884,373 (“’373 File History”)
1003	Declaration of Dr. Jack Lee in Support of IPR Petition
1004	CV of Dr. Jack Lee
1005	Translated Excerpts from File History of Japanese Patent No. 5857225 to Sato et al. (“JP ’225 patent”) (Japanese counterpart of the ’373 Patent)
1006	U.S. Publication 2008/0036010 to Tamaki, et al. (“Tamaki”)
1007	U.S. Publication 2008/0042218 to Igarashi, et al. (“Igarashi”)
1008	Japanese Patent No. 9181194 to Sumi (“Sumi”)
1009	Certified Translation of Japanese Patent No. 9181194 to Sumi
1010	U.S. Publication 2009/0096031 to Ekbote, et al. (“Ekbote”)
1011	Plummer, J., et al., “Silicon VLSI Technology: Fundamentals, Practice and Modeling” (Prentice Hall 2000) (“Plummer”)
1012	Weste, “CMOS VLSI Design: A Circuits and Systems Perspective” (Third Edition, 2005) (“Weste”)
1013	Wolf, “Silicon Processing for the VLSI Era, Volume 4 – DeepSubmicron Process Technology” (2002) (“Wolf-4”)

Exhibit	Description
1014	Van Zant, “Microchip Fabrication” (Fifth Edition, 2004) (“Van Zant”)
1015	“International Technology Roadmap for Semiconductors: Process Integration, Devices, and Structures” (2007 Edition) (“ITRS”)
1016	U.S. Patent No. 8,355,269 to Zampaglione (“Zampaglione”)
1017	Sze, “Semiconductor Devices: Physics and Technology” (2nd Edition) (2002) (“Sze”)
1018	U.S. Patent 5,208,184 to Bayraktaroglu (“Bayraktaroglu”)
1019	TSMC’s stipulation regarding U.S. Patent Nos. 8,884,373, 8,329,572, 7,439,623, and 7,632,751

Respectfully submitted,

/Lori A. Gordon/

Lori A. Gordon
Reg. No. 50,633
Attorney for Petitioner

Goodwin Procter LLP
1900 N. Street NW
Washington, D.C. 20036
Phone: (202) 346-4000
Fax: (202) 346-4444

Date: August 26, 2025

CERTIFICATE OF SERVICE

The undersigned hereby certifies that a copy of the foregoing Petitioner's Updated Exhibit List and TSMC-1019 were served via e-mail directed to counsel of record for Patent Owner at the following:

Russell A. Chorush
Michael F. Heim
Christopher L. Limbacher
Lily R. Glick
Kyle Friesen
HEIM, PAYNE & CHORUSH, LLP 609
Main Street, Suite 3200
Houston, TX 77002
rchorush@hpcllp.com
mheim@hpcllp.com
climbacher@hpcllp.com
lglick@hpcllp.com
kfriesen@hpcllp.com
aicppllit@hpcllp.com

Patent Owner has consented to electronic service via email.

August 26, 2025

/Lori A. Gordon/

Lori A. Gordon
Reg. No. 50,633
Attorney for Petitioner

Goodwin Procter LLP
1900 N. Street NW
Washington, D.C. 20036
Phone: (202) 346-4000
Fax: (202) 346-4444