

EXHIBIT E-5
KARCZEWICZ II
ANTICIPATES / RENDERS OBVIOUS U.S. PATENT NO. 11,805,267

As described in the following claim chart, claims 1-36 of U.S. Patent No. 11,805,267 (the “’267 patent”) are invalid in view of US Patent Application Publication 2009/0257499 (“Karczewicz II”) (RESPNOKIA 00025222), entitled “Advanced Interpolation Techniques for Motion Compensation in Video Coding,” published on October 15, 2009. Karczewicz II was filed April 8, 2009, and is prior art under at least 35 U.S.C. §§ 102(a)/(b)/(e).

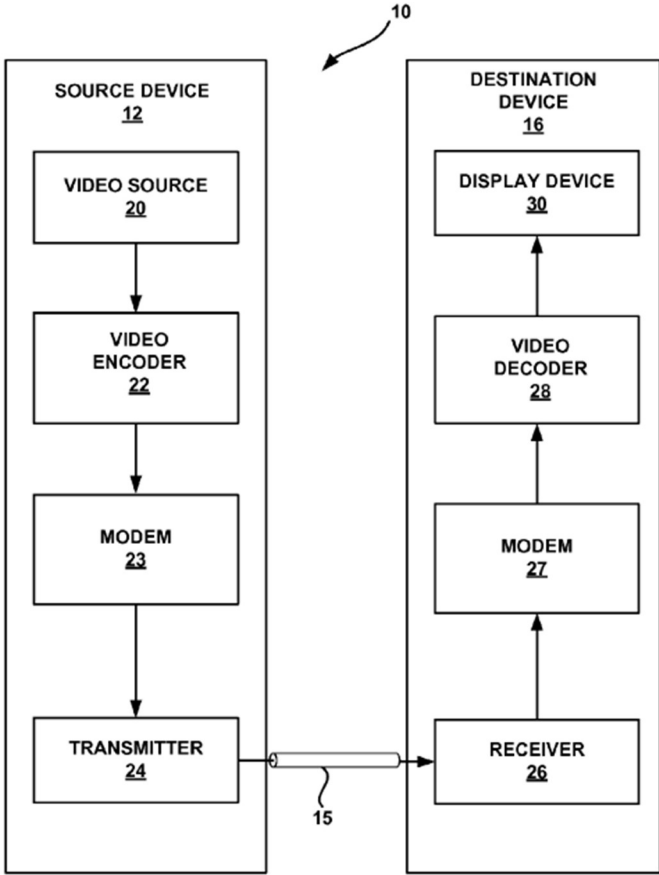
ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
<p>1[preamble] A method for encoding a block of pixels, the method comprising:</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses a method for encoding a block of pixels.</p> <p><i>See, e.g.,</i></p> <p>Karczewicz II, Abstract:</p> <p style="padding-left: 40px;">This disclosure describes various interpolation techniques performed by an encoder and a decoder during the motion compensation process of video coding. In one example, an encoder interpolates pixel values of reference video data based on a plurality of different pre-defined interpolation filters. In this example, the decoder receives a syntax element that identifies an interpolation filter, and interpolates pixel values of reference video data based on</p>

To the extent that these Invalidity Contentions rely on or otherwise embody particular constructions of terms or phrases in the Asserted Claims, Respondents are not proposing any such constructions as proper constructions of those terms or phrases. Various positions put forth in this document are predicated on Complainants’ incorrect and overly broad interpretation of its claims. Those positions are not intended to and do not necessarily reflect Respondents’ interpretation of the true and proper scope of Complainants’ claims, and Respondents reserve the right to adopt claim construction positions that differ from or even conflict with various positions put forth in this document.

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>the interpolation filter identified by the syntax element. In another example, a method of interpolating predictive video data includes generating half-pixel values based on integer pixel values, rounding the half-pixel values to generate half-pixel interpolated values, storing the half-pixel values as non-rounded versions of the half-pixel values, and generating quarter-pixel values based on the non-rounded versions of the half-pixel values and the integer pixel values.</p> <p>Karczewicz II, ¶0002:</p> <p>This disclosure relates to digital video coding and, more particularly, fractional interpolations of predictive data used in video coding.</p> <p>Karczewicz II, ¶0004:</p> <p>Block based inter-coding is a very useful coding technique that relies on temporal prediction to reduce or remove temporal redundancy between video blocks of successive coded units of a video sequence. The coded units may comprise video frames, slices of video frames, groups of pictures, or another defined unit of video blocks. For inter-coding, the video encoder performs motion estimation and motion compensation to track the movement of corresponding video blocks of two or more adjacent coded units. Motion estimation generates motion vectors, which indicate the displacement of video blocks relative to corresponding prediction video blocks in one or more reference frames or other coded units. Motion compensation uses the motion vectors to generate prediction video blocks from the reference frame or other coded unit. After motion compensation, residual video blocks are formed by subtracting prediction video blocks from the original video blocks being coded.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>Karczewicz II, ¶0012:</p> <p>In one example, this disclosure provides a method of encoding video data. The method comprises generating prediction data, wherein generating the prediction data includes interpolating pixel values of reference video data based on a plurality of different pre-defined interpolation filters. The method also comprises encoding the video data based on the prediction data.</p> <p>Karczewicz II, ¶0014:</p> <p>In another example, this disclosure provides a method of interpolating predictive video data for video coding. The method comprises generating half-pixel values based on integer pixel values, rounding the half-pixel values to generate half-pixel interpolated values, storing the half-pixel values as non-rounded versions of the half-pixel values, and generating quarter-pixel values based on the non-rounded versions of the half-pixel values and the integer pixel values.</p> <p>Karczewicz II, ¶0035:</p> <p>This disclosure describes various interpolation techniques performed by an encoder and a decoder during the motion compensation process of video coding. According to one aspect of this disclosure, the encoder may apply a plurality of pre-defined interpolation filters in order to generate a plurality of different interpolated prediction data. The interpolated prediction data that achieves the highest levels of compression may be selected at the encoder, and the interpolation filter that was used may be coded as syntax and communicated to the decoding device as part of the coded video data. The interpolated prediction data comprises reference data. Video data to be coded (e.g., a video block) may be subtracted from interpolated prediction data (e.g., an interpolated predictive reference block) in order to define a residual</p>

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	<p>block of encoded data, which may then be transformed, quantized and entropy coded.</p> <p>Karczewicz II, Fig.1:</p>

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	 <p data-bbox="737 1339 814 1372">FIG. 1</p>

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	<p>Karczewicz II, ¶0041:</p> <p>In the example of FIG. 1, source device 12 may include a video source 20, video encoder 22, a modulator/demodulator (modem) 23 and a transmitter 24. Destination device 16 may include a receiver 26 a modem 27, a video decoder 28, and a display device 30. In accordance with this disclosure, video encoder 22 of source device 12 may be configured to apply one or more of the interpolation techniques of this disclosure as part of a video encoding process. Similarly, video decoder 28 of destination device 16 may be configured to apply one or more of the interpolation techniques of this disclosure as part of a video decoding process.</p> <p>Karczewicz II, Fig. 2:</p>

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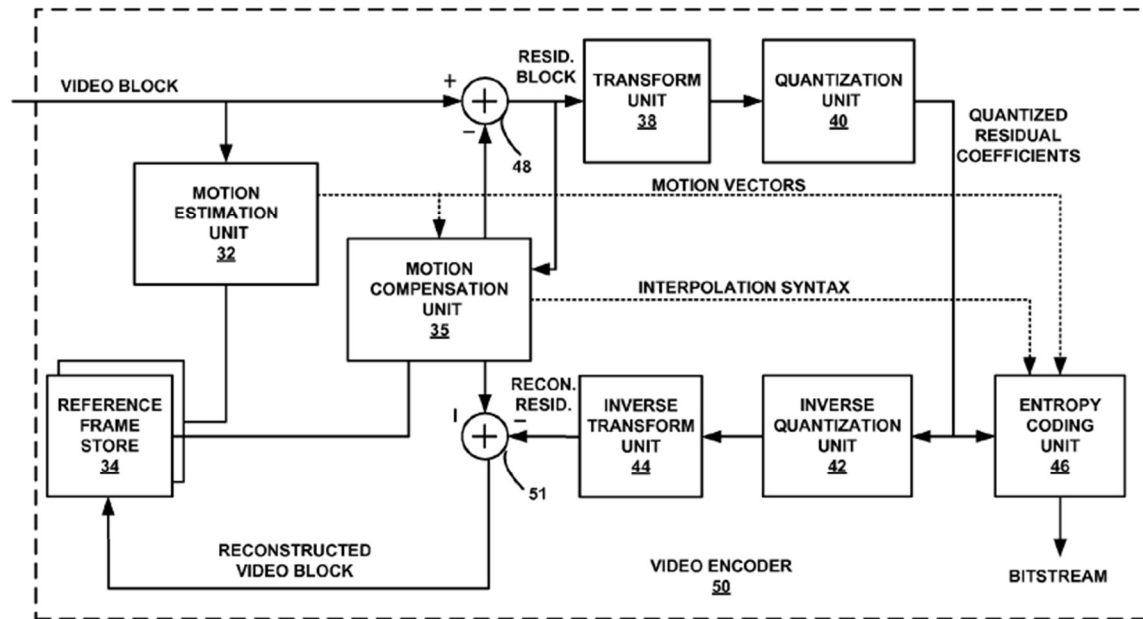


FIG. 2

Karczewicz II, ¶0049:

A video sequence typically includes a series of video frames. Video encoder 22 operates on video blocks within individual video frames in order to encode the video data. The video blocks may have fixed or varying sizes, and may differ in size according to a specified coding standard. Each video frame includes a series of slices. Each slice may include a series of macroblocks, which may be arranged into sub-blocks. As an example, the ITU-T

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	<p>H.264 standard supports intra prediction in various block sizes, such as 16 by 16, 8 by 8, or 4 by 4 for luma components, and 8×8 for chroma components, as well as inter prediction in various block sizes, such as 16 by 16, 16 by 8, 8 by 16, 8 by 8, 8 by 4, 4 by 8 and 4 by 4 for luma components and corresponding scaled sizes for chroma components. Video blocks may comprise blocks of pixel data, or blocks of transformation coefficients, e.g., following a transformation process such as discrete cosine transform or a conceptually similar transformation process.</p> <p>Karczewicz II, ¶0050:</p> <p>Smaller video blocks can provide better resolution, and may be used for locations of a video frame that include high levels of detail. In general, macroblocks (MBs) and the various sub-blocks may be considered to be video blocks. In addition, a slice may be considered to be a series of video blocks, such as MBs and/or sub-blocks. Each slice may be an independently decodable unit of a video frame. Video encoder 22 and video decoder 28 perform inter-based predictive coding, which involves the generation of predictive reference data and the subtraction of a video block to be coded from the predictive reference data to generate residual data, which may then be transformed, quantized and entropy coded. The inter-based predictive coding may include interpolation of the predictive data in accordance with this disclosure.</p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
1[a] determining, for a current block, a first reference block	Karczewicz II, as evidenced by the example citations below, discloses determining, for a current block, a first reference block based on a first motion vector and a second reference block based on a

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<p>based on a first motion vector and a second reference block based on a second motion vector, wherein the pixels of the current block, the first reference block, and the second reference block have values with a first precision;</p>	<p>second motion vector, wherein the pixels of the current block, the first reference block, and the second reference block have values with a first precision.</p> <p>Karczewicz II, ¶0004:</p> <p>Block based inter-coding is a very useful coding technique that relies on temporal prediction to reduce or remove temporal redundancy between video blocks of successive coded units of a video sequence. The coded units may comprise video frames, slices of video frames, groups of pictures, or another defined unit of video blocks. For inter-coding, the video encoder performs motion estimation and motion compensation to track the movement of corresponding video blocks of two or more adjacent coded units. Motion estimation generates motion vectors, which indicate the displacement of video blocks relative to corresponding prediction video blocks in one or more reference frames or other coded units. Motion compensation uses the motion vectors to generate prediction video blocks from the reference frame or other coded unit. After motion compensation, residual video blocks are formed by subtracting prediction video blocks from the original video blocks being coded.</p> <p>Karczewicz II, Fig. 2:</p>

ASSERTED CLAIMS

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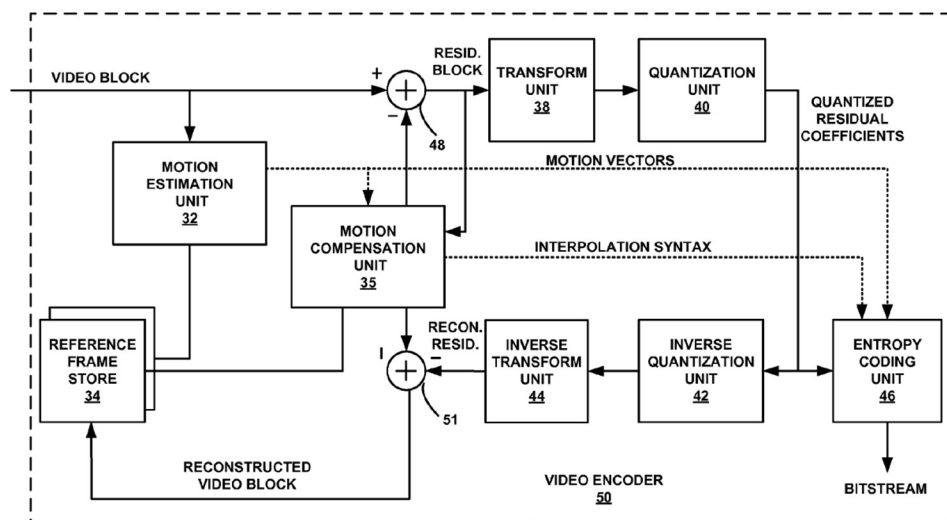


FIG. 2

Karczewicz II, ¶0056:

During the encoding process, video encoder 50 receives a video block to be coded, and motion estimation unit 32 and motion compensation unit 35 perform inter-predictive coding. Motion estimation unit 32 and motion compensation unit 35 may be highly integrated, but are illustrated separately for conceptual purposes. Motion estimation is typically considered the process of generating motion vectors, which estimate motion for video blocks. A motion vector, for example, may indicate the displacement of a predictive block within a predictive frame (or other coded unit) relative to the current block being coded within the current frame (or other coded unit). Motion compensation is typically considered the process of fetching or

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	<p>generating the predictive block based on the motion vector determined by motion estimation. Again, motion estimation unit 32 and motion compensation unit 35 may be functionally integrated. The interpolation techniques described in this disclosure are described as being performed by motion compensation unit 35. However, interpolation may be performed during motion estimation in order to facilitate the selection of the best motion vectors.</p> <p>Karczewicz II, ¶0057:</p> <p>In accordance with this disclosure, motion estimation 32 selects the appropriate motion vector for the video block to be coded by comparing the video block to video blocks of a predictive coded unit (e.g., a previous frame). At this point, motion compensation unit 35 may perform interpolation in order to generate predictive data at sub-pixel resolution. In some cases, during motion estimation, the interpolation may be based on a fixed interpolation filter. In other cases, the different interpolation filters applied during motion compensation (as outlined below) may also be used during the motion estimation process for purposes of motion vector selection.</p> <p>Karczewicz II, ¶0058:</p> <p>Once motion estimation unit 32 has selected the motion vector for the video block to be coded, motion compensation unit 35 generates the predictive video block associated with that motion vector. According to this disclosure, however, motion compensation unit 35 may consider several versions of any predictive video block that has sub-pixel resolution. In this case, motion compensation unit 35 may apply a plurality of pre-defined interpolation filters in order to generate a plurality of different interpolated prediction data for the video block to be coded. Motion compensation unit 35 then selects the interpolated prediction data (e.g., an interpolated video block associated with one of the interpolation filters) that achieves the highest levels of compression. The interpolation filter that was used to generate the</p>

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	<p>interpolated data may be coded as interpolation syntax and communicated to entropy coding unit 46 for inclusion in the coded bitstream. Once motion compensation unit 35 has selected and applied the best interpolation filter, the motion compensation unit 35 generates the predictive data using that interpolation filter, and video encoder applies adder 48 to subtract that predictive data from the video block being coded to generate residual data.</p> <p>Karczewicz II, ¶0073:</p> <p>After motion estimation, the best motion vector for a given video block may be identified, e.g., possibly using a rate-distortion model to balance the coding rate and quality. Then, the prediction video block is formed during motion compensation using the best motion vector. As outlined above, the residual video block is formed by subtracting the prediction video block from the original video block. A transform is then applied on the residual block, and the transform coefficients are quantized and entropy coded to further reduce bit rate.</p> <p>Karczewicz II, ¶0089:</p> <p>The average filter for a particular sub-pixel position may also be normalized such that all filter coefficients add up to 1.0. The average filter may also be quantized to a certain fixed-point precision (e.g., 13-bit precision). The use of fixed-point precision in the filter coefficient ensures that implementations across different platforms will not have drifts. Furthermore, a true fixed-point implementation may be derived from such pre-defined filter coefficients. Any customized filters may have different characteristics. Specifically, different filters may be non-separable filters or separable filters, and different filters may define different filter supports (e.g., 6×6 or 4×4).</p>

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	<p>Karczewicz II, ¶0103:</p> <p>Unfortunately, interpolation of value “j” can be more complicated since the interpolation at position j requires two levels of half-pixel interpolation. In particular, to interpolate “j,” twenty bits may be required to represent the intermediate value “j1.” In this case, the intermediate version “j1” of “j” may be rounded to fit within sixteen bits. In this case, the operations discussed above may be implemented with minor modification so that all operations can fit within sixteen bit data structures without sacrificing precision. The interpolation of “b” and “h” may be the same as that defined above, except that their intermediate versions “b1” and “h1” may be stored for other sub-pixel interpolation. The following Tables show the interpolation process for other sub-pixels in sixteen bit storage elements. In the Tables below, the operations defined in each column are performed sequentially through the respective table. Shaded rows contain operations that are performed at the end of the process, and are not used for interpolation of other sub-pixel positions.</p>

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	<div>TABLE 5</div> <div><div>positions {a, c, d, l} of FIGS. 4A-4D</div><table><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r1 = x</td><td>r1 is integer pixel x</td><td>0</td><td>255</td><td>8u</td></tr><tr><td>r1 = r1 << 5</td><td>r1 is 32 * x</td><td>0</td><td>8160</td><td>13u</td></tr><tr><td>r2 = y0</td><td>r2 is y0 (y0 is a one-dimensional (1-D) half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r1 = r1 + r2</td><td>r1 is 32 * x + y0</td><td>-2550</td><td>18870</td><td>16s</td></tr><tr><td>r1 = r1 + 32</td><td>r1 is 32 * x + y0 + 32</td><td>-2518</td><td>18902</td><td>16s</td></tr><tr><td>r1 = r1 >> 6</td><td>r1 is (32 * x + y0 + 32) >> 6</td><td>-39</td><td>295</td><td>11s</td></tr><tr><td>r1 = max (0, r1)</td><td>clip r1 on the low side</td><td>0</td><td>295</td><td>10u</td></tr><tr><td>r1 = min (255, r1)</td><td>clip r1 on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table></div>	Operation	Comment	Min value	Max value	Register size	r1 = x	r1 is integer pixel x	0	255	8u	r1 = r1 << 5	r1 is 32 * x	0	8160	13u	r2 = y0	r2 is y0 (y0 is a one-dimensional (1-D) half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r1 = r1 + r2	r1 is 32 * x + y0	-2550	18870	16s	r1 = r1 + 32	r1 is 32 * x + y0 + 32	-2518	18902	16s	r1 = r1 >> 6	r1 is (32 * x + y0 + 32) >> 6	-39	295	11s	r1 = max (0, r1)	clip r1 on the low side	0	295	10u	r1 = min (255, r1)	clip r1 on the high side	0	255	8u
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	<p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
<p>1[b] using said first reference block to obtain a first prediction, said first prediction having a second precision, which is higher than said first precision;</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses using the first reference block to obtain a first prediction, the first prediction having a second precision, which is higher than the first precision.</p> <p><i>See, e.g.,</i></p> <p>Karczewicz II, ¶0010:</p> <p>In addition, this disclosure also recognizes coding inefficiencies due to conventional rounding of half-pixel values, and provides techniques that may improve interpolation by reducing or eliminating intermediate rounding. In this case, interpolated half-pixel values may be rounded for purposes of half-pixel interpolation. Quarter-pixel values, however, which may be generated based on one or more of the interpolated half-pixel values, may rely on non-rounded versions of the half-pixel values. This can eliminate propagation of rounding inaccuracies from the half-pixel values to the quarter-pixel values. In some cases, slight rounding without sacrificing the accuracy of the final values may be applied to one specific half-pixel value in order to ensure that sixteen-bit storage elements can be used to store any intermediate values of half-pixels. In particular, when fifteen possible sub-pixel locations are defined for every pixel location, one of the specific half-pixel values may need to be generated based on other half-pixel values (i.e., requiring two levels of half-pixel interpolation), and this specific half-pixel value may require rounding to ensure that sixteen-bit storage elements can be used to store all interpolated values.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>Karczewicz II, ¶0038:</p> <p>In another aspect of this disclosure, interpolation techniques are described that eliminate or significantly reduce intermediate rounding of half-pixel values for quarter-pixel interpolation. The techniques that eliminate or significantly reduce intermediate rounding may be used with or without the techniques mentioned above that use pre-defined interpolation filters. In other words, the techniques that eliminate or significantly reduce intermediate rounding may be used during interpolation according to one or more pre-defined interpolation filters, but could also be used with interpolation that uses a fixed interpolation filter or possibly with adaptive refinement interpolations techniques.</p> <p>Karczewicz II, ¶0053:</p> <p>Also, according to another aspect of this disclosure, video encoder 22 and video decoder 28 may apply interpolation techniques that eliminate or significantly reduce intermediate rounding of half-pixel values for quarter-pixel interpolation. In this case, video encoder 22 and video decoder 28 may round the interpolated half-pixel values purposes of half-pixel interpolation. For quarter-pixel interpolation, however, video encoder 22 and video decoder 28 may store and use non-rounded versions of the half-pixel values in order to reduce or eliminate propagation of rounding inaccuracies from the half-pixel values to the quarter-pixel values. In some cases, slight rounding may be applied to one particular half-pixel value that requires two levels of interpolation in order to ensure that a fixed size storage elements (e.g., sixteen-bit registers) can be used to store any intermediate values. In some cases, the impact of the slight rounding on the final accuracy can be made to be zero by performing the rounding properly, as described herein.</p>

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	<p>Karczewicz II, ¶0059:</p> <p>Also, according to another aspect of this disclosure, video encoder 22 and video decoder 28 may apply interpolation techniques that eliminate or significantly reduce intermediate rounding of half-pixel values for quarter-pixel interpolation. In this case, video encoder 22 and video decoder 28 may round the interpolated half-pixel values for purposes of half-pixel interpolation. For quarter-pixel interpolation, however, video encoder 22 and video decoder 28 may store and use non-rounded versions of the half-pixel values in order to reduce or eliminate propagation of rounding inaccuracies from the half-pixel values to the quarter-pixel values. In some cases, slight rounding may be applied to one particular half-pixel value that requires two levels of interpolation in order to ensure that fixed size storage elements (e.g., 16-bit registers) can be used to store any intermediate values.</p> <p>Karczewicz II, ¶¶0093-0094:</p> <p>A sub-pixel motion vector refers to a sub-pixel position in a reference picture which needs to be interpolated. H.264 defines one interpolation process for sub-pixels in which sub-pixels b and h (see FIGS. 4A-4D) may be calculated by horizontal and vertical filtering with a 6-tap filter having tap values (1, -5, 20, 20, -5, 1) as follows:</p> $b1 = C1 - 5 * C2 + 20 * C3 + 20 * C4 - 5 * C5 + C6$ <p>where “C1,” “C2,” “C3,” “C4,” “C5” and “C6” represent the six closest integer pixels that surround “b” in the horizontal direction, with pixels “C3” and “C4” being the closest, “C2” and “C5” being the next closest, and “C1” and “C6” being the next closest.</p> $h1 = A3 - 5 * B3 + 20 * C3 + 20 * D3 - 5 * E3 + F3$

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II									
	<p>where “A3,” “B3,” “C3,” “D3,” “E3” and “F3” represent the six closest integer pixels that surround “h” in the vertical direction, with pixels “C3” and “D3” being the closest, “B3” and “E3” being the next closest, and “A3” and “F3” being the next closest.</p> <p>In this case, the values of “b” and “h” may be defined as:</p> <p>$b=\max(0, \min(255, (b1+16)>>5))$</p> <p>$h=\max(0, \min(255, (h1+16)>>5))$</p> <p>where “>>” is a right-shift operation. In this disclosure, “>>” represents a right shift operation and “<<” represents a left shift operation.</p> <p>Karczewicz II, ¶0096:</p> <p>The pixels at quarter-pixel positions labeled as “a,” “c,” “d,” “l,” “f,” “i,” “k,” and “n” may be derived according to Table 1 by averaging the two nearest pixels at integer and half-pixel positions and then applying upward rounding.</p> <table><tr><th>TABLE 1</th></tr><tr><td>$a = (C3 + b + 1) >> 1$</td></tr><tr><td>$c = (C4 + b + 1) >> 1$</td></tr><tr><td>$d = (C3 + h + 1) >> 1$</td></tr><tr><td>$l = (D3 + h + 1) >> 1$</td></tr><tr><td>$f = (j + b + 1) >> 1$</td></tr><tr><td>$i = (j + h + 1) >> 1$</td></tr><tr><td>$k = (j + ee + 1) >> 1$</td></tr><tr><td>$n = (j + hh + 1) >> 1$</td></tr></table>	TABLE 1	$a = (C3 + b + 1) >> 1$	$c = (C4 + b + 1) >> 1$	$d = (C3 + h + 1) >> 1$	$l = (D3 + h + 1) >> 1$	$f = (j + b + 1) >> 1$	$i = (j + h + 1) >> 1$	$k = (j + ee + 1) >> 1$	$n = (j + hh + 1) >> 1$
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	<p>Karczewicz II, ¶¶0099-0100:</p> <p>In order to keep the highest possible precision through the intermediate steps, any shifting, rounding and clipping operations may be avoided until the very last step of the interpolation process. Unlike in the H.264 standard defined implementation, the pixels at quarter-pixel positions labeled as “a,” “c,” “d,” “l,” “f,” “i,” “k,” and “n” are derived according to Table 3 by averaging the two nearest pixels at integer and half-pixel positions “b,” “h,” “j,” “ee” and “hh,” with upward rounding.</p> <div style="text-align: center;"> <p>TABLE 3</p> <hr/> $\begin{aligned} a &= (C3 \ll 5 + b1 + 32) \gg 6 \\ c &= (C4 \ll 5 + b1 + 32) \gg 6 \\ d &= (C3 \ll 5 + h1 + 32) \gg 6 \\ l &= (D3 \ll 5 + h1 + 32) \gg 6 \\ f &= (j1 \gg 5 + b1 + 32) \gg 6 \\ i &= (j1 \gg 5 + h1 + 32) \gg 6 \\ k &= (j1 \gg 5 + ee1 + 32) \gg 6 \\ n &= (j1 \gg 5 + hh1 + 32) \gg 6 \end{aligned}$ <hr/> </div> <p>In this case, the intermediate values denoted as “ee1” and “hh1” are derived by applying the 6-tap filter vertically and horizontally in the same manner as the calculation of “h1” and “b1” listed above, but at the positions of “ee” and “hh.” Rounding may be avoided at this stage in the generation of “ee1” and “hh1” as described herein.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>Karczewicz II, ¶0103:</p> <p>Unfortunately, interpolation of value “j” can be more complicated since the interpolation at position j requires two levels of half-pixel interpolation. In particular, to interpolate “j,” twenty bits may be required to represent the intermediate value “j1.” In this case, the intermediate version “j1” of “j” may be rounded to fit within sixteen bits. In this case, the operations discussed above may be implemented with minor modification so that all operations can fit within sixteen bit data structures without sacrificing precision. The interpolation of “b” and “h” may be the same as that defined above, except that their intermediate versions “b1” and “h1” may be stored for other sub-pixel interpolation. The following Tables show the interpolation process for other sub-pixels in sixteen bit storage elements. In the Tables below, the operations defined in each column are performed sequentially through the respective table. Shaded rows contain operations that are performed at the end of the process, and are not used for interpolation of other sub-pixel positions.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																																													
	<div>TABLE 5</div> <div><div>positions {a, c, d, l} of FIGS. 4A-4D</div><table><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r1 = x</td><td>r1 is integer pixel x</td><td>0</td><td>255</td><td>8u</td></tr><tr><td>r1 = r1 << 5</td><td>r1 is 32 * x</td><td>0</td><td>8160</td><td>13u</td></tr><tr><td>r2 = y0</td><td>r2 is y0 (y0 is a one-dimensional (1-D) half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r1 = r1 + r2</td><td>r1 is 32 * x + y0</td><td>-2550</td><td>18870</td><td>16s</td></tr><tr><td>r1 = r1 + 32</td><td>r1 is 32 * x + y0 + 32</td><td>-2518</td><td>18902</td><td>16s</td></tr><tr><td>r1 = r1 >> 6</td><td>r1 is (32 * x + y0 + 32) >> 6</td><td>-39</td><td>295</td><td>11s</td></tr><tr><td>r1 = max (0, r1)</td><td>clip r1 on the low side</td><td>0</td><td>295</td><td>10u</td></tr><tr><td>r1 = min (255, r1)</td><td>clip r1 on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table></div> <div>Karczewicz II, ¶¶0097-0098: The pixels at quarter-pixel positions labeled as “e,” “g,” “m,” and “o” are calculated according to Table 2 by averaging the two nearest pixels at half-pixel positions in the diagonal direction and then applying upward rounding.</div>	Operation	Comment	Min value	Max value	Register size	r1 = x	r1 is integer pixel x	0	255	8u	r1 = r1 << 5	r1 is 32 * x	0	8160	13u	r2 = y0	r2 is y0 (y0 is a one-dimensional (1-D) half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r1 = r1 + r2	r1 is 32 * x + y0	-2550	18870	16s	r1 = r1 + 32	r1 is 32 * x + y0 + 32	-2518	18902	16s	r1 = r1 >> 6	r1 is (32 * x + y0 + 32) >> 6	-39	295	11s	r1 = max (0, r1)	clip r1 on the low side	0	295	10u	r1 = min (255, r1)	clip r1 on the high side	0	255	8u
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ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																				
	<div data-bbox="732 428 1554 631" data-label="Table"> <table> <tr> <th colspan="2">TABLE 2</th></tr> <tr> <td>$e = (b + h + 1) \gg 1$</td><td></td></tr> <tr> <td>$g = (b + ee + 1) \gg 1$</td><td></td></tr> <tr> <td>$m = (h + hh + 1) \gg 1$</td><td></td></tr> <tr> <td>$o = (ee + hh + 1) \gg 1$</td><td></td></tr> </table> </div> <p data-bbox="705 691 1906 761">The final values of those quarter-pixels may be finally clipped to values in the interval (0,255).</p> <p data-bbox="611 802 982 834">Karczewicz II, ¶¶0101-0102:</p> <p data-bbox="705 870 1906 976">The pixels at quarter-pixel positions labeled as “e,” “g,” “m,” and “o” may be calculated according to Table 4 by averaging the two nearest pixels at half-pixel positions in the diagonal direction with upward rounding.</p> <div data-bbox="705 1027 1526 1230" data-label="Table"> <table> <tr> <th colspan="2">TABLE 4</th></tr> <tr> <td>$e = (b1 + h1 + 32) \gg 6$</td><td></td></tr> <tr> <td>$g = (b1 + ee1 + 32) \gg 6$</td><td></td></tr> <tr> <td>$m = (h1 + hh1 + 32) \gg 6$</td><td></td></tr> <tr> <td>$o = (ee1 + hh1 + 32) \gg 6$</td><td></td></tr> </table> </div> <p data-bbox="705 1295 1906 1401">The final values of those quarter-pixels may be clipped to values in the range (0,255). By preserving the full precision of the intermediate values, the interpolated sub-pixels will be more accurate. In particular, half-pixel values at locations “b,” “h,” “ee,” and “hh” may be</p>	TABLE 2		$e = (b + h + 1) \gg 1$		$g = (b + ee + 1) \gg 1$		$m = (h + hh + 1) \gg 1$		$o = (ee + hh + 1) \gg 1$		TABLE 4		$e = (b1 + h1 + 32) \gg 6$		$g = (b1 + ee1 + 32) \gg 6$		$m = (h1 + hh1 + 32) \gg 6$		$o = (ee1 + hh1 + 32) \gg 6$	
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ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>unrounded values denoted as “b1,” “h1,” “ee1,” and “hh1” for purposes of quarter-pixel generation.</p> <p>Karczewicz II, ¶0103:</p> <p>Unfortunately, interpolation of value “j” can be more complicated since the interpolation at position j requires two levels of half-pixel interpolation. In particular, to interpolate “j,” twenty bits may be required to represent the intermediate value “j1.” In this case, the intermediate version “j1” of “j” may be rounded to fit within sixteen bits. In this case, the operations discussed above may be implemented with minor modification so that all operations can fit within sixteen bit data structures without sacrificing precision. The interpolation of “b” and “h” may be the same as that defined above, except that their intermediate versions “b1” and “h1” may be stored for other sub-pixel interpolation. The following Tables show the interpolation process for other sub-pixels in sixteen bit storage elements. In the Tables below, the operations defined in each column are performed sequentially through the respective table. Shaded rows contain operations that are performed at the end of the process, and are not used for interpolation of other sub-pixel positions.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																																													
	<div>TABLE 6</div> <table><tr><th colspan="5">positions {e, g, m, o} of FIGS. 4A-4D</th></tr><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r1 = y0</td><td>r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r2 = y1</td><td>r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r1 = r1 + r2</td><td>r1 is y0 + y1</td><td>-5100</td><td>21420</td><td>16s</td></tr><tr><td>r1 = r1 + 32</td><td>r1 is y0 + y1 + 32</td><td>-5068</td><td>21452</td><td>16s</td></tr><tr><td>r1 = r1 >> 6</td><td>r1 is (y0 + y1 + 32) >> 6</td><td>-79</td><td>335</td><td>11s</td></tr><tr><td>r1 = max(0, r1)</td><td>clip r1 on the low side</td><td>0</td><td>335</td><td>10u</td></tr><tr><td>r1 = min(255, r1)</td><td>clip r1 on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table> <p>Karczewicz II, ¶0095:</p> <p>To interpolate sub-pixel “j,” an intermediate value “j1” is first derived as:</p> <p>$j1=aa1-5*bb1+20*b1+20*hh1-5*ii1+jj1,$</p> <p>where the intermediate values denoted as “aa1,” “bb1,” “hh1,” “ii1” and “jj1” are derived by applying the 6-tap filter horizontally in the same manner as the calculation of b1 at the positions of “aa,” “bb,” “hh,” “ii” and “jj.” The final value j may be calculated as:</p>	positions {e, g, m, o} of FIGS. 4A-4D					Operation	Comment	Min value	Max value	Register size	r1 = y0	r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r2 = y1	r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r1 = r1 + r2	r1 is y0 + y1	-5100	21420	16s	r1 = r1 + 32	r1 is y0 + y1 + 32	-5068	21452	16s	r1 = r1 >> 6	r1 is (y0 + y1 + 32) >> 6	-79	335	11s	r1 = max(0, r1)	clip r1 on the low side	0	335	10u	r1 = min(255, r1)	clip r1 on the high side	0	255	8u
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ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>$j = \max(0, \min(255, (j1 + 512) \gg 10))$.</p> <p>Karczewicz II, ¶0104-0106:</p> <p>Table 7, below, illustrates a sixteen-bit implementation of deriving pixel value at pixel location “j.” All 1-D half-pixels “aa1,” “bb1,” “b1,” “hh1,” “ii1” and “jj1” are obtained without any shift down. The clipping range to avoid overflow is (−2372,18640). Again shaded rows in the table indicate operations that are only performed at the end of the interpolation process, and these results are not used for the interpolation of other sub-pixel positions. The intermediate value “j1” will be saved for the interpolation of “f,” “i,” “k” and “n.”</p>

ASSERTED CLAIMS

PRIOR ART REFERENCE: KARCZEWICZ II

TABLE 7

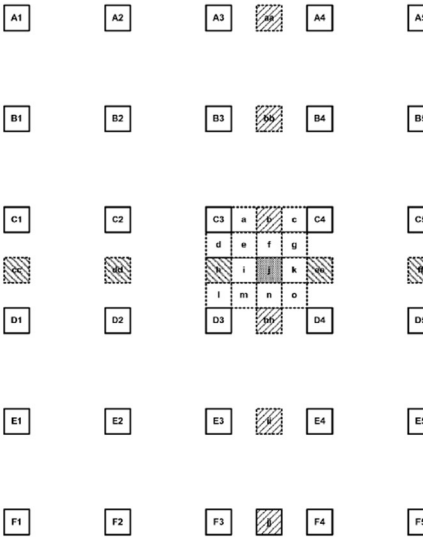
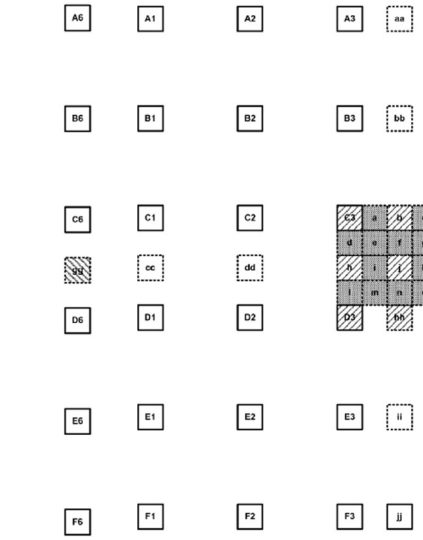
position {j} of FIGS. 4A-4D

Operation	Comment	Min value	Max value	Register size
$r1 = y0$	y0 is 1-D half-pixel aal before shifting down	-2550	10710	15s
$r2 = y5$	y5 is 1-D half-pixel jj1 before shifting down	-2550	10710	15s
$r1 = r1 + r2$	r1 is $y0 + y5$	-5100	21420	16s
$r2 = y1$	y1 is 1-D half-pixel bb1 before shifting down	-2550	10710	15s
$r3 = y4$	y4 is 1-D half-pixel iil before shifting down	-2550	10710	15s
$r2 = r2 + r3$	r2 is $y1 + y4$	-5100	21420	16s
$r1 = r1 - r2$	r1 is $(y0 + y5) - (y1 + y4)$	-26520	26520	16s
$r1 = r1 \gg 2$	r1 is $((y0 + y5) - (y1 + y4)) \gg 2$	-6630	6630	14s
$r3 = y2$	y2 is 1-D half-pixel b1 before shifting down	-2550	10710	15s
$r4 = y3$	y3 is 1-D half-pixel hh1 before shifting down	-2550	10710	15s
$r3 = r3 + r4$	r3 is $y2 + y3$	-5100	21420	16s
$r3 = \max(-2372, r3)$	clip r3 to avoid overflow (low side)	-2372	21420	16s
$r3 = \min(18640, r3)$	clip r3 to avoid overflow (high side)	-2372	18640	16s
$r2 = r3 - r4$	r2 is $(y2 + y3) - (y1 + y4)$	-23792	23740	16s
$r1 = r1 + r2$	r1 is $((y0 + y5) - 5^* (y1 + y4)) + 4^* (y2 + y3) \gg 2$	-30422	30370	16s
$r1 = r1 \gg 2$	r1 is $((y0 + y5) - 5^* (y1 + y4)) + 4^* (y2 + y3) \gg 4$	-7605	7592	14s
$r1 = r1 + r3$	r1 is $((y0 + y5) - 5^* (y1 + y4)) + 20^* (y2 + y3) \gg 4$	-9977	26232	16s
$j1 = r1$	store result			
$r1 = r1 + 32$	r1 is $((y0 + y5) - 5^* (y1 + y4)) + 20^* (y2 + y3) + 512) \gg 4$	-9945	26264	16s
$r1 = r1 \gg 5$	r1 is $((y0 + y5) - 5^* (y1 + y4)) + 20^* (y2 + y3) + 512) \gg 10$	-155	410	10s
$r1 = \max(0, r1)$	clip r1 on the low side	0	410	9a
$r1 = \min(255, r1)$	clip r1 on the high side	0	255	8a

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																				
	<p>Table 8, below demonstrates steps that can be taken for sixteen-bit implementation of interpolating {f,i,k,n}, which are the positions that use to interpolate the intermediate value “j1.”</p> <p style="text-align: center;">TABLE 8</p> <table><tr><th colspan="5">positions {f, i, k, n} of FIGS. 4A-4D</th></tr><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r1 = y0</td><td>r1 is y0 (1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r2 = j1</td><td>r2 is j1 (2-D half-pixel j1 before shifting down)</td><td>-9914</td><td>26232</td><td>16s</td></tr></table>	positions {f, i, k, n} of FIGS. 4A-4D					Operation	Comment	Min value	Max value	Register size	r1 = y0	r1 is y0 (1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r2 = j1	r2 is j1 (2-D half-pixel j1 before shifting down)	-9914	26232	16s
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ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																																								
	<div>TABLE 8-continued</div> <table><tr><th colspan="5">positions {f, i, k, n} of FIGS. 4A-4D</th></tr><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r2 = r2 >> 1</td><td>r2 is j1 >> 1</td><td>-4957</td><td>13116</td><td>15s</td></tr><tr><td>r1 = r1 + r2</td><td>r1 is y0 + (j1 >> 1)</td><td>-7507</td><td>23826</td><td>16s</td></tr><tr><td>r1 = r1 + 32</td><td>r1 is y0 + (j1 >> 1) + 32</td><td>-7491</td><td>23842</td><td>16s</td></tr><tr><td>r1 = r1 >> 6</td><td>r1 is (y0 + (j1 >> 1) + 32) >> 6</td><td>-235</td><td>745</td><td>11s</td></tr><tr><td>r1 = max (0, r1)</td><td>clip r1 on the low side</td><td>0</td><td>745</td><td>10u</td></tr><tr><td>r1 = min (255, r1)</td><td>clip r1 on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table> <p>In short, the discussion above demonstrates a complete technique for interpolating every possible sub-pixel location “a,” “b,” “c,” “d,” “e,” “f,” “g,” “h,” “i,” “j,” “k,” “l,” “m,” “n,” and “o” without exceeding sixteen-bit storage elements, which is desirable for an implementation. Most intermediate half-pixel values remain unrounded for purposes of quarter-pixel interpolation. However, pixel location “j” is a special case that may require rounding of intermediate results for purposes of generating its half-pixel value since pixel location “j” requires two levels of half-pixel interpolation. In accordance with this disclosure, however, the rounding in pixel location “j” will not impact the accuracy of the final value of quarter-pixels which rely on “j.”</p>	positions {f, i, k, n} of FIGS. 4A-4D					Operation	Comment	Min value	Max value	Register size	r2 = r2 >> 1	r2 is j1 >> 1	-4957	13116	15s	r1 = r1 + r2	r1 is y0 + (j1 >> 1)	-7507	23826	16s	r1 = r1 + 32	r1 is y0 + (j1 >> 1) + 32	-7491	23842	16s	r1 = r1 >> 6	r1 is (y0 + (j1 >> 1) + 32) >> 6	-235	745	11s	r1 = max (0, r1)	clip r1 on the low side	0	745	10u	r1 = min (255, r1)	clip r1 on the high side	0	255	8u
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ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>Karczewicz II, Figs. 4A-4D:</p> <div><div><div><div>A1</div><div>A2</div><div>A3</div><div>aa</div><div>A4</div><div>A5</div><div>A6</div><div>A1</div><div>A2</div><div><div></div></div><div>aa</div><div>A4</div><div>A5</div><div>A6</div></div><div><div>B1</div><div>B2</div><div>B3</div><div>bb</div><div>B4</div><div>B5</div><div>B6</div><div>B1</div><div>B2</div><div><div></div></div><div>bb</div><div>B4</div><div>B5</div><div>B6</div></div><div><div>C1</div><div>C2</div><div><div>C3</div><div>a</div><div>b</div><div>c</div><div>C4</div></div><div>C5</div><div>C6</div><div><div></div></div><div><div></div></div><div><div><div><div>d</div><div>e</div><div>f</div><div>g</div></div><div><div>h</div><div>i</div><div>j</div><div>k</div><div>ee</div></div><div><div>l</div><div>m</div><div>n</div><div>o</div></div></div></div><div><div>ff</div><div>gg</div><div>cc</div><div>dd</div></div><div><div>D1</div><div>D2</div><div>D3</div><div>hh</div><div>D4</div><div>D5</div><div>D6</div><div>D1</div><div>D2</div><div><div></div></div><div>hh</div><div>D4</div><div>D5</div><div>D6</div></div><div><div>E1</div><div>E2</div><div>E3</div><div>ii</div><div>E4</div><div>E5</div><div>E6</div><div>E1</div><div>E2</div><div><div></div></div><div>ii</div><div>E4</div><div>E5</div><div>E6</div></div><div><div>F1</div><div>F2</div><div>F3</div><div>jj</div><div>F4</div><div>F5</div><div>F6</div><div>F1</div><div>F2</div><div><div></div></div><div>jj</div><div>F4</div><div>F5</div><div>F6</div></div></div><div>FIG. 4A</div><div>FIG. 4B</div></div></div>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<div style="display: flex; justify-content: space-around; align-items: flex-start;"> <div style="text-align: center;">  <p>FIG. 4C</p> </div> <div style="text-align: center;">  <p>FIG. 4D</p> </div> </div> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
1[c] using said second reference block to obtain a second prediction, said second	Karczewicz II, as evidenced by the example citations below, discloses using the second reference block to obtain a second prediction, the second prediction having the second precision.

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
prediction having the second precision;	<p><i>See, e.g.,</i></p> <p>Karczewicz II, ¶0010:</p> <p>In addition, this disclosure also recognizes coding inefficiencies due to conventional rounding of half-pixel values, and provides techniques that may improve interpolation by reducing or eliminating intermediate rounding. In this case, interpolated half-pixel values may be rounded for purposes of half-pixel interpolation. Quarter-pixel values, however, which may be generated based on one or more of the interpolated half-pixel values, may rely on non-rounded versions of the half-pixel values. This can eliminate propagation of rounding inaccuracies from the half-pixel values to the quarter-pixel values. In some cases, slight rounding without sacrificing the accuracy of the final values may be applied to one specific half-pixel value in order to ensure that sixteen-bit storage elements can be used to store any intermediate values of half-pixels. In particular, when fifteen possible sub-pixel locations are defined for every pixel location, one of the specific half-pixel values may need to be generated based on other half-pixel values (i.e., requiring two levels of half-pixel interpolation), and this specific half-pixel value may require rounding to ensure that sixteen-bit storage elements can be used to store all interpolated values.</p> <p>Karczewicz II, ¶0038:</p> <p>In another aspect of this disclosure, interpolation techniques are described that eliminate or significantly reduce intermediate rounding of half-pixel values for quarter-pixel interpolation. The techniques that eliminate or significantly reduce intermediate rounding may be used with or without the techniques mentioned above that use pre-defined interpolation filters. In other words, the techniques that eliminate or significantly reduce intermediate rounding may be used during interpolation according to one or more pre-defined interpolation filters, but could also be used with interpolation that uses a fixed interpolation filter or possibly with adaptive</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>refinement interpolations techniques.</p> <p>Karczewicz II, ¶0053:</p> <p>Also, according to another aspect of this disclosure, video encoder 22 and video decoder 28 may apply interpolation techniques that eliminate or significantly reduce intermediate rounding of half-pixel values for quarter-pixel interpolation. In this case, video encoder 22 and video decoder 28 may round the interpolated half-pixel values purposes of half-pixel interpolation. For quarter-pixel interpolation, however, video encoder 22 and video decoder 28 may store and use non-rounded versions of the half-pixel values in order to reduce or eliminate propagation of rounding inaccuracies from the half-pixel values to the quarter-pixel values. In some cases, slight rounding may be applied to one particular half-pixel value that requires two levels of interpolation in order to ensure that a fixed size storage elements (e.g., sixteen-bit registers) can be used to store any intermediate values. In some cases, the impact of the slight rounding on the final accuracy can be made to be zero by performing the rounding properly, as described herein.</p> <p>Karczewicz II, ¶0059:</p> <p>Also, according to another aspect of this disclosure, video encoder 22 and video decoder 28 may apply interpolation techniques that eliminate or significantly reduce intermediate rounding of half-pixel values for quarter-pixel interpolation. In this case, video encoder 22 and video decoder 28 may round the interpolated half-pixel values for purposes of half-pixel interpolation. For quarter-pixel interpolation, however, video encoder 22 and video decoder 28 may store and use non-rounded versions of the half-pixel values in order to reduce or eliminate propagation of rounding inaccuracies from the half-pixel values to the quarter-pixel values. In some cases, slight rounding may be applied to one particular half-pixel value that requires two levels of interpolation in order to ensure that fixed size storage elements (e.g.,</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>16-bit registers) can be used to store any intermediate values.</p> <p>Karczewicz II, ¶¶0093-0094:</p> <p>A sub-pixel motion vector refers to a sub-pixel position in a reference picture which needs to be interpolated. H.264 defines one interpolation process for sub-pixels in which sub-pixels b and h (see FIGS. 4A-4D) may be calculated by horizontal and vertical filtering with a 6-tap filter having tap values (1, -5, 20, 20, -5, 1) as follows:</p> $b1 = C1 - 5 * C2 + 20 * C3 + 20 * C4 - 5 * C5 + C6$ <p>where “C1,” “C2,” “C3,” “C4,” “C5” and “C6” represent the six closest integer pixels that surround “b” in the horizontal direction, with pixels “C3” and “C4” being the closest, “C2” and “C5” being the next closest, and “C1” and “C6” being the next closest.</p> $h1 = A3 - 5 * B3 + 20 * C3 + 20 * D3 - 5 * E3 + F3$ <p>where “A3,” “B3,” “C3,” “D3,” “E3” and “F3” represent the six closest integer pixels that surround “h” in the vertical direction, with pixels “C3” and “D3” being the closest, “B3” and “E3” being the next closest, and “A3” and “F3” being the next closest.</p> <p>In this case, the values of “b” and “h” may be defined as:</p> $b = \max(0, \min(255, (b1 + 16) >> 5))$ $h = \max(0, \min(255, (h1 + 16) >> 5))$ <p>where “>>” is a right-shift operation. In this disclosure, “>>” represents a right shift operation and “<<” represents a left shift operation.</p> <p>Karczewicz II, ¶0096:</p> <p>The pixels at quarter-pixel positions labeled as “a,” “c,” “d,” “l,” “f,” “i,” “k,” and “n” may be derived according to Table 1 by averaging the two nearest pixels at integer and half-pixel positions and then applying upward rounding.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<div>TABLE 1</div> <div>$a = (C3 + b + 1) >> 1$$c = (C4 + b + 1) >> 1$$d = (C3 + h + 1) >> 1$$l = (D3 + h + 1) >> 1$$f = (j + b + 1) >> 1$$i = (j + h + 1) >> 1$$k = (j + ee + 1) >> 1$$n = (j + hh + 1) >> 1$</div> <div>Karczewicz II, ¶¶0099-0100:</div> <div>In order to keep the highest possible precision through the intermediate steps, any shifting, rounding and clipping operations may be avoided until the very last step of the interpolation process. Unlike in the H.264 standard defined implementation, the pixels at quarter-pixel positions labeled as “a,” “c,” “d,” “l,” “f,” “i,” “k,” and “n” are derived according to Table 3 by averaging the two nearest pixels at integer and half-pixel positions “b,” “h,” “j,” “ee” and “hh,” with upward rounding.</div>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p style="text-align: center;">TABLE 3</p> <hr/> $ \begin{aligned} a &= (C3 \ll 5 + b1 + 32) \gg 6 \\ c &= (C4 \ll 5 + b1 + 32) \gg 6 \\ d &= (C3 \ll 5 + h1 + 32) \gg 6 \\ l &= (D3 \ll 5 + h1 + 32) \gg 6 \\ f &= (j1 \gg 5 + b1 + 32) \gg 6 \\ i &= (j1 \gg 5 + h1 + 32) \gg 6 \\ k &= (j1 \gg 5 + ee1 + 32) \gg 6 \\ n &= (j1 \gg 5 + hh1 + 32) \gg 6 \end{aligned} $ <hr/> <p>In this case, the intermediate values denoted as “ee1” and “hh1” are derived by applying the 6-tap filter vertically and horizontally in the same manner as the calculation of “h1” and “b1” listed above, but at the positions of “ee” and “hh.” Rounding may be avoided at this stage in the generation of “ee1” and “hh1” as described herein.</p> <p>Karczewicz II, ¶0103:</p> <p>Unfortunately, interpolation of value “j” can be more complicated since the interpolation at position j requires two levels of half-pixel interpolation. In particular, to interpolate “j,” twenty bits may be required to represent the intermediate value “j1.” In this case, the intermediate version “j1” of “j” may be rounded to fit within sixteen bits. In this case, the operations discussed above may be implemented with minor modification so that all operations can fit within sixteen bit data structures without sacrificing precision. The interpolation of “b” and “h” may be the same as that defined above, except that their intermediate versions “b1” and “h1” may be stored for other sub-pixel interpolation. The following Tables show the interpolation process for other sub-pixels in sixteen bit storage elements. In the Tables below, the operations defined in each column are performed</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	sequentially through the respective table. Shaded rows contain operations that are performed at the end of the process, and are not used for interpolation of other sub-pixel positions.

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II				
	TABLE 5				
	positions {a, c, d, l} of FIGS. 4A-4D				
	Operation	Comment	Min value	Max value	Register size
	r1 = x	r1 is integer pixel x	0	255	8u
	r1 = r1 << 5	r1 is 32 * x	0	8160	13u
	r2 = y0	r2 is y0 (y0 is a one-dimensional (1-D) half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s
	r1 = r1 + r2	r1 is 32 * x + y0	-2550	18870	16s
	r1 = r1 + 32	r1 is 32 * x + y0 + 32	-2518	18902	16s
	r1 = r1 >> 6	r1 is (32 * x + y0 + 32) >> 6	-39	295	11s
	r1 = max (0, r1)	clip r1 on the low side	0	295	10u
	r1 = min (255, r1)	clip r1 on the high side	0	255	8u

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																																													
	<div>TABLE 6</div> <div><table><tr><th colspan="5">positions {e, g, m, o} of FIGS. 4A-4D</th></tr><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r1 = y0</td><td>r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r2 = y1</td><td>r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r1 = r1 + r2</td><td>r1 is y0 + y1</td><td>-5100</td><td>21420</td><td>16s</td></tr><tr><td>r1 = r1 + 32</td><td>r1 is y0 + y1 + 32</td><td>-5068</td><td>21452</td><td>16s</td></tr><tr><td>r1 = r1 >> 6</td><td>r1 is (y0 + y1 + 32) >> 6</td><td>-79</td><td>335</td><td>11s</td></tr><tr><td>r1 = max (0, r1)</td><td>clip r1 on the low side</td><td>0</td><td>335</td><td>10u</td></tr><tr><td>r1 = min (255, r1)</td><td>clip r1 on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table></div> <div>Karczewicz II, ¶¶0097-0098: The pixels at quarter-pixel positions labeled as “e,” “g,” “m,” and “o” are calculated according to Table 2 by averaging the two nearest pixels at half-pixel positions in the diagonal direction and then applying upward rounding.</div>	positions {e, g, m, o} of FIGS. 4A-4D					Operation	Comment	Min value	Max value	Register size	r1 = y0	r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r2 = y1	r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r1 = r1 + r2	r1 is y0 + y1	-5100	21420	16s	r1 = r1 + 32	r1 is y0 + y1 + 32	-5068	21452	16s	r1 = r1 >> 6	r1 is (y0 + y1 + 32) >> 6	-79	335	11s	r1 = max (0, r1)	clip r1 on the low side	0	335	10u	r1 = min (255, r1)	clip r1 on the high side	0	255	8u
positions {e, g, m, o} of FIGS. 4A-4D																																														
Operation	Comment	Min value	Max value	Register size																																										
r1 = y0	r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s																																										
r2 = y1	r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s																																										
r1 = r1 + r2	r1 is y0 + y1	-5100	21420	16s																																										
r1 = r1 + 32	r1 is y0 + y1 + 32	-5068	21452	16s																																										
r1 = r1 >> 6	r1 is (y0 + y1 + 32) >> 6	-79	335	11s																																										
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ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p style="text-align: center;">TABLE 2</p> <hr/> $e = (b + h + 1) \gg 1$ $g = (b + ee + 1) \gg 1$ $m = (h + hh + 1) \gg 1$ $o = (ee + hh + 1) \gg 1$ <hr/> <p>The final values of those quarter-pixels may be finally clipped to values in the interval (0,255).</p> <p>Karczewicz II, ¶¶0101-0102:</p> <p>The pixels at quarter-pixel positions labeled as “e,” “g,” “m,” and “o” may be calculated according to Table 4 by averaging the two nearest pixels at half-pixel positions in the diagonal direction with upward rounding.</p> <p style="text-align: center;">TABLE 4</p> <hr/> $e = (b1 + h1 + 32) \gg 6$ $g = (b1 + ee1 + 32) \gg 6$ $m = (h1 + hh1 + 32) \gg 6$ $o = (ee1 + hh1 + 32) \gg 6$ <hr/> <p>The final values of those quarter-pixels may be clipped to values in the range (0,255). By preserving the full precision of the intermediate values, the interpolated sub-pixels will be more accurate. In particular, half-pixel values at locations “b,” “h,” “ee,” and “hh” may be</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>unrounded values denoted as “b1,” “h1,” “ee1,” and “hh1” for purposes of quarter-pixel generation.</p> <p>Karczewicz II, ¶0095:</p> <p>To interpolate sub-pixel “j,” an intermediate value “j1” is first derived as: $j1 = aa1 - 5 * bb1 + 20 * b1 + 20 * hh1 - 5 * ii1 + jj1$, where the intermediate values denoted as “aa1,” “bb1,” “hh1,” “ii1” and “jj1” are derived by applying the 6-tap filter horizontally in the same manner as the calculation of b1 at the positions of “aa,” “bb,” “hh,” “ii” and “jj.” The final value j may be calculated as: $j = \max(0, \min(255, (j1 + 512) >> 10))$.</p> <p>Karczewicz II, ¶0104-106:</p> <p>Table 7, below, illustrates a sixteen-bit implementation of deriving pixel value at pixel location “j.” All 1-D half-pixels “aa1,” “bb1,” “b1,” “hh1,” “ii1” and “jj1” are obtained without any shift down. The clipping range to avoid overflow is (−2372,18640). Again shaded rows in the table indicate operations that are only performed at the end of the interpolation process, and these results are not used for the interpolation of other sub-pixel positions. The intermediate value “j1” will be saved for the interpolation of “f,” “i,” “k” and “n.”</p>

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TABLE 7

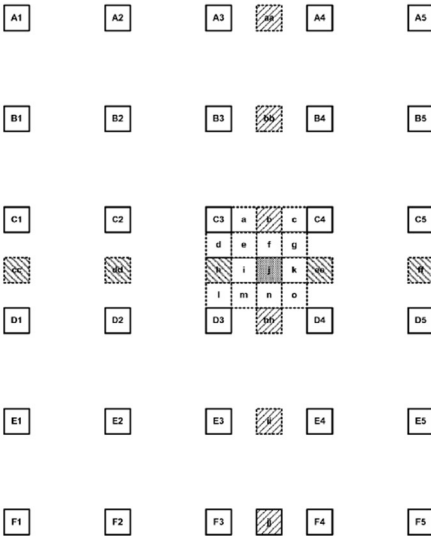
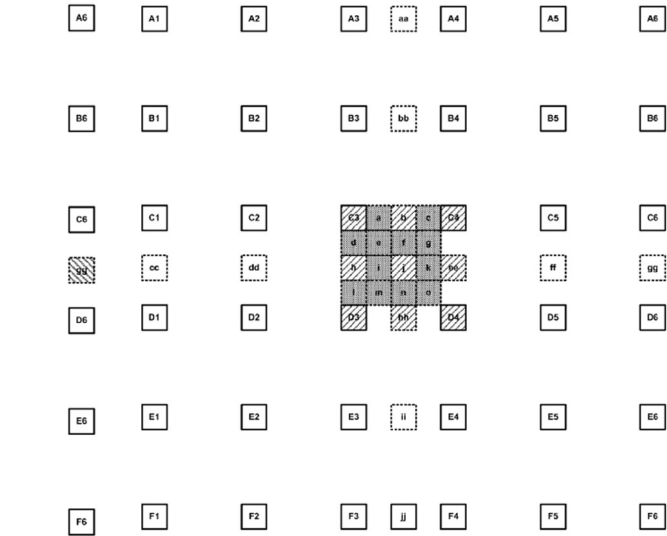
position {j} of FIGS. 4A-4D

Operation	Comment	Min value	Max value	Register size
$r1 = y0$	$y0$ is 1-D half-pixel aa1 before shifting down	-2550	10710	15s
$r2 = y5$	$y5$ is 1-D half-pixel jj1 before shifting down	-2550	10710	15s
$r1 = r1 + r2$	$r1$ is $y0 + y5$	-5100	21420	16s
$r2 = y1$	$y1$ is 1-D half-pixel bb1 before shifting down	-2550	10710	15s
$r3 = y4$	$y4$ is 1-D half-pixel ii1 before shifting down	-2550	10710	15s
$r2 = r2 + r3$	$r2$ is $y1 + y4$	-5100	21420	16s
$r1 = r1 - r2$	$r1$ is $(y0 + y5) - (y1 + y4)$	-26520	26520	16s
$r1 = r1 \gg 2$	$r1$ is $((y0 + y5) - (y1 + y4)) \gg 2$	-6630	6630	14s
$r3 = y2$	$y2$ is 1-D half-pixel bl before shifting down	-2550	10710	15s
$r4 = y3$	$y3$ is 1-D half-pixel lh1 before shifting down	-2550	10710	15s
$r3 = r3 + r4$	$r3$ is $y2 + y3$	-5100	21420	16s
$r3 = \max(-2372, r3)$	clip $r3$ to avoid overflow (low side)	-2372	21420	16s
$r3 = \min(18640, r3)$	clip $r3$ to avoid overflow (high side)	-2372	18640	16s
$r2 = r3 - r4$	$r2$ is $(y2 + y3) - (y1 + y4)$	-23792	23740	16s
$r1 = r1 + r2$	$r1$ is $((y0 + y5) - 5 * (y1 + y4)) + 4 * (y2 + y3) \gg 2$	-30422	30370	16s
$r1 = r1 \gg 2$	$r1$ is $((y0 + y5) - 5 * (y1 + y4)) + 4 * (y2 + y3) \gg 4$	-7605	7592	14s
$r1 = r1 + r3$	$r1$ is $((y0 + y5) - 5 * (y1 + y4)) + 20 * (y2 + y3) \gg 4$	-9977	26232	16s
$j1 = r1$	store result			
$r1 = r1 + 32$	$r1$ is $((y0 + y5) - 5 * (y1 + y4)) + 20 * (y2 + y3) + 512 \gg 4$	-9045	26264	16s
$r1 = r1 \gg 5$	$r1$ is $((y0 + y5) - 5 * (y1 + y4)) + 20 * (y2 + y3) + 512 \gg 10$	-135	410	15s
$r1 = \max(0, r1)$	clip $r1$ on the low side	0	410	9a
$r1 = \min(255, r1)$	clip $r1$ on the high side	0	255	8a

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																																																												
	<p>Table 8, below demonstrates steps that can be taken for sixteen-bit implementation of interpolating {f,i,k,n}, which are the positions that use to interpolate the intermediate value “j1.”</p> <p style="text-align: center;">TABLE 8</p> <table><tr><th colspan="5"><u>positions {f, i, k, n} of FIGS. 4A-4D</u></th></tr><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r1 = y0</td><td>r1 is y0 (1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r2 = j1</td><td>r2 is j1 (2-D half-pixel j1 before shifting down)</td><td>-9914</td><td>26232</td><td>16s</td></tr></table> <p style="text-align: center;">TABLE 8-continued</p> <table><tr><th colspan="5"><u>positions {f, i, k, n} of FIGS. 4A-4D</u></th></tr><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r2 = r2 >> 1</td><td>r2 is j1 >> 1</td><td>-4957</td><td>13116</td><td>15s</td></tr><tr><td>r1 = r1 + r2</td><td>r1 is y0 + (j1 >> 1)</td><td>-7507</td><td>23826</td><td>16s</td></tr><tr><td>r1 = r1 + 32</td><td>r1 is y0 + (j1 >> 1) + 32</td><td>-7491</td><td>23842</td><td>16s</td></tr><tr><td>r1 = r1 >> 6</td><td>r1 is (y0 + (j1 >> 1) + 32) >> 6</td><td>-235</td><td>745</td><td>11s</td></tr><tr><td>r1 = max (0, r1)</td><td>clip r1 on the low side</td><td>0</td><td>745</td><td>10u</td></tr><tr><td>r1 = min (255, r1)</td><td>clip r1 on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table>	<u>positions {f, i, k, n} of FIGS. 4A-4D</u>					Operation	Comment	Min value	Max value	Register size	r1 = y0	r1 is y0 (1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r2 = j1	r2 is j1 (2-D half-pixel j1 before shifting down)	-9914	26232	16s	<u>positions {f, i, k, n} of FIGS. 4A-4D</u>					Operation	Comment	Min value	Max value	Register size	r2 = r2 >> 1	r2 is j1 >> 1	-4957	13116	15s	r1 = r1 + r2	r1 is y0 + (j1 >> 1)	-7507	23826	16s	r1 = r1 + 32	r1 is y0 + (j1 >> 1) + 32	-7491	23842	16s	r1 = r1 >> 6	r1 is (y0 + (j1 >> 1) + 32) >> 6	-235	745	11s	r1 = max (0, r1)	clip r1 on the low side	0	745	10u	r1 = min (255, r1)	clip r1 on the high side	0	255	8u
<u>positions {f, i, k, n} of FIGS. 4A-4D</u>																																																													
Operation	Comment	Min value	Max value	Register size																																																									
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ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>In short, the discussion above demonstrates a complete technique for interpolating every possible sub-pixel location “a,” “b,” “c,” “d,” “e,” “f,” “g,” “h,” “i,” “j,” “k,” “l,” “m,” “n,” and “o” without exceeding sixteen-bit storage elements, which is desirable for an implementation. Most intermediate half-pixel values remain unrounded for purposes of quarter-pixel interpolation. However, pixel location “j” is a special case that may require rounding of intermediate results for purposes of generating its half-pixel value since pixel location “j” requires two levels of half-pixel interpolation. In accordance with this disclosure, however, the rounding in pixel location “j” will not impact the accuracy of the final value of quarter-pixels which rely on “j.”</p> <p>Karczewicz II, Figs. 4A-4D:</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II												
	<div><div><div><div>A1</div><div>A2</div><div>A3</div><div>aa</div><div>A4</div><div>A5</div><div>A6</div><div>A1</div><div>A2</div><div>A3</div><div>aa</div><div>A4</div><div>A5</div><div>A6</div></div><div><div><div>B1</div><div>B2</div><div>B3</div><div>bb</div><div>B4</div><div>B5</div><div>B6</div><div>B1</div><div>B2</div><div>B3</div><div>bb</div><div>B4</div><div>B5</div><div>B6</div></div><div><div><div>C1</div><div>C2</div><div>C3</div><div>a</div><div>b</div><div>c</div><div>C4</div><div>C5</div><div>C6</div><div>C7</div><div>C8</div><div>C9</div><div>C10</div><div>C11</div><div>C12</div><div>C13</div><div>C14</div><div>C15</div><div>C16</div><div>C17</div><div>C18</div><div>C19</div><div>C20</div><div>C21</div><div>C22</div><div>C23</div><div>C24</div><div>C25</div><div>C26</div><div>C27</div><div>C28</div><div>C29</div><div>C30</div><div>C31</div><div>C32</div><div>C33</div><div>C34</div><div>C35</div><div>C36</div><div>C37</div><div>C38</div><div>C39</div><div>C40</div><div>C41</div><div>C42</div><div>C43</div><div>C44</div><div>C45</div><div>C46</div><div>C47</div><div>C48</div><div>C49</div><div>C50</div><div>C51</div><div>C52</div><div>C53</div><div>C54</div><div>C55</div><div>C56</div><div>C57</div><div>C58</div><div>C59</div><div>C60</div><div>C61</div><div>C62</div><div>C63</div><div>C64</div><div>C65</div><div>C66</div><div>C67</div><div>C68</div><div>C69</div><div>C70</div><div>C71</div><div>C72</div><div>C73</div><div>C74</div><div>C75</div><div>C76</div><div>C77</div><div>C78</div><div>C79</div><div>C80</div><div>C81</div><div>C82</div><div>C83</div><div>C84</div><div>C85</div><div>C86</div><div>C87</div><div>C88</div><div>C89</div><div>C90</div><div>C91</div><div>C92</div><div>C93</div><div>C94</div><div>C95</div><div>C96</div><div>C97</div><div>C98</div><div>C99</div><div>C100</div><div>C101</div><div>C102</div><div>C103</div><div>C104</div><div>C105</div><div>C106</div><div>C107</div><div>C108</div><div>C109</div><div>C110</div><div>C111</div><div>C112</div><div>C113</div><div>C114</div><div>C115</div><div>C116</div><div>C117</div><div>C118</div><div>C119</div><div>C120</div><div>C121</div><div>C122</div><div>C123</div><div>C124</div><div>C125</div><div>C126</div><div>C127</div><div>C128</div><div>C129</div><div>C130</div><div>C131</div><div>C132</div><div>C133</div><div>C134</div><div>C135</div><div>C136</div><div>C137</div><div>C138</div><div>C139</div><div>C140</div><div>C141</div><div>C142</div><div>C143</div><div>C144</div><div>C145</div><div>C146</div><div>C147</div><div>C148</div><div>C149</div><div>C150</div><div>C151</div><div>C152</div><div>C153</div><div>C154</div><div>C155</div><div>C156</div><div>C157</div><div>C158</div><div>C159</div><div>C160</div><div>C161</div><div>C162</div><div>C163</div><div>C164</div><div>C165</div><div>C166</div><div>C167</div><div>C168</div><div>C169</div><div>C170</div><div>C171</div><div>C172</div><div>C173</div><div>C174</div><div>C175</div><div>C176</div><div>C177</div><div>C178</div><div>C179</div><div>C180</div><div>C181</div><div>C182</div><div>C183</div><div>C184</div><div>C185</div><div>C186</div><div>C187</div><div>C188</div><div>C189</div><div>C190</div><div>C191</div><div>C192</div><div>C193</div><div>C194</div><div>C195</div><div>C196</div><div>C197</div><div>C198</div><div>C199</div><div>C200</div><div>C201</div><div>C202</div><div>C203</div><div>C204</div><div>C205</div><div>C206</div><div>C207</div><div>C208</div><div>C209</div><div>C210</div><div>C211</div><div>C212</div><div>C213</div><div>C214</div><div>C215</div><div>C216</div><div>C217</div><div>C218</div><div>C219</div><div>C220</div><div>C221</div><div>C222</div><div>C223</div><div>C224</div><div>C225</div><div>C226</div><div>C227</div><div>C228</div><div>C229</div><div>C230</div><div>C231</div><div>C232</div><div>C233</div><div>C234</div><div>C235</div><div>C236</div><div>C237</div><div>C238</div><div>C239</div><div>C240</div><div>C241</div><div>C242</div><div>C243</div><div>C244</div><div>C245</div><div>C246</div><div>C247</div><div>C248</div><div>C249</div><div>C250</div><div>C251</div><div>C252</div><div>C253</div><div>C254</div><div>C255</div><div>C256</div><div>C257</div><div>C258</div><div>C259</div><div>C260</div><div>C261</div><div>C262</div><div>C263</div><div>C264</div><div>C265</div><div>C266</div><div>C267</div><div>C268</div><div>C269</div><div>C270</div><div>C271</div><div>C272</div><div>C273</div><div>C274</div><div>C275</div><div>C276</div><div>C277</div><div>C278</div><div>C279</div><div>C280</div><div>C281</div><div>C282</div><div>C283</div><div>C284</div><div>C285</div><div>C286</div><div>C287</div><div>C288</div><div>C289</div><div>C290</div><div>C291</div><div>C292</div><div>C293</div><div>C294</div><div>C295</div><div>C296</div><div>C297</div><div>C298</div><div>C299</div><div>C300</div><div>C301</div><div>C302</div><div>C303</div><div>C304</div><div>C305</div><div>C306</div><div>C307</div><div>C308</div><div>C309</div><div>C310</div><div>C311<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4A</div></div></div></div></div></div>												<div><div><div>A1</div><div>A2</div><div>A3</div><div>aa</div><div>A4</div><div>A5</div><div>A6</div><div>A1</div><div>A2</div><div>A3</div><div>aa</div><div>A4</div><div>A5</div><div>A6</div></div><div><div><div>B1</div><div>B2</div><div>B3</div><div>bb</div><div>B4</div><div>B5</div><div>B6</div><div>B1</div><div>B2</div><div>B3</div><div>bb</div><div>B4</div><div>B5</div><div>B6</div></div><div><div><div>C1</div><div>C2</div><div>C3</div><div>a</div><div>b</div><div>c</div><div>C4</div><div>C5</div><div>C6</div><div>C7</div><div>C8</div><div>C9</div><div>C10</div><div>C11</div><div>C12</div><div>C13</div><div>C14</div><div>C15</div><div>C16</div><div>C17</div><div>C18</div><div>C19</div><div>C20</div><div>C21</div><div>C22</div><div>C23</div><div>C24</div><div>C25</div><div>C26</div><div>C27</div><div>C28</div><div>C29</div><div>C30</div><div>C31</div><div>C32</div><div>C33</div><div>C34</div><div>C35</div><div>C36</div><div>C37</div><div>C38</div><div>C39</div><div>C40</div><div>C41</div><div>C42</div><div>C43</div><div>C44</div><div>C45</div><div>C46</div><div>C47</div><div>C48</div><div>C49</div><div>C50</div><div>C51</div><div>C52</div><div>C53</div><div>C54</div><div>C55</div><div>C56</div><div>C57</div><div>C58</div><div>C59</div><div>C60</div><div>C61</div><div>C62</div><div>C63</div><div>C64</div><div>C65</div><div>C66</div><div>C67</div><div>C68</div><div>C69</div><div>C70</div><div>C71</div><div>C72</div><div>C73</div><div>C74</div><div>C75</div><div>C76</div><div>C77</div><div>C78</div><div>C79</div><div>C80</div><div>C81</div><div>C82</div><div>C83</div><div>C84</div><div>C85</div><div>C86</div><div>C87</div><div>C88</div><div>C89</div><div>C90</div><div>C91</div><div>C92</div><div>C93</div><div>C94</div><div>C95</div><div>C96</div><div>C97</div><div>C98</div><div>C99</div><div>C100</div><div>C101</div><div>C102</div><div>C103</div><div>C104</div><div>C105</div><div>C106</div><div>C107</div><div>C108</div><div>C109</div><div>C110</div><div>C111</div><div>C112</div><div>C113</div><div>C114</div><div>C115</div><div>C116</div><div>C117</div><div>C118</div><div>C119</div><div>C120</div><div>C121</div><div>C122</div><div>C123</div><div>C124</div><div>C125</div><div>C126</div><div>C127</div><div>C128</div><div>C129</div><div>C130</div><div>C131</div><div>C132</div><div>C133</div><div>C134</div><div>C135</div><div>C136</div><div>C137</div><div>C138</div><div>C139</div><div>C140</div><div>C141</div><div>C142</div><div>C143</div><div>C144</div><div>C145</div><div>C146</div><div>C147</div><div>C148</div><div>C149</div><div>C150</div><div>C151</div><div>C152</div><div>C153</div><div>C154</div><div>C155</div><div>C156</div><div>C157</div><div>C158</div><div>C159</div><div>C160</div><div>C161</div><div>C162</div><div>C163</div><div>C164</div><div>C165</div><div>C166</div><div>C167</div><div>C168</div><div>C169</div><div>C170</div><div>C171</div><div>C172</div><div>C173</div><div>C174</div><div>C175</div><div>C176</div><div>C177</div><div>C178</div><div>C179</div><div>C180</div><div>C181</div><div>C182</div><div>C183</div><div>C184</div><div>C185</div><div>C186</div><div>C187</div><div>C188</div><div>C189</div><div>C190</div><div>C191</div><div>C192</div><div>C193</div><div>C194</div><div>C195</div><div>C196</div><div>C197</div><div>C198</div><div>C199</div><div>C200</div><div>C201</div><div>C202</div><div>C203</div><div>C204</div><div>C205</div><div>C206</div><div>C207</div><div>C208</div><div>C209</div><div>C210</div><div>C211</div><div>C212</div><div>C213</div><div>C214</div><div>C215</div><div>C216</div><div>C217</div><div>C218</div><div>C219</div><div>C220</div><div>C221</div><div>C222</div><div>C223</div><div>C224</div><div>C225</div><div>C226</div><div>C227</div><div>C228</div><div>C229</div><div>C230</div><div>C231</div><div>C232</div><div>C233</div><div>C234</div><div>C235</div><div>C236</div><div>C237</div><div>C238</div><div>C239</div><div>C240</div><div>C241</div><div>C242</div><div>C243</div><div>C244</div><div>C245</div><div>C246</div><div>C247</div><div>C248</div><div>C249</div><div>C250</div><div>C251</div><div>C252</div><div>C253</div><div>C254</div><div>C255</div><div>C256</div><div>C257</div><div>C258</div><div>C259</div><div>C260</div><div>C261</div><div>C262</div><div>C263</div><div>C264</div><div>C265</div><div>C266</div><div>C267</div><div>C268</div><div>C269</div><div>C270</div><div>C271</div><div>C272</div><div>C273</div><div>C274</div><div>C275</div><div>C276</div><div>C277</div><div>C278</div><div>C279</div><div>C280</div><div>C281</div><div>C282</div><div>C283</div><div>C284</div><div>C285</div><div>C286</div><div>C287</div><div>C288</div><div>C289</div><div>C290</div><div>C291</div><div>C292</div><div>C293</div><div>C294</div><div>C295</div><div>C296</div><div>C297</div><div>C298</div><div>C299</div><div>C300</div><div>C301</div><div>C302</div><div>C303</div><div>C304</div><div>C305</div><div>C306</div><div>C307</div><div>C308</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ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<div style="display: flex; justify-content: space-around; align-items: flex-start;"> <div style="text-align: center;">  <p>FIG. 4C</p> </div> <div style="text-align: center;">  <p>FIG. 4D</p> </div> </div> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
1[d] obtaining a combined prediction based at least partly upon said first prediction and	Karczewicz II, as evidenced by the example citations below, discloses obtaining a combined prediction based at least partly upon the first prediction and the second prediction.

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
said second prediction;	<p><i>See, e.g.,</i></p> <p>Karczewicz II, ¶¶0093-0106:</p> <p>A sub-pixel motion vector refers to a sub-pixel position in a reference picture which needs to be interpolated. H.264 defines one interpolation process for sub-pixels in which sub-pixels b and h (see FIGS. 4A-4D) may be calculated by horizontal and vertical filtering with a 6-tap filter having tap values (1, -5, 20, 20, -5, 1) as follows:</p> $b1 = C1 - 5 * C2 + 20 * C3 + 20 * C4 - 5 * C5 + C6$ <p>where “C1,” “C2,” “C3,” “C4,” “C5” and “C6” represent the six closest integer pixels that surround “b” in the horizontal direction, with pixels “C3” and “C4” being the closest, “C2” and “C5” being the next closest, and “C1” and “C6” being the next closest.</p> $h1 = A3 - 5 * B3 + 20 * C3 + 20 * D3 - 5 * E3 + F3$ <p>where “A3,” “B3,” “C3,” “D3,” “E3” and “F3” represent the six closest integer pixels that surround “h” in the vertical direction, with pixels “C3” and “D3” being the closest, “B3” and “E3” being the next closest, and “A3” and “F3” being the next closest.</p> <p>In this case, the values of “b” and “h” may be defined as:</p> $b = \max(0, \min(255, (b1 + 16) >> 5))$ $h = \max(0, \min(255, (h1 + 16) >> 5))$ <p>where “>>” is a right-shift operation. In this disclosure, “>>” represents a right shift operation and “<<” represents a left shift operation.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>To interpolate sub-pixel “j,” an intermediate value “j1” is first derived as: $j1 = aa1 - 5 * bb1 + 20 * b1 + 20 * hh1 - 5 * ii1 + jj1$, where the intermediate values denoted as “aa1,” “bb1,” “hh1,” “ii1” and “jj1” are derived by applying the 6-tap filter horizontally in the same manner as the calculation of b1 at the positions of “aa,” “bb,” “hh,” “ii” and “jj.” The final value j may be calculated as: $j = \max(0, \min(255, (j1 + 512) \gg 10))$.</p> <p>The pixels at quarter-pixel positions labeled as “a,” “c,” “d,” “l,” “f,” “i,” “k,” and “n” may be derived according to Table 1 by averaging the two nearest pixels at integer and half-pixel positions and then applying upward rounding.</p> <div style="text-align: center;"> <p>TABLE 1</p> <hr/> $\begin{aligned} a &= (C3 + b + 1) \gg 1 \\ c &= (C4 + b + 1) \gg 1 \\ d &= (C3 + h + 1) \gg 1 \\ l &= (D3 + h + 1) \gg 1 \\ f &= (j + b + 1) \gg 1 \\ i &= (j + h + 1) \gg 1 \\ k &= (j + ee + 1) \gg 1 \\ n &= (j + hh + 1) \gg 1 \end{aligned}$ <hr/> </div> <p>The pixels at quarter-pixel positions labeled as “e,” “g,” “m,” and “o” are calculated according to Table 2 by averaging the two nearest pixels at half-pixel positions in the diagonal direction and then applying upward rounding.</p> <div style="text-align: center;"> <p>TABLE 2</p> <hr/> $\begin{aligned} e &= (b + h + 1) \gg 1 \\ g &= (b + ee + 1) \gg 1 \\ m &= (h + hh + 1) \gg 1 \\ o &= (ee + hh + 1) \gg 1 \end{aligned}$ <hr/> </div>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>The final values of those quarter-pixels may be finally clipped to values in the interval (0,255).</p> <p>In order to keep the highest possible precision through the intermediate steps, any shifting, rounding and clipping operations may be avoided until the very last step of the interpolation process. Unlike in the H.264 standard defined implementation, the pixels at quarter-pixel positions labeled as “a,” “c,” “d,” “l,” “f,” “i,” “k,” and “n” are derived according to Table 3 by averaging the two nearest pixels at integer and half-pixel positions “b,” “h,” “j,” “ee” and “hh,” with upward rounding.</p> <div><div>TABLE 3</div><div><div><div>a = (C3 << 5 + b1 + 32) >> 6</div><div>c = (C4 << 5 + b1 + 32) >> 6</div><div>d = (C3 << 5 + h1 + 32) >> 6</div><div>l = (D3 << 5 + h1 + 32) >> 6</div><div>f = (j1 >> 5 + b1 + 32) >> 6</div><div>i = (j1 >> 5 + h1 + 32) >> 6</div><div>k = (j1 >> 5 + ee1 + 32) >> 6</div><div>n = (j1 >> 5 + hh1 + 32) >> 6</div></div></div><p>In this case, the intermediate values denoted as “ee1” and “hh1” are derived by applying the 6-tap filter vertically and horizontally in the same manner as the calculation of “h1” and “b1” listed above, but at the positions of “ee” and “hh.” Rounding may be avoided at this stage in the generation of “ee1” and “hh1” as described herein.</p><p>The pixels at quarter-pixel positions labeled as “e,” “g,” “m,” and “o” may be calculated according to Table 4 by averaging the two nearest pixels at half-pixel positions in the diagonal direction with upward rounding.</p></div>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																																													
	<div>TABLE 5</div> <div>positions {s, c, d, l} of FIGS. 4A-4D</div> <table><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>$r1 = x$</td><td>$r1$ is integer pixel x</td><td>0</td><td>255</td><td>8u</td></tr><tr><td>$r1 = r1 \ll 5$</td><td>$r1$ is $32 * x$</td><td>0</td><td>8160</td><td>13u</td></tr><tr><td>$r2 = y0$</td><td>$r2$ is $y0$ ($y0$ is a one-dimensional (1-D) half-pixel such as $b1$, $h1$, $ee1$ and $hh1$ before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>$r1 = r1 + r2$</td><td>$r1$ is $32 * x + y0$</td><td>-2550</td><td>18870</td><td>16s</td></tr><tr><td>$r1 = r1 + 32$</td><td>$r1$ is $32 * x + y0 + 32$</td><td>-2518</td><td>18902</td><td>16s</td></tr><tr><td>$r1 = r1 \gg 6$</td><td>$r1$ is $(32 * x + y0 + 32) \gg 6$</td><td>-39</td><td>295</td><td>11s</td></tr><tr><td>$r1 = \max(0, r1)$</td><td>clip $r1$ on the low side</td><td>0</td><td>295</td><td>10u</td></tr><tr><td>$r1 = \min(255, r1)$</td><td>clip $r1$ on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table>	Operation	Comment	Min value	Max value	Register size	$r1 = x$	$r1$ is integer pixel x	0	255	8u	$r1 = r1 \ll 5$	$r1$ is $32 * x$	0	8160	13u	$r2 = y0$	$r2$ is $y0$ ($y0$ is a one-dimensional (1-D) half-pixel such as $b1$, $h1$, $ee1$ and $hh1$ before shifting down)	-2550	10710	15s	$r1 = r1 + r2$	$r1$ is $32 * x + y0$	-2550	18870	16s	$r1 = r1 + 32$	$r1$ is $32 * x + y0 + 32$	-2518	18902	16s	$r1 = r1 \gg 6$	$r1$ is $(32 * x + y0 + 32) \gg 6$	-39	295	11s	$r1 = \max(0, r1)$	clip $r1$ on the low side	0	295	10u	$r1 = \min(255, r1)$	clip $r1$ on the high side	0	255	8u
Operation	Comment	Min value	Max value	Register size																																										
$r1 = x$	$r1$ is integer pixel x	0	255	8u																																										
$r1 = r1 \ll 5$	$r1$ is $32 * x$	0	8160	13u																																										
$r2 = y0$	$r2$ is $y0$ ($y0$ is a one-dimensional (1-D) half-pixel such as $b1$, $h1$, $ee1$ and $hh1$ before shifting down)	-2550	10710	15s																																										
$r1 = r1 + r2$	$r1$ is $32 * x + y0$	-2550	18870	16s																																										
$r1 = r1 + 32$	$r1$ is $32 * x + y0 + 32$	-2518	18902	16s																																										
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$r1 = \max(0, r1)$	clip $r1$ on the low side	0	295	10u																																										
$r1 = \min(255, r1)$	clip $r1$ on the high side	0	255	8u																																										

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																																								
	<div>TABLE 6</div> <div><div>positions {e, g, m, o} of FIGS. 4A-4D</div><table><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r1 = y0</td><td>r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r2 = y1</td><td>r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r1 = r1 + r2</td><td>r1 is y0 + y1</td><td>-5100</td><td>21420</td><td>16s</td></tr><tr><td>r1 = r1 + 32</td><td>r1 is y0 + y1 + 32</td><td>-5068</td><td>21452</td><td>16s</td></tr><tr><td>r1 = r1 >> 6</td><td>r1 is (y0 + y1 + 32) >> 6</td><td>-79</td><td>335</td><td>11s</td></tr><tr><td>r1 = max (0, r1)</td><td>clip r1 on the low side</td><td>0</td><td>335</td><td>10u</td></tr><tr><td>r1 = min (255, r1)</td><td>clip r1 on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table></div>	Operation	Comment	Min value	Max value	Register size	r1 = y0	r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r2 = y1	r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r1 = r1 + r2	r1 is y0 + y1	-5100	21420	16s	r1 = r1 + 32	r1 is y0 + y1 + 32	-5068	21452	16s	r1 = r1 >> 6	r1 is (y0 + y1 + 32) >> 6	-79	335	11s	r1 = max (0, r1)	clip r1 on the low side	0	335	10u	r1 = min (255, r1)	clip r1 on the high side	0	255	8u
Operation	Comment	Min value	Max value	Register size																																					
r1 = y0	r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s																																					
r2 = y1	r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s																																					
r1 = r1 + r2	r1 is y0 + y1	-5100	21420	16s																																					
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r1 = r1 >> 6	r1 is (y0 + y1 + 32) >> 6	-79	335	11s																																					
r1 = max (0, r1)	clip r1 on the low side	0	335	10u																																					
r1 = min (255, r1)	clip r1 on the high side	0	255	8u																																					
	<p>Table 7, below, illustrates a sixteen-bit implementation of deriving pixel value at pixel location “j.” All 1-D half-pixels “aa1,” “bb1,” “b1,” “hh1,” “ii1” and “jj1” are obtained without any shift down. The clipping range to avoid overflow is (−2372,18640). Again shaded rows in the table indicate operations that are only performed at the end of the interpolation process, and these results are not used for the interpolation of other sub-pixel positions. The intermediate value “j1” will be saved for the interpolation of “f,” “i,” “k” and “n.”</p>																																								

ASSERTED CLAIMS

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TABLE 7

position [j] of FIGS. 4A-4D

Operation	Comment	Min value	Max value	Register size
$r1 = y0$	y0 is 1-D half-pixel aal before shifting down	-2550	10710	15s
$r2 = y5$	y5 is 1-D half-pixel j1 before shifting down	-2550	10710	15s
$r1 = r1 + r2$	r1 is y0 + y5	-5100	21420	16s
$r2 = y1$	y1 is 1-D half-pixel bb1 before shifting down	-2550	10710	15s
$r3 = y4$	y4 is 1-D half-pixel i1 before shifting down	-2550	10710	15s
$r2 = r2 + r3$	r2 is y1 + y4	-5100	21420	16s
$r1 = r1 - r2$	r1 is (y0 + y5) - (y1 + y4)	-26520	26520	16s
$r1 = r1 >> 2$	r1 is ((y0 + y5) - (y1 + y4)) >> 2	-6630	6630	14s
$r3 = y2$	y2 is 1-D half-pixel bl before shifting down	-2550	10710	15s
$r4 = y3$	y3 is 1-D half-pixel hh1 before shifting down	-2550	10710	15s
$r3 = r3 + r4$	r3 is y2 + y3	-5100	21420	16s
$r3 = \max(-2372, r3)$	clip r3 to avoid overflow (low side)	-2372	21420	16s
$r3 = \min(18640, r3)$	clip r3 to avoid overflow (high side)	-2372	18640	16s
$r2 = r3 - r4$	r2 is (y2 + y3) - (y1 + y4)	-23792	23740	16s
$r1 = r1 + r2$	r1 is ((y0 + y5) - 5 * (y1 + y4)) + 4 * (y2 + y3) >> 2	-30422	30370	16s
$r1 = r1 >> 2$	r1 is ((y0 + y5) - 5 * (y1 + y4)) + 4 * (y2 + y3) >> 4	-7605	7592	14s
$r1 = r1 + r3$	r1 is ((y0 + y5) - 5 * (y1 + y4)) + 20 * (y2 + y3) >> 4	-9977	26232	16s
$j1 = r1$	store result			
$r1 = r1 + 32$	r1 is ((y0 + y5) - 5 * (y1 + y4)) - 30 * (y2 + y3) + 512 >> 4	-9945	26264	16s
$r1 = r1 >> 5$	r1 is ((y0 + y5) - 5 * (y1 + y4)) - 20 * (y2 + y3) + 512 >> 10	-185	410	10s
$r1 = \max(0, r1)$	clip r1 on the low side	0	410	9u
$r1 = \min(255, r1)$	clip r1 on the high side	0	255	8u

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																																																												
	<p>Table 8, below demonstrates steps that can be taken for sixteen-bit implementation of interpolating {f,i,k,n}, which are the positions that use to interpolate the intermediate value “j1.”</p> <div><div>TABLE 8</div><div><table><tr><th colspan="5">positions {f, i, k, n} of FIGS. 4A-4D</th></tr><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r1 = y0</td><td>r1 is y0 (1-D half-pixel such as b1, h1, eel and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r2 = j1</td><td>r2 is j1 (2-D half-pixel j1 before shifting down)</td><td>-9914</td><td>26232</td><td>16s</td></tr></table></div></div> <div><div>TABLE 8-continued</div><div><table><tr><th colspan="5">positions {f, i, k, n} of FIGS. 4A-4D</th></tr><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r2 = r2 >> 1</td><td>r2 is j1 >> 1</td><td>-4957</td><td>13116</td><td>15s</td></tr><tr><td>r1 = r1 + r2</td><td>r1 is y0 + (j1 >> 1)</td><td>-7507</td><td>23826</td><td>16s</td></tr><tr><td>r1 = r1 + 32</td><td>r1 is y0 + (j1 >> 1) + 32</td><td>-7491</td><td>23842</td><td>16s</td></tr><tr><td>r1 = r1 >> 6</td><td>r1 is (y0 + (j1 >> 1) + 32) >> 6</td><td>-235</td><td>745</td><td>11s</td></tr><tr><td>r1 = max(0, r1)</td><td>clip r1 on the low side</td><td>0</td><td>745</td><td>10u</td></tr><tr><td>r1 = min(255, r1)</td><td>clip r1 on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table></div></div> <p>In short, the discussion above demonstrates a complete technique for interpolating every possible sub-pixel location “a,” “b,” “c,” “d,” “e,” “f,” “g,” “h,” “i,” “j,” “k,” “l,” “m,” “n,” and “o” without exceeding sixteen-bit storage elements, which is desirable for an implementation. Most intermediate half-pixel values remain unrounded for purposes of quarter-pixel interpolation. However, pixel location “j” is a special case that may require</p>	positions {f, i, k, n} of FIGS. 4A-4D					Operation	Comment	Min value	Max value	Register size	r1 = y0	r1 is y0 (1-D half-pixel such as b1, h1, eel and hh1 before shifting down)	-2550	10710	15s	r2 = j1	r2 is j1 (2-D half-pixel j1 before shifting down)	-9914	26232	16s	positions {f, i, k, n} of FIGS. 4A-4D					Operation	Comment	Min value	Max value	Register size	r2 = r2 >> 1	r2 is j1 >> 1	-4957	13116	15s	r1 = r1 + r2	r1 is y0 + (j1 >> 1)	-7507	23826	16s	r1 = r1 + 32	r1 is y0 + (j1 >> 1) + 32	-7491	23842	16s	r1 = r1 >> 6	r1 is (y0 + (j1 >> 1) + 32) >> 6	-235	745	11s	r1 = max(0, r1)	clip r1 on the low side	0	745	10u	r1 = min(255, r1)	clip r1 on the high side	0	255	8u
positions {f, i, k, n} of FIGS. 4A-4D																																																													
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ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>rounding of intermediate results for purposes of generating its half-pixel value since pixel location “j” requires two levels of half-pixel interpolation. In accordance with this disclosure, however, the rounding in pixel location “j” will not impact the accuracy of the final value of quarter-pixels which rely on “j.”</p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of the references identified in Exhibit E-9</i></p>
<p>1[e] decreasing a precision of said combined prediction by shifting bits of the combined prediction to the right; and</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses decreasing a precision of the combined prediction by shifting bits of the combined prediction to the right.</p> <p><i>See, e.g.,</i></p> <p>Karczewicz II, ¶0094:</p> <p style="padding-left: 40px;">In this case, the values of “b” and “h” may be defined as:</p> <p style="padding-left: 40px;">$b = \max(0, \min(255, (b1+16) \gg 5))$</p> <p style="padding-left: 40px;">$h = \max(0, \min(255, (h1+16) \gg 5))$</p> <p style="padding-left: 40px;">where “>>” is a right-shift operation. In this disclosure, “>>” represents a right shift operation and “<<” represents a left shift operation.</p> <p><i>See also, e.g., ¶¶0094-0103 (using the “>>” right shift operation).</i></p> <p style="padding-left: 40px;">In this case, the values of “b” and “h” may be defined as:</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																		
	<p> $b = \max(0, \min(255, (b1+16) \gg 5))$ $h = \max(0, \min(255, (h1+16) \gg 5))$ where “$\gg$” is a right-shift operation. In this disclosure, “$\gg$” represents a right shift operation and “$\ll$” represents a left shift operation. </p> <p> To interpolate sub-pixel “j,” an intermediate value “j1” is first derived as: $j1 = aa1 - 5 * bb1 + 20 * b1 + 20 * hh1 - 5 * ii1 + jj1$, where the intermediate values denoted as “aa1,” “bb1,” “hh1,” “ii1” and “jj1” are derived by applying the 6-tap filter horizontally in the same manner as the calculation of b1 at the positions of “aa,” “bb,” “hh,” “ii” and “jj.” The final value j may be calculated as: $j = \max(0, \min(255, (j1+512) \gg 10))$. </p> <p> The pixels at quarter-pixel positions labeled as “a,” “c,” “d,” “l,” “f,” “i,” “k,” and “n” may be derived according to Table 1 by averaging the two nearest pixels at integer and half-pixel positions and then applying upward rounding. </p> <div data-bbox="726 987 1264 1201" data-label="Table"> <table> <tr> <th colspan="2">TABLE 1</th></tr> <tr> <td>$a = (C3 + b + 1) \gg 1$</td><td></td></tr> <tr> <td>$c = (C4 + b + 1) \gg 1$</td><td></td></tr> <tr> <td>$d = (C3 + h + 1) \gg 1$</td><td></td></tr> <tr> <td>$l = (D3 + h + 1) \gg 1$</td><td></td></tr> <tr> <td>$f = (j + b + 1) \gg 1$</td><td></td></tr> <tr> <td>$i = (j + h + 1) \gg 1$</td><td></td></tr> <tr> <td>$k = (j + ee + 1) \gg 1$</td><td></td></tr> <tr> <td>$n = (j + hh + 1) \gg 1$</td><td></td></tr> </table> </div> <p> The pixels at quarter-pixel positions labeled as “e,” “g,” “m,” and “o” are calculated according to Table 2 by averaging the two nearest pixels at half-pixel positions in the diagonal direction and then applying upward rounding. </p>	TABLE 1		$a = (C3 + b + 1) \gg 1$		$c = (C4 + b + 1) \gg 1$		$d = (C3 + h + 1) \gg 1$		$l = (D3 + h + 1) \gg 1$		$f = (j + b + 1) \gg 1$		$i = (j + h + 1) \gg 1$		$k = (j + ee + 1) \gg 1$		$n = (j + hh + 1) \gg 1$	
TABLE 1																			
$a = (C3 + b + 1) \gg 1$																			
$c = (C4 + b + 1) \gg 1$																			
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$i = (j + h + 1) \gg 1$																			
$k = (j + ee + 1) \gg 1$																			
$n = (j + hh + 1) \gg 1$																			

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<div data-bbox="709 402 1243 539"> <p style="text-align: center;">TABLE 2</p> <hr/> $e = (b + h + 1) \gg 1$ $g = (b + ee + 1) \gg 1$ $m = (h + hh + 1) \gg 1$ $o = (ee + hh + 1) \gg 1$ <hr/> </div> <p>The final values of those quarter-pixels may be finally clipped to values in the interval (0,255).</p> <p>In order to keep the highest possible precision through the intermediate steps, any shifting, rounding and clipping operations may be avoided until the very last step of the interpolation process. Unlike in the H.264 standard defined implementation, the pixels at quarter-pixel positions labeled as “a,” “c,” “d,” “l,” “f,” “i,” “k,” and “n” are derived according to Table 3 by averaging the two nearest pixels at integer and half-pixel positions “b,” “h,” “j,” “ee” and “hh,” with upward rounding.</p> <div data-bbox="722 974 1255 1188"> <p style="text-align: center;">TABLE 3</p> <hr/> $a = (C3 \ll 5 + b1 + 32) \gg 6$ $c = (C4 \ll 5 + b1 + 32) \gg 6$ $d = (C3 \ll 5 + h1 + 32) \gg 6$ $l = (D3 \ll 5 + h1 + 32) \gg 6$ $f = (j1 \gg 5 + b1 + 32) \gg 6$ $i = (j1 \gg 5 + h1 + 32) \gg 6$ $k = (j1 \gg 5 + ee1 + 32) \gg 6$ $n = (j1 \gg 5 + hh1 + 32) \gg 6$ <hr/> </div> <p>In this case, the intermediate values denoted as “ee1” and “hh1” are derived by applying the 6-tap filter vertically and horizontally in the same manner as the calculation of “h1” and “b1” listed above, but at the positions of “ee” and “hh.” Rounding may be avoided at this stage in the generation of “ee1” and “hh1” as described herein.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>The pixels at quarter-pixel positions labeled as “e,” “g,” “m,” and “o” may be calculated according to Table 4 by averaging the two nearest pixels at half-pixel positions in the diagonal direction with upward rounding.</p> <div data-bbox="716 548 1253 680"> <p style="text-align: center;">TABLE 4</p> <hr/> $e = (b1 + h1 + 32) \gg 6$ $g = (b1 + ee1 + 32) \gg 6$ $m = (h1 + hh1 + 32) \gg 6$ $o = (ee1 + hh1 + 32) \gg 6$ <hr/> </div> <p>The final values of those quarter-pixels may be clipped to values in the range (0,255). By preserving the full precision of the intermediate values, the interpolated sub-pixels will be more accurate. In particular, half-pixel values at locations “b,” “h,” “ee,” and “hh” may be unrounded values denoted as “b1,” “h1,” “ee1,” and “hh1” for purposes of quarter-pixel generation.</p> <p>Unfortunately, interpolation of value “j” can be more complicated since the interpolation at position j requires two levels of half-pixel interpolation. In particular, to interpolate “j,” twenty bits may be required to represent the intermediate value “j1.” In this case, the intermediate version “j1” of “j” may be rounded to fit within sixteen bits. In this case, the operations discussed above may be implemented with minor modification so that all operations can fit within sixteen bit data structures without sacrificing precision. The interpolation of “b” and “h” may be the same as that defined above, except that their intermediate versions “b1” and “h1” may be stored for other sub-pixel interpolation. The following Tables show the interpolation process for other sub-pixels in sixteen bit storage elements. In the Tables below, the operations defined in each column are performed sequentially through the respective table. Shaded rows contain operations that are performed at the end of the process, and are not used for interpolation of other sub-pixel positions.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																																													
	<div>TABLE 5</div> <div>positions {s, c, d, l} of FIGS. 4A-4D</div> <table><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>$r1 = x$</td><td>$r1$ is integer pixel x</td><td>0</td><td>255</td><td>8u</td></tr><tr><td>$r1 = r1 \ll 5$</td><td>$r1$ is $32 * x$</td><td>0</td><td>8160</td><td>13u</td></tr><tr><td>$r2 = y0$</td><td>$r2$ is $y0$ ($y0$ is a one-dimensional (1-D) half-pixel such as $b1$, $h1$, $ee1$ and $hh1$ before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>$r1 = r1 + r2$</td><td>$r1$ is $32 * x + y0$</td><td>-2550</td><td>18870</td><td>16s</td></tr><tr><td>$r1 = r1 + 32$</td><td>$r1$ is $32 * x + y0 + 32$</td><td>-2518</td><td>18902</td><td>16s</td></tr><tr><td>$r1 = r1 \gg 6$</td><td>$r1$ is $(32 * x + y0 + 32) \gg 6$</td><td>-39</td><td>295</td><td>11s</td></tr><tr><td>$r1 = \max(0, r1)$</td><td>clip $r1$ on the low side</td><td>0</td><td>295</td><td>10u</td></tr><tr><td>$r1 = \min(255, r1)$</td><td>clip $r1$ on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table>	Operation	Comment	Min value	Max value	Register size	$r1 = x$	$r1$ is integer pixel x	0	255	8u	$r1 = r1 \ll 5$	$r1$ is $32 * x$	0	8160	13u	$r2 = y0$	$r2$ is $y0$ ($y0$ is a one-dimensional (1-D) half-pixel such as $b1$, $h1$, $ee1$ and $hh1$ before shifting down)	-2550	10710	15s	$r1 = r1 + r2$	$r1$ is $32 * x + y0$	-2550	18870	16s	$r1 = r1 + 32$	$r1$ is $32 * x + y0 + 32$	-2518	18902	16s	$r1 = r1 \gg 6$	$r1$ is $(32 * x + y0 + 32) \gg 6$	-39	295	11s	$r1 = \max(0, r1)$	clip $r1$ on the low side	0	295	10u	$r1 = \min(255, r1)$	clip $r1$ on the high side	0	255	8u
Operation	Comment	Min value	Max value	Register size																																										
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ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																																								
	<div>TABLE 6</div> <div><div>positions {e, g, m, o} of FIGS. 4A-4D</div><table><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r1 = y0</td><td>r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r2 = y1</td><td>r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r1 = r1 + r2</td><td>r1 is y0 + y1</td><td>-5100</td><td>21420</td><td>16s</td></tr><tr><td>r1 = r1 + 32</td><td>r1 is y0 + y1 + 32</td><td>-5068</td><td>21452</td><td>16s</td></tr><tr><td>r1 = r1 >> 6</td><td>r1 is (y0 + y1 + 32) >> 6</td><td>-79</td><td>335</td><td>11s</td></tr><tr><td>r1 = max(0, r1)</td><td>clip r1 on the low side</td><td>0</td><td>335</td><td>10u</td></tr><tr><td>r1 = min(255, r1)</td><td>clip r1 on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table></div> <div>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</div>	Operation	Comment	Min value	Max value	Register size	r1 = y0	r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r2 = y1	r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r1 = r1 + r2	r1 is y0 + y1	-5100	21420	16s	r1 = r1 + 32	r1 is y0 + y1 + 32	-5068	21452	16s	r1 = r1 >> 6	r1 is (y0 + y1 + 32) >> 6	-79	335	11s	r1 = max(0, r1)	clip r1 on the low side	0	335	10u	r1 = min(255, r1)	clip r1 on the high side	0	255	8u
Operation	Comment	Min value	Max value	Register size																																					
r1 = y0	r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s																																					
r2 = y1	r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s																																					
r1 = r1 + r2	r1 is y0 + y1	-5100	21420	16s																																					
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r1 = max(0, r1)	clip r1 on the low side	0	335	10u																																					
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1[f] encoding residual data in a bitstream, wherein the residual data is determined based upon a difference between the combined prediction and the block of pixels.	<div>Karczewicz II, as evidenced by the example citations below, discloses encoding residual data in a bitstream, wherein the residual data is determined based upon a difference between the combined prediction and the block of pixels.</div> <div>See, e.g.,</div> <div>Karczewicz II, ¶0004:</div> <div>Block based inter-coding is a very useful coding technique that relies on temporal prediction</div>																																								

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>to reduce or remove temporal redundancy between video blocks of successive coded units of a video sequence. The coded units may comprise video frames, slices of video frames, groups of pictures, or another defined unit of video blocks. For inter-coding, the video encoder performs motion estimation and motion compensation to track the movement of corresponding video blocks of two or more adjacent coded units. Motion estimation generates motion vectors, which indicate the displacement of video blocks relative to corresponding prediction video blocks in one or more reference frames or other coded units. Motion compensation uses the motion vectors to generate prediction video blocks from the reference frame or other coded unit. After motion compensation, residual video blocks are formed by subtracting prediction video blocks from the original video blocks being coded.</p> <p>Karczewicz II, ¶0006:</p> <p>A coded video block may be represented by prediction information that can be used to create or identify a predictive block, and a residual block of data indicative of differences between the block being coded and the predictive block. The prediction information may comprise the one or more motion vectors that are used to identify the predictive block of data. Given the motion vectors, the decoder is able to reconstruct the predictive blocks that were used to code the residual. Thus, given a set of residual blocks and a set of motion vectors (and possibly some additional syntax), the decoder may be able to reconstruct a video frame that was originally encoded. Inter-coding based on motion estimation and motion compensation can achieve very good compression because successive video frames or other types of coded units are often very similar. An encoded video sequence may comprise blocks of residual data, motion vectors, and possibly other types of syntax.</p> <p>Karczewicz II, ¶0035:</p> <p>This disclosure describes various interpolation techniques performed by an encoder and a</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>decoder during the motion compensation process of video coding. According to one aspect of this disclosure, the encoder may apply a plurality of pre-defined interpolation filters in order to generate a plurality of different interpolated prediction data. The interpolated prediction data that achieves the highest levels of compression may be selected at the encoder, and the interpolation filter that was used may be coded as syntax and communicated to the decoding device as part of the coded video data. The interpolated prediction data comprises reference data. Video data to be coded (e.g., a video block) may be subtracted from interpolated prediction data (e.g., an interpolated predictive reference block) in order to define a residual block of encoded data, which may then be transformed, quantized and entropy coded.</p> <p>Karczewicz II, ¶0050:</p> <p>Smaller video blocks can provide better resolution, and may be used for locations of a video frame that include high levels of detail. In general, macroblocks (MBs) and the various sub-blocks may be considered to be video blocks. In addition, a slice may be considered to be a series of video blocks, such as MBs and/or sub-blocks. Each slice may be an independently decodable unit of a video frame. Video encoder 22 and video decoder 28 perform inter-based predictive coding, which involves the generation of predictive reference data and the subtraction of a video block to be coded from the predictive reference data to generate residual data, which may then be transformed, quantized and entropy coded. The inter-based predictive coding may include interpolation of the predictive data in accordance with this disclosure.</p> <p>Karczewicz II, ¶0058:</p> <p>Once motion estimation unit 32 has selected the motion vector for the video block to be coded, motion compensation unit 35 generates the predictive video block associated with that motion vector. According to this disclosure, however, motion compensation unit 35 may</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>consider several versions of any predictive video block that has sub-pixel resolution. In this case, motion compensation unit 35 may apply a plurality of pre-defined interpolation filters in order to generate a plurality of different interpolated prediction data for the video block to be coded. Motion compensation unit 35 then selects the interpolated prediction data (e.g., an interpolated video block associated with one of the interpolation filters) that achieves the highest levels of compression. The interpolation filter that was used to generate the interpolated data may be coded as interpolation syntax and communicated to entropy coding unit 46 for inclusion in the coded bitstream. Once motion compensation unit 35 has selected and applied the best interpolation filter, the motion compensation unit 35 generates the predictive data using that interpolation filter, and video encoder applies adder 48 to subtract that predictive data from the video block being coded to generate residual data.</p> <p>Karczewicz II, ¶0060:</p> <p>As noted, once motion compensation unit 35 generated prediction data (e.g., an interpolated predictive video block), video encoder 50 forms a residual video block by subtracting the prediction data from the original video block being coded. Adder 48 represents the component or components that perform this subtraction operation. Transform unit 38 applies a transform, such as a discrete cosine transform (DCT) or a conceptually similar transform, to the residual block, producing a video block comprising residual transform block coefficients. Transform unit 38, for example, may perform other transforms, such as those defined by the H.264 standard, which are conceptually similar to DCT. Wavelet transforms, integer transforms, sub-band transforms or other types of transforms could also be used. In any case, transform unit 38 applies the transform to the residual block, producing a block of residual transform coefficients. The transform may convert the residual information from a pixel domain to a frequency domain.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>Karczewicz II, ¶0073:</p> <p>After motion estimation, the best motion vector for a given video block may be identified, e.g., possibly using a rate-distortion model to balance the coding rate and quality. Then, the prediction video block is formed during motion compensation using the best motion vector. As outlined above, the residual video block is formed by subtracting the prediction video block from the original video block. A transform is then applied on the residual block, and the transform coefficients are quantized and entropy coded to further reduce bit rate.</p> <p>Karczewicz II, ¶0005:</p> <p>The video encoder may also apply transform, quantization and entropy coding processes to further reduce the bit rate associated with communication of residual blocks. Transform techniques may comprise discrete cosine transforms (DCTs) or conceptually similar processes. Alternatively, wavelet transforms, integer transforms, or other types of transforms may be used. In a DCT process, as an example, a set of pixel values are converted into transform coefficients, which may represent the energy of the pixel values in the frequency domain. Quantization is applied to the transform coefficients, and generally involves a process that limits the number of bits associated with any given transform coefficient. Entropy coding comprises one or more processes that collectively compress a sequence of quantized transform coefficients. Examples of entropy coding include but are not limited to content adaptive variable length coding (CAVLC) and context adaptive binary arithmetic coding (CABAC).</p> <p>Karczewicz II, ¶0061:</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>As noted, once motion compensation unit 35 generated prediction data (e.g., an interpolated predictive video block), video encoder 50 forms a residual video block by subtracting the prediction data from the original video block being coded. Adder 48 represents the component or components that perform this subtraction operation. Transform unit 38 applies a transform, such as a discrete cosine transform (DCT) or a conceptually similar transform, to the residual block, producing a video block comprising residual transform block coefficients. Transform unit 38, for example, may perform other transforms, such as those defined by the H.264 standard, which are conceptually similar to DCT. Wavelet transforms, integer transforms, sub-band transforms or other types of transforms could also be used. In any case, transform unit 38 applies the transform to the residual block, producing a block of residual transform coefficients. The transform may convert the residual information from a pixel domain to a frequency domain.</p> <p>Karczewicz II, ¶0110:</p> <p>FIG. 6 is a flow diagram consistent with one or more aspects of video decoding consistent with this disclosure. FIG. 6 will be discussed from the perspective of video encoder 50 shown in FIG. 2. In particular, video encoder 60 receives an encoded video data (111), and receives a syntax element that identifies an interpolation filter from a plurality of interpolation filters (112). The encoded bitstream may include both the encoded residual video data and the syntax element that identifies the interpolation filter that was used at the encoder. Entropy decoding unit 52 may entropy decode the received bitstream to parse out the transformed and quantized residual blocks, which are sent to inverse transform quantization unit 56 and inverse transform unit 58, and to parse out the interpolation syntax element and motion vectors, which are sent to motion compensation unit 55.</p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination</i></p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<i>with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i>
<p>2. The method according to claim 1, wherein in an instance in which said first motion vector points to a subpixel, said first prediction is obtained by interpolation using pixel values of said first reference block.</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses that in an instance in which the first motion vector points to a subpixel, the first prediction is obtained by interpolation using pixel values of the first reference block.</p> <p><i>See, e.g.,</i></p> <p>Karczewicz II, ¶0007:</p> <p>Interpolation techniques have been developed in order to improve the level of compression that can be achieved in inter-coding. In this case, the predictive data generated during motion compensation, which is used to code a video block, may be interpolated from the pixels of video blocks of the video frame or other coded unit used in motion estimation. Interpolation is often performed to generate predictive half-pixel values (half-pel) and predictive quarter-pixel values (quarter-pel). Such interpolation often generates predictive blocks that are even more similar to the video blocks being coded than the actual video blocks of the predictive frame or other coded unit used in the video coding.</p> <p>Karczewicz II, ¶0010:</p> <p>In addition, this disclosure also recognizes coding inefficiencies due to conventional rounding of half-pixel values, and provides techniques that may improve interpolation by reducing or eliminating intermediate rounding. In this case, interpolated half-pixel values may be rounded for purposes of half-pixel interpolation. Quarter-pixel values, however, which may be generated based on one or more of the interpolated half-pixel values, may rely on non-rounded versions of the half-pixel values. This can eliminate propagation of rounding</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>inaccuracies from the half-pixel values to the quarter-pixel values. In some cases, slight rounding without sacrificing the accuracy of the final values may be applied to one specific half-pixel value in order to ensure that sixteen-bit storage elements can be used to store any intermediate values of half-pixels. In particular, when fifteen possible sub-pixel locations are defined for every pixel location, one of the specific half-pixel values may need to be generated based on other half-pixel values (i.e., requiring two levels of half-pixel interpolation), and this specific half-pixel value may require rounding to ensure that sixteen-bit storage elements can be used to store all interpolated values.</p> <p>Karczewicz II, ¶0042:</p> <p>The illustrated system 10 of FIG. 1 is merely exemplary. The interpolation techniques of this disclosure may be performed by any encoding device that supports motion compensated interpolation to sub-pixel resolution. Source device 12 and destination device 16 are merely examples of such coding devices. In this case, source device 12 generates coded video data for transmission to destination device 16. Devices 12, 16 may operate in a substantially symmetrical manner such that each of devices 12, 16 include video encoding and decoding components, e.g., in a combined encoder-decoder (CODEC). Hence, system 10 may support one-way or two-way video transmission between video devices 12, 16, e.g., for video streaming, video playback, video broadcasting, or video telephony.</p> <p>Karczewicz II, ¶0053:</p> <p>Also, according to another aspect of this disclosure, video encoder 22 and video decoder 28 may apply interpolation techniques that eliminate or significantly reduce intermediate rounding of half-pixel values for quarter-pixel interpolation. In this case, video encoder 22 and video decoder 28 may round the interpolated half-pixel values purposes of half-pixel interpolation. For quarter-pixel interpolation, however, video encoder 22 and video decoder</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>28 may store and use non-rounded versions of the half-pixel values in order to reduce or eliminate propagation of rounding inaccuracies from the half-pixel values to the quarter-pixel values. In some cases, slight rounding may be applied to one particular half-pixel value that requires two levels of interpolation in order to ensure that a fixed size storage elements (e.g., sixteen-bit registers) can be used to store any intermediate values. In some cases, the impact of the slight rounding on the final accuracy can be made to be zero by performing the rounding properly, as described herein.</p> <p>Karczewicz II, ¶0059:</p> <p>Also, according to another aspect of this disclosure, video encoder 22 and video decoder 28 may apply interpolation techniques that eliminate or significantly reduce intermediate rounding of half-pixel values for quarter-pixel interpolation. In this case, video encoder 22 and video decoder 28 may round the interpolated half-pixel values for purposes of half-pixel interpolation. For quarter-pixel interpolation, however, video encoder 22 and video decoder 28 may store and use non-rounded versions of the half-pixel values in order to reduce or eliminate propagation of rounding inaccuracies from the half-pixel values to the quarter-pixel values. In some cases, slight rounding may be applied to one particular half-pixel value that requires two levels of interpolation in order to ensure that fixed size storage elements (e.g., 16-bit registers) can be used to store any intermediate values.</p> <p>Karczewicz II, ¶0066:</p> <p>Again, the techniques of this disclosure concern motion compensated interpolation in which pixel values of predictive video blocks are interpolated to sub-pixel resolution. The encoder uses the techniques of this disclosure to identify a desirable interpolation filter from a plurality of pre-defined interpolation filters. The different filters may be characterized by different sets of filter coefficients, different numbers of filter coefficients, or different filter</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>types. The decoder interprets syntax elements sent from the encoder in order to identify the same desirable set of interpolation filter coefficients used by the encoder.</p> <p>Karczewicz II, ¶0067:</p> <p>FIGS. 4A-4D are conceptual diagrams illustrating integer pixels and interpolated pixels that may comprise pixels of predictive video blocks. In the conceptual illustration of FIG. 4, the different boxes represent pixels. Capitalized letters (in the boxes with solid lines) represent integer pixel locations, while small letters (in the boxes with dotted lines) represent the interpolated pixel locations. The lettered labels may be used herein to describe the pixel locations, or pixel positions, or may refer to the pixel values associated with the various locations. Pixel locations “aa,” “bb,” “cc,” “dd,” “ee,” “ff,” “gg,” “hh,” “ii,” and “jj,” are half-pixel locations used in the fractional interpolations of various fractional locations associated with pixel location “C3.”</p> <p>Karczewicz II, ¶0069:</p> <p>In the ITU H.264/AVC standard, in order to obtain luma signals at half-pixel positions, a 6-tap Wiener filter with coefficients [1, -5, 20, 20, -5, 1] is typically used. Then, in order to obtain luma signals at quarter-pixel locations, a bilinear filter is used. The bilinear filter may also be used in fractional pixel interpolation for the chroma components, which may have up to 1/8-pixel precision in H.264/AVC.</p> <p>Karczewicz II, ¶0070:</p> <p>Half-pixel interpolations of pixel locations “b” and “h” are demonstrated in FIG. 4B in the horizontal and vertical directions respectively. In this case, pixel location “b” may be interpolated based on integer pixels “C1,” “C2,” “C3,” “C4,” “C5,” and “C6.” Similarly, pixel</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>location “h” may be interpolated based on integer pixels “A3,” “B3,” “C3,” “D3,” “E3,” and “F3.” Different interpolation filters (e.g., different sets of filter taps) may be applied to generate different interpolated values of pixel locations “b” and “h” as described herein. In FIG. 4B, interpolated pixel locations “b” and “h” are shown with shading, and integer pixel locations “C1,” “C2,” “C3,” “C4,” “C5,” “C6,” “A3,” “B3,” “C3,” “D3,” “E3” and “F3” are shown with cross-hashes.</p> <p>Karczewicz II, ¶¶0093-0106:</p> <p>A sub-pixel motion vector refers to a sub-pixel position in a reference picture which needs to be interpolated. H.264 defines one interpolation process for sub-pixels in which sub-pixels b and h (see FIGS. 4A-4D) may be calculated by horizontal and vertical filtering with a 6-tap filter having tap values (1, -5, 20, 20, -5, 1) as follows:</p> $b1 = C1 - 5 * C2 + 20 * C3 + 20 * C4 - 5 * C5 + C6$ <p>where “C1,” “C2,” “C3,” “C4,” “C5” and “C6” represent the six closest integer pixels that surround “b” in the horizontal direction, with pixels “C3” and “C4” being the closest, “C2” and “C5” being the next closest, and “C1” and “C6” being the next closest.</p> $h1 = A3 - 5 * B3 + 20 * C3 + 20 * D3 - 5 * E3 + F3$ <p>where “A3,” “B3,” “C3,” “D3,” “E3” and “F3” represent the six closest integer pixels that surround “h” in the vertical direction, with pixels “C3” and “D3” being the closest, “B3” and “E3” being the next closest, and “A3” and “F3” being the next closest.</p> <p>In this case, the values of “b” and “h” may be defined as:</p> $b = \max(0, \min(255, (b1 + 16) >> 5))$

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p> $h = \max(0, \min(255, (h1+16) \gg 5))$ where “$\gg$” is a right-shift operation. In this disclosure, “$\gg$” represents a right shift operation and “$\ll$” represents a left shift operation. </p> <p> To interpolate sub-pixel “j,” an intermediate value “j1” is first derived as: $j1 = aa1 - 5 * bb1 + 20 * b1 + 20 * hh1 - 5 * ii1 + jj1$, where the intermediate values denoted as “aa1,” “bb1,” “hh1,” “ii1” and “jj1” are derived by applying the 6-tap filter horizontally in the same manner as the calculation of b1 at the positions of “aa,” “bb,” “hh,” “ii” and “jj.” The final value j may be calculated as: $j = \max(0, \min(255, (j1+512) \gg 10))$. </p> <p> The pixels at quarter-pixel positions labeled as “a,” “c,” “d,” “l,” “f,” “i,” “k,” and “n” may be derived according to Table 1 by averaging the two nearest pixels at integer and half-pixel positions and then applying upward rounding. </p> <div data-bbox="726 951 1264 1166"> <p style="text-align: center;">TABLE 1</p> <hr/> $\begin{aligned} a &= (C3 + b + 1) \gg 1 \\ c &= (C4 + b + 1) \gg 1 \\ d &= (C3 + h + 1) \gg 1 \\ l &= (D3 + h + 1) \gg 1 \\ f &= (j + b + 1) \gg 1 \\ i &= (j + h + 1) \gg 1 \\ k &= (j + ee + 1) \gg 1 \\ n &= (j + hh + 1) \gg 1 \end{aligned}$ <hr/> </div> <p> The pixels at quarter-pixel positions labeled as “e,” “g,” “m,” and “o” are calculated according to Table 2 by averaging the two nearest pixels at half-pixel positions in the diagonal direction and then applying upward rounding. </p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<div data-bbox="709 402 1243 539"> <p style="text-align: center;">TABLE 2</p> <hr/> $e = (b + h + 1) \gg 1$ $g = (b + ee + 1) \gg 1$ $m = (h + hh + 1) \gg 1$ $o = (ee + hh + 1) \gg 1$ <hr/> </div> <p>The final values of those quarter-pixels may be finally clipped to values in the interval (0,255).</p> <p>In order to keep the highest possible precision through the intermediate steps, any shifting, rounding and clipping operations may be avoided until the very last step of the interpolation process. Unlike in the H.264 standard defined implementation, the pixels at quarter-pixel positions labeled as “a,” “c,” “d,” “l,” “f,” “i,” “k,” and “n” are derived according to Table 3 by averaging the two nearest pixels at integer and half-pixel positions “b,” “h,” “j,” “ee” and “hh,” with upward rounding.</p> <div data-bbox="722 974 1255 1188"> <p style="text-align: center;">TABLE 3</p> <hr/> $a = (C3 \ll 5 + b1 + 32) \gg 6$ $c = (C4 \ll 5 + b1 + 32) \gg 6$ $d = (C3 \ll 5 + h1 + 32) \gg 6$ $l = (D3 \ll 5 + h1 + 32) \gg 6$ $f = (j1 \gg 5 + b1 + 32) \gg 6$ $i = (j1 \gg 5 + h1 + 32) \gg 6$ $k = (j1 \gg 5 + ee1 + 32) \gg 6$ $n = (j1 \gg 5 + hh1 + 32) \gg 6$ <hr/> </div> <p>In this case, the intermediate values denoted as “ee1” and “hh1” are derived by applying the 6-tap filter vertically and horizontally in the same manner as the calculation of “h1” and “b1” listed above, but at the positions of “ee” and “hh.” Rounding may be avoided at this stage in the generation of “ee1” and “hh1” as described herein.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>The pixels at quarter-pixel positions labeled as “e,” “g,” “m,” and “o” may be calculated according to Table 4 by averaging the two nearest pixels at half-pixel positions in the diagonal direction with upward rounding.</p> <div data-bbox="716 581 1253 716"> <p style="text-align: center;">TABLE 4</p> <hr/> $e = (b1 + h1 + 32) \gg 6$ $g = (b1 + ee1 + 32) \gg 6$ $m = (h1 + hh1 + 32) \gg 6$ $o = (ee1 + hh1 + 32) \gg 6$ <hr/> </div> <p>The final values of those quarter-pixels may be clipped to values in the range (0,255). By preserving the full precision of the intermediate values, the interpolated sub-pixels will be more accurate. In particular, half-pixel values at locations “b,” “h,” “ee,” and “hh” may be unrounded values denoted as “b1,” “h1,” “ee1,” and “hh1” for purposes of quarter-pixel generation.</p> <p>Unfortunately, interpolation of value “j” can be more complicated since the interpolation at position j requires two levels of half-pixel interpolation. In particular, to interpolate “j,” twenty bits may be required to represent the intermediate value “j1.” In this case, the intermediate version “j1” of “j” may be rounded to fit within sixteen bits. In this case, the operations discussed above may be implemented with minor modification so that all operations can fit within sixteen bit data structures without sacrificing precision. The interpolation of “b” and “h” may be the same as that defined above, except that their intermediate versions “b1” and “h1” may be stored for other sub-pixel interpolation. The following Tables show the interpolation process for other sub-pixels in sixteen bit storage elements. In the Tables below, the operations defined in each column are performed sequentially through the respective table. Shaded rows contain operations that are performed</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																																																		
	<p>at the end of the process, and are not used for interpolation of other sub-pixel positions.</p> <p>TABLE 5</p> <table><tr><th colspan="5">positions {s, c, d, l} of FIGS. 4A-4D</th></tr><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r1 = x</td><td>r1 is integer pixel x</td><td>0</td><td>255</td><td>8u</td></tr><tr><td>r1 = r1 << 5</td><td>r1 is 32 * x</td><td>0</td><td>8160</td><td>13u</td></tr><tr><td>r2 = y0</td><td>r2 is y0 (y0 is a one-dimensional (1-D) half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r1 = r1 + r2</td><td>r1 is 32 * x + y0</td><td>-2550</td><td>18870</td><td>16s</td></tr><tr><td>r1 = r1 + 32</td><td>r1 is 32 * x + y0 + 32</td><td>-2518</td><td>18902</td><td>16s</td></tr><tr><td>r1 = r1 >> 6</td><td>r1 is (32 * x + y0 + 32) >> 6</td><td>-39</td><td>295</td><td>11s</td></tr><tr><td>r1 = max(0, r1)</td><td>clip r1 on the low side</td><td>0</td><td>295</td><td>10u</td></tr><tr><td>r1 = min(255, r1)</td><td>clip r1 on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table>	positions {s, c, d, l} of FIGS. 4A-4D					Operation	Comment	Min value	Max value	Register size	r1 = x	r1 is integer pixel x	0	255	8u	r1 = r1 << 5	r1 is 32 * x	0	8160	13u	r2 = y0	r2 is y0 (y0 is a one-dimensional (1-D) half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r1 = r1 + r2	r1 is 32 * x + y0	-2550	18870	16s	r1 = r1 + 32	r1 is 32 * x + y0 + 32	-2518	18902	16s	r1 = r1 >> 6	r1 is (32 * x + y0 + 32) >> 6	-39	295	11s	r1 = max(0, r1)	clip r1 on the low side	0	295	10u	r1 = min(255, r1)	clip r1 on the high side	0	255	8u
positions {s, c, d, l} of FIGS. 4A-4D																																																			
Operation	Comment	Min value	Max value	Register size																																															
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r1 = r1 + r2	r1 is 32 * x + y0	-2550	18870	16s																																															
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ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																																								
	<div>TABLE 6</div> <div><div>positions {e, g, m, o} of FIGS. 4A-4D</div><table><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r1 = y0</td><td>r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r2 = y1</td><td>r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r1 = r1 + r2</td><td>r1 is y0 + y1</td><td>-5100</td><td>21420</td><td>16s</td></tr><tr><td>r1 = r1 + 32</td><td>r1 is y0 + y1 + 32</td><td>-5068</td><td>21452</td><td>16s</td></tr><tr><td>r1 = r1 >> 6</td><td>r1 is (y0 + y1 + 32) >> 6</td><td>-79</td><td>335</td><td>11s</td></tr><tr><td>r1 = max (0, r1)</td><td>clip r1 on the low side</td><td>0</td><td>335</td><td>10u</td></tr><tr><td>r1 = min (255, r1)</td><td>clip r1 on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table></div>	Operation	Comment	Min value	Max value	Register size	r1 = y0	r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r2 = y1	r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r1 = r1 + r2	r1 is y0 + y1	-5100	21420	16s	r1 = r1 + 32	r1 is y0 + y1 + 32	-5068	21452	16s	r1 = r1 >> 6	r1 is (y0 + y1 + 32) >> 6	-79	335	11s	r1 = max (0, r1)	clip r1 on the low side	0	335	10u	r1 = min (255, r1)	clip r1 on the high side	0	255	8u
Operation	Comment	Min value	Max value	Register size																																					
r1 = y0	r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s																																					
r2 = y1	r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s																																					
r1 = r1 + r2	r1 is y0 + y1	-5100	21420	16s																																					
r1 = r1 + 32	r1 is y0 + y1 + 32	-5068	21452	16s																																					
r1 = r1 >> 6	r1 is (y0 + y1 + 32) >> 6	-79	335	11s																																					
r1 = max (0, r1)	clip r1 on the low side	0	335	10u																																					
r1 = min (255, r1)	clip r1 on the high side	0	255	8u																																					
	<p>Table 7, below, illustrates a sixteen-bit implementation of deriving pixel value at pixel location “j.” All 1-D half-pixels “aa1,” “bb1,” “b1,” “hh1,” “ii1” and “jj1” are obtained without any shift down. The clipping range to avoid overflow is (−2372,18640). Again shaded rows in the table indicate operations that are only performed at the end of the interpolation process, and these results are not used for the interpolation of other sub-pixel positions. The intermediate value “j1” will be saved for the interpolation of “f,” “i,” “k” and “n.”</p>																																								

ASSERTED CLAIMS

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TABLE 7

position {j} of FIGS. 4A-4D

Operation	Comment	Min value	Max value	Register size
$r1 = y0$	y0 is 1-D half-pixel aal before shifting down	-2550	10710	15s
$r2 = y5$	y5 is 1-D half-pixel j1 before shifting down	-2550	10710	15s
$r1 = r1 + r2$	r1 is y0 + y5	-5100	21420	16s
$r2 = y1$	y1 is 1-D half-pixel bb1 before shifting down	-2550	10710	15s
$r3 = y4$	y4 is 1-D half-pixel i1 before shifting down	-2550	10710	15s
$r2 = r2 + r3$	r2 is y1 + y4	-5100	21420	16s
$r1 = r1 - r2$	r1 is (y0 + y5) - (y1 + y4)	-26520	26520	16s
$r1 = r1 >> 2$	r1 is ((y0 + y5) - (y1 + y4)) >> 2	-6630	6630	14s
$r3 = y2$	y2 is 1-D half-pixel bl before shifting down	-2550	10710	15s
$r4 = y3$	y3 is 1-D half-pixel hh1 before shifting down	-2550	10710	15s
$r3 = r3 + r4$	r3 is y2 + y3	-5100	21420	16s
$r3 = \max(-2372, r3)$	clip r3 to avoid overflow (low side)	-2372	21420	16s
$r3 = \min(18640, r3)$	clip r3 to avoid overflow (high side)	-2372	18640	16s
$r2 = r3 - r4$	r2 is (y2 + y3) - (y1 + y4)	-23792	23740	16s
$r1 = r1 + r2$	r1 is ((y0 + y5) - 5* (y1 + y4)) + 4* (y2 + y3) >> 2	-30422	30370	16s
$r1 = r1 >> 2$	r1 is ((y0 + y5) - 5* (y1 + y4)) + 4* (y2 + y3) >> 4	-7605	7592	14s
$r1 = r1 + r3$	r1 is ((y0 + y5) - 5* (y1 + y4)) + 20* (y2 + y3) >> 4	-9977	26232	16s
$j1 = r1$	store result			
$r1 = r1 + 32$	r1 is ((y0 + y5) - 5* (y1 + y4)) - 30* (y2 + y3) + 512 >> 4	-9945	26264	16s
$r1 = r1 >> 5$	r1 is ((y0 + y5) - 5* (y1 + y4)) - 20* (y2 + y3) + 512 >> 10	-185	410	10s
$r1 = \max(0, r1)$	clip r1 on the low side	0	410	9u
$r1 = \min(255, r1)$	clip r1 on the high side	0	255	8u

ASSERTED CLAIMS

PRIOR ART REFERENCE:
KARCZEWICZ II

Table 8, below demonstrates steps that can be taken for sixteen-bit implementation of interpolating {f,i,k,n}, which are the positions that use to interpolate the intermediate value “j1.”

TABLE 8

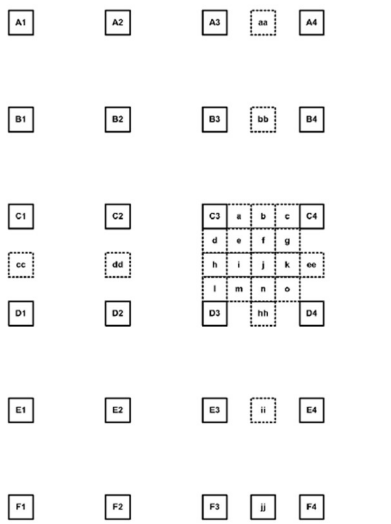
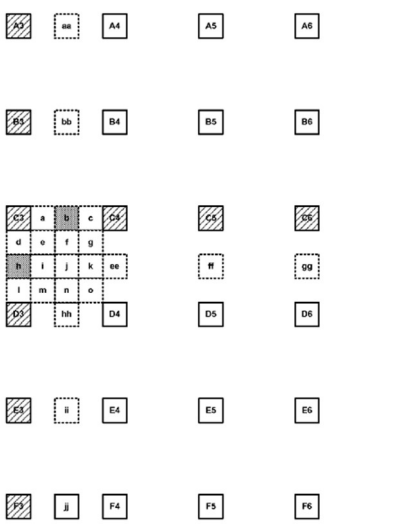
positions {f, i, k, n} of FIGS. 4A-4D				
Operation	Comment	Min value	Max value	Register size
r1 = y0	r1 is y0 (1-D half-pixel such as b1, h1, eel and hh1 before shifting down)	-2550	10710	15s
r2 = j1	r2 is j1 (2-D half-pixel j1 before shifting down)	-9914	26232	16s

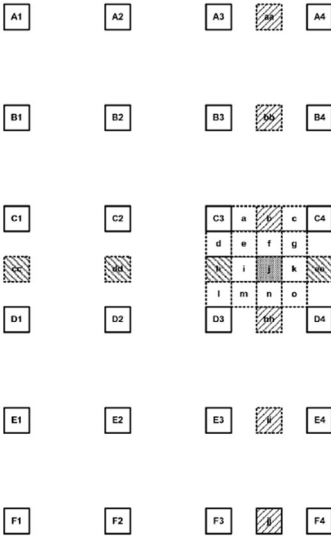
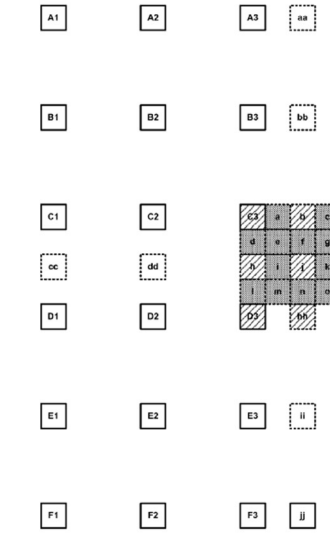
TABLE 8-continued

positions {f, i, k, n} of FIGS. 4A-4D				
Operation	Comment	Min value	Max value	Register size
r2 = r2 >> 1	r2 is j1 >> 1	-4957	13116	15s
r1 = r1 + r2	r1 is y0 + (j1 >> 1)	-7507	23826	16s
r1 = r1 + 32	r1 is y0 + (j1 >> 1) + 32	-7491	23842	16s
r1 = r1 >> 6	r1 is (y0 + (j1 >> 1) + 32) >> 6	-235	745	11s
r1 = max(0, r1)	clip r1 on the low side	0	745	10u
r1 = min(255, r1)	clip r1 on the high side	0	255	8u

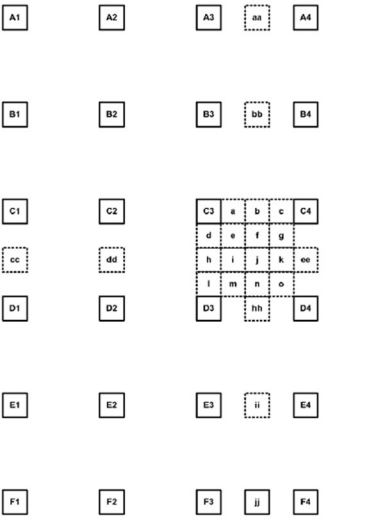
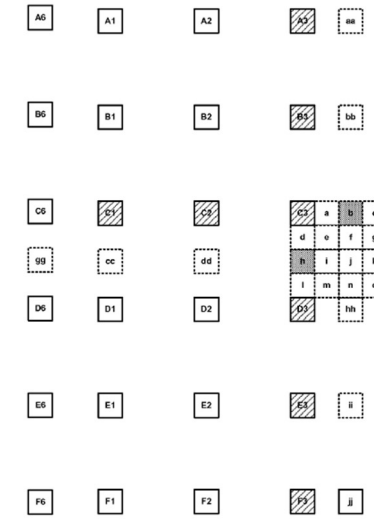
In short, the discussion above demonstrates a complete technique for interpolating every possible sub-pixel location “a,” “b,” “c,” “d,” “e,” “f,” “g,” “h,” “i,” “j,” “k,” “l,” “m,” “n,” and “o” without exceeding sixteen-bit storage elements, which is desirable for an implementation. Most intermediate half-pixel values remain unrounded for purposes of quarter-pixel interpolation. However, pixel location “j” is a special case that may require

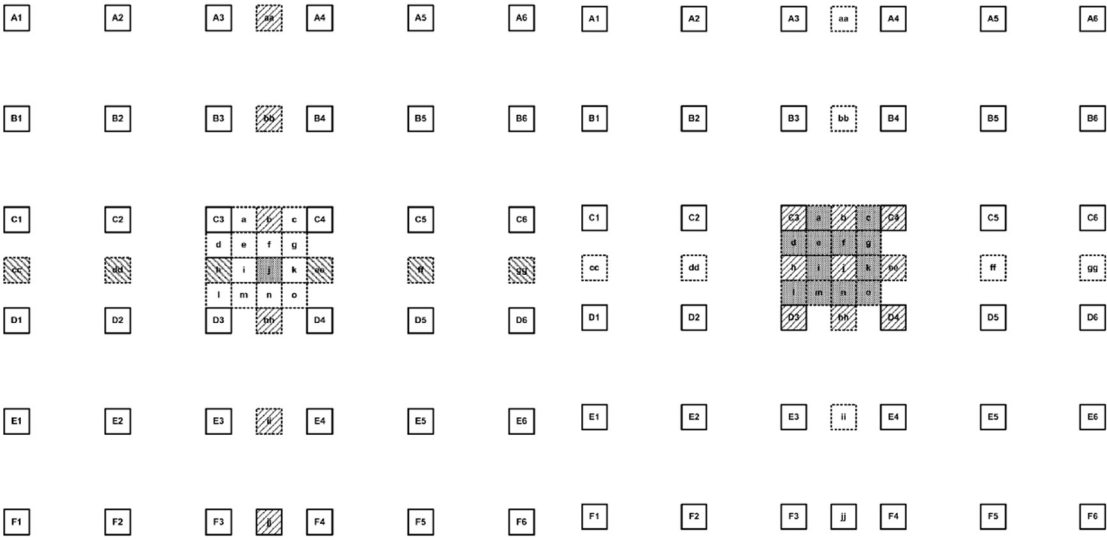
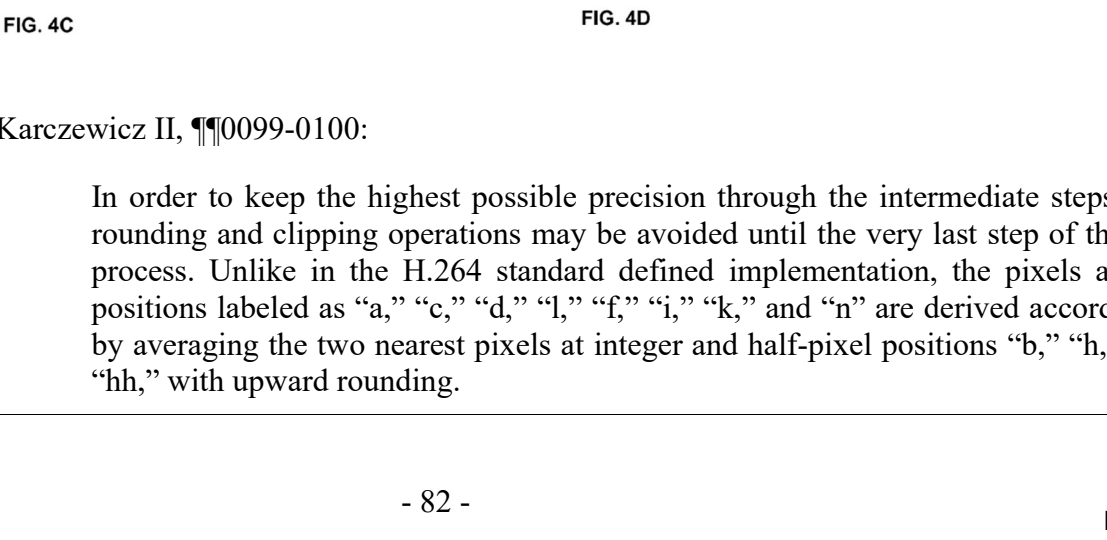
ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>rounding of intermediate results for purposes of generating its half-pixel value since pixel location “j” requires two levels of half-pixel interpolation. In accordance with this disclosure, however, the rounding in pixel location “j” will not impact the accuracy of the final value of quarter-pixels which rely on “j.”</p> <p>Karczewicz II, ¶0067:</p> <p>FIGS. 4A-4D are conceptual diagrams illustrating integer pixels and interpolated pixels that may comprise pixels of predictive video blocks. In the conceptual illustration of FIG. 4, the different boxes represent pixels. Capitalized letters (in the boxes with solid lines) represent integer pixel locations, while small letters (in the boxes with dotted lines) represent the interpolated pixel locations. The lettered labels may be used herein to describe the pixel locations, or pixel positions, or may refer to the pixel values associated with the various locations. Pixel locations “aa,” “bb,” “cc,” “dd,” “ee,” “ff,” “gg,” “hh,” “ii,” and “jj,” are half-pixel locations used in the fractional interpolations of various fractional locations associated with pixel location “C3.”</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II	
	 <p>FIG. 4A</p>	 <p>FIG. 4B</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<div style="display: flex; justify-content: space-around;"> <div style="text-align: center;">  <p>FIG. 4C</p> </div> <div style="text-align: center;">  <p>FIG. 4D</p> </div> </div> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
3. The method according to claim 2, wherein said first prediction is obtained by	Karczewicz II, as evidenced by the example citations below, discloses that the first prediction is obtained by interpolation using values of the first reference block by right shifting a sum of a P-tap

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
<p>interpolation using values of said first reference block by: right shifting a sum of a P-tap filter using values of said first reference block.</p>	<p>filter using values of the first reference block.</p> <p><i>See, e.g.,</i></p> <p>Karczewicz II, ¶0095:</p> <p>To interpolate sub-pixel “j,” an intermediate value “j1” is first derived as: $j1 = aa1 - 5 * bb1 + 20 * b1 + 20 * hh1 - 5 * ii1 + jj1,$ where the intermediate values denoted as “aa1,” “bb1,” “hh1,” “ii1” and “jj1” are derived by applying the 6-tap filter horizontally in the same manner as the calculation of b1 at the positions of “aa,” “bb,” “hh,” “ii” and “jj.” The final value j may be calculated as: $j = \max(0, \min(255, (j1 + 512) >> 10)).$</p> <p>Karczewicz II, ¶0067:</p> <p>FIGS. 4A-4D are conceptual diagrams illustrating integer pixels and interpolated pixels that may comprise pixels of predictive video blocks. In the conceptual illustration of FIG. 4, the different boxes represent pixels. Capitalized letters (in the boxes with solid lines) represent integer pixel locations, while small letters (in the boxes with dotted lines) represent the interpolated pixel locations. The lettered labels may be used herein to describe the pixel locations, or pixel positions, or may refer to the pixel values associated with the various locations. Pixel locations “aa,” “bb,” “cc,” “dd,” “ee,” “ff,” “gg,” “hh,” “ii,” and “jj,” are half-pixel locations used in the fractional interpolations of various fractional locations associated with pixel location “C3.”</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II													
	 <p>FIG. 4A</p>							 <p>FIG. 4B</p>						

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<div><p>FIG. 4C</p></div> <div><p>FIG. 4D</p></div> <p>Karczewicz II, ¶¶0099-0100:</p> <p>In order to keep the highest possible precision through the intermediate steps, any shifting, rounding and clipping operations may be avoided until the very last step of the interpolation process. Unlike in the H.264 standard defined implementation, the pixels at quarter-pixel positions labeled as “a,” “c,” “d,” “l,” “f,” “i,” “k,” and “n” are derived according to Table 3 by averaging the two nearest pixels at integer and half-pixel positions “b,” “h,” “j,” “ee” and “hh,” with upward rounding.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p style="text-align: center;">TABLE 3</p> <hr/> $ \begin{aligned} a &= (C3 \ll 5 + b1 + 32) \gg 6 \\ c &= (C4 \ll 5 + b1 + 32) \gg 6 \\ d &= (C3 \ll 5 + h1 + 32) \gg 6 \\ l &= (D3 \ll 5 + h1 + 32) \gg 6 \\ f &= (j1 \gg 5 + b1 + 32) \gg 6 \\ i &= (j1 \gg 5 + h1 + 32) \gg 6 \\ k &= (j1 \gg 5 + ee1 + 32) \gg 6 \\ n &= (j1 \gg 5 + hh1 + 32) \gg 6 \end{aligned} $ <hr/> <p>In this case, the intermediate values denoted as “ee1” and “hh1” are derived by applying the 6-tap filter vertically and horizontally in the same manner as the calculation of “h1” and “b1” listed above, but at the positions of “ee” and “hh.” Rounding may be avoided at this stage in the generation of “ee1” and “hh1” as described herein.</p> <p>Karczewicz II, ¶0105:</p> <p>Table 8, below demonstrates steps that can be taken for sixteen-bit implementation of interpolating {f,i,k,n}, which are the positions that use to interpolate the intermediate value “j1.”</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II				
	TABLE 8				
	<u>positions {f, i, k, n} of FIGS. 4A-4D</u>				
	Operation	Comment	Min value	Max value	Register size
	r1 = y0	r1 is y0 (1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s
	r2 = j1	r2 is j1 (2-D half-pixel j1 before shifting down)	-9914	26232	16s
	TABLE 8-continued				
	<u>positions {f, i, k, n} of FIGS. 4A-4D</u>				
	Operation	Comment	Min value	Max value	Register size
	r2 = r2 >> 1	r2 is j1 >> 1	-4957	13116	15s
	r1 = r1 + r2	r1 is y0 + (j1 >> 1)	-7507	23826	16s
	r1 = r1 + 32	r1 is y0 + (j1 >> 1) + 32	-7491	23842	16s
	r1 = r1 >> 6	r1 is (y0 + (j1 >> 1) + 32) >> 6	-235	745	11s
	r1 = max (0, r1)	clip r1 on the low side	0	745	10u
	r1 = min (255, r1)	clip r1 on the high side	0	255	8u

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
<p>4. The method according to claim 2, wherein in an instance in which said second motion vector points to an integer sample, said second prediction is obtained by shifting values of said second reference block to the left.</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses that in an instance in which the second motion vector points to an integer sample, the second prediction is obtained by shifting values of the second reference block to the left.</p> <p><i>See, e.g.,</i></p> <p>Karczewicz II, ¶0096:</p> <p>The pixels at quarter-pixel positions labeled as “a,” “c,” “d,” “l,” “f,” “i,” “k,” and “n” may be derived according to Table 1 by averaging the two nearest pixels at integer and half-pixel positions and then applying upward rounding.</p> <div style="text-align: center;"> <p>TABLE 1</p> <hr/> <pre> a = (C3 + b + 1) >> 1 c = (C4 + b + 1) >> 1 d = (C3 + h + 1) >> 1 l = (D3 + h + 1) >> 1 f = (j + b + 1) >> 1 i = (j + h + 1) >> 1 k = (j + ee + 1) >> 1 n = (j + hh + 1) >> 1 </pre> <hr/> </div>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>Karczewicz II, ¶¶0099-0100:</p> <p>In order to keep the highest possible precision through the intermediate steps, any shifting, rounding and clipping operations may be avoided until the very last step of the interpolation process. Unlike in the H.264 standard defined implementation, the pixels at quarter-pixel positions labeled as “a,” “c,” “d,” “l,” “f,” “i,” “k,” and “n” are derived according to Table 3 by averaging the two nearest pixels at integer and half-pixel positions “b,” “h,” “j,” “ee” and “hh,” with upward rounding.</p> <div style="text-align: center;"> <p>TABLE 3</p> <hr/> $a = (C3 \ll 5 + b1 + 32) \gg 6$ $c = (C4 \ll 5 + b1 + 32) \gg 6$ $d = (C3 \ll 5 + h1 + 32) \gg 6$ $l = (D3 \ll 5 + h1 + 32) \gg 6$ $f = (j1 \gg 5 + b1 + 32) \gg 6$ $i = (j1 \gg 5 + h1 + 32) \gg 6$ $k = (j1 \gg 5 + ee1 + 32) \gg 6$ $n = (j1 \gg 5 + hh1 + 32) \gg 6$ <hr/> </div> <p>In this case, the intermediate values denoted as “ee1” and “hh1” are derived by applying the 6-tap filter vertically and horizontally in the same manner as the calculation of “h1” and “b1” listed above, but at the positions of “ee” and “hh.” Rounding may be avoided at this stage in the generation of “ee1” and “hh1” as described herein.</p> <p>Karczewicz II, ¶0093:</p> <p>A sub-pixel motion vector refers to a sub-pixel position in a reference picture which needs to be interpolated. H.264 defines one interpolation process for sub-pixels in which sub-pixels b</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>and h (see FIGS. 4A-4D) may be calculated by horizontal and vertical filtering with a 6-tap filter having tap values (1, -5, 20, 20, -5, 1) as follows: $b1 = C1 - 5 * C2 + 20 * C3 + 20 * C4 - 5 * C5 + C6$ where “C1,” “C2,” “C3,” “C4,” “C5” and “C6” represent the six closest integer pixels that surround “b” in the horizontal direction, with pixels “C3” and “C4” being the closest, “C2” and “C5” being the next closest, and “C1” and “C6” being the next closest. $h1 = A3 - 5 * B3 + 20 * C3 + 20 * D3 - 5 * E3 + F3$ where “A3,” “B3,” “C3,” “D3,” “E3” and “F3” represent the six closest integer pixels that surround “h” in the vertical direction, with pixels “C3” and “D3” being the closest, “B3” and “E3” being the next closest, and “A3” and “F3” being the next closest.</p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
<p>5. The method according to claim 1, wherein said decreasing said precision of said combined prediction by shifting bits of the combined prediction to the right, further comprises: inserting a rounding offset to the combined prediction before said decreasing.</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses that the decreasing the precision of the combined prediction by shifting bits of the combined prediction to the right, further comprises inserting a rounding offset to the combined prediction before the decreasing.</p> <p><i>See, e.g.,</i></p> <p>Karczewicz II, ¶¶0096-0106:</p> <p>The pixels at quarter-pixel positions labeled as “a,” “c,” “d,” “l,” “f,” “i,” “k,” and “n” may be derived according to Table 1 by averaging the two nearest pixels at integer and half-pixel positions and then applying upward rounding.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<div data-bbox="730 402 1264 613"> <p style="text-align: center;">TABLE 1</p> <hr/> $\begin{aligned} a &= (C3 + b + 1) \gg 1 \\ c &= (C4 + b + 1) \gg 1 \\ d &= (C3 + h + 1) \gg 1 \\ l &= (D3 + h + 1) \gg 1 \\ f &= (j + b + 1) \gg 1 \\ i &= (j + h + 1) \gg 1 \\ k &= (j + ee + 1) \gg 1 \\ n &= (j + hh + 1) \gg 1 \end{aligned}$ <hr/> </div> <p>The pixels at quarter-pixel positions labeled as “e,” “g,” “m,” and “o” are calculated according to Table 2 by averaging the two nearest pixels at half-pixel positions in the diagonal direction and then applying upward rounding.</p> <div data-bbox="709 831 1243 961"> <p style="text-align: center;">TABLE 2</p> <hr/> $\begin{aligned} e &= (b + h + 1) \gg 1 \\ g &= (b + ee + 1) \gg 1 \\ m &= (h + hh + 1) \gg 1 \\ o &= (ee + hh + 1) \gg 1 \end{aligned}$ <hr/> </div> <p>The final values of those quarter-pixels may be finally clipped to values in the interval (0,255).</p> <p>In order to keep the highest possible precision through the intermediate steps, any shifting, rounding and clipping operations may be avoided until the very last step of the interpolation process. Unlike in the H.264 standard defined implementation, the pixels at quarter-pixel positions labeled as “a,” “c,” “d,” “l,” “f,” “i,” “k,” and “n” are derived according to Table 3 by averaging the two nearest pixels at integer and half-pixel positions “b,” “h,” “j,” “ee” and “hh,” with upward rounding.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<div data-bbox="722 402 1257 613"> <p style="text-align: center;">TABLE 3</p> <hr/> <pre> a = (C3 << 5 + b1 + 32) >> 6 c = (C4 << 5 + b1 + 32) >> 6 d = (C3 << 5 + h1 + 32) >> 6 l = (D3 << 5 + h1 + 32) >> 6 f = (j1 >> 5 + b1 + 32) >> 6 i = (j1 >> 5 + h1 + 32) >> 6 k = (j1 >> 5 + ee1 + 32) >> 6 n = (j1 >> 5 + hh1 + 32) >> 6 </pre> <hr/> </div> <p data-bbox="705 678 1906 821">In this case, the intermediate values denoted as “ee1” and “hh1” are derived by applying the 6-tap filter vertically and horizontally in the same manner as the calculation of “h1” and “b1” listed above, but at the positions of “ee” and “hh.” Rounding may be avoided at this stage in the generation of “ee1” and “hh1” as described herein.</p> <p data-bbox="705 862 1906 969">The pixels at quarter-pixel positions labeled as “e,” “g,” “m,” and “o” may be calculated according to Table 4 by averaging the two nearest pixels at half-pixel positions in the diagonal direction with upward rounding.</p> <div data-bbox="716 1019 1251 1154"> <p style="text-align: center;">TABLE 4</p> <hr/> <pre> e = (b1 + h1 + 32) >> 6 g = (b1 + ee1 + 32) >> 6 m = (h1 + hh1 + 32) >> 6 o = (ee1 + hh1 + 32) >> 6 </pre> <hr/> </div> <p data-bbox="705 1218 1906 1395">The final values of those quarter-pixels may be clipped to values in the range (0,255). By preserving the full precision of the intermediate values, the interpolated sub-pixels will be more accurate. In particular, half-pixel values at locations “b,” “h,” “ee,” and “hh” may be unrounded values denoted as “b1,” “h1,” “ee1,” and “hh1” for purposes of quarter-pixel generation.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>Unfortunately, interpolation of value “j” can be more complicated since the interpolation at position j requires two levels of half-pixel interpolation. In particular, to interpolate “j,” twenty bits may be required to represent the intermediate value “j1.” In this case, the intermediate version “j1” of “j” may be rounded to fit within sixteen bits. In this case, the operations discussed above may be implemented with minor modification so that all operations can fit within sixteen bit data structures without sacrificing precision. The interpolation of “b” and “h” may be the same as that defined above, except that their intermediate versions “b1” and “h1” may be stored for other sub-pixel interpolation. The following Tables show the interpolation process for other sub-pixels in sixteen bit storage elements. In the Tables below, the operations defined in each column are performed sequentially through the respective table. Shaded rows contain operations that are performed at the end of the process, and are not used for interpolation of other sub-pixel positions.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																																													
	<div>TABLE 5</div> <div>positions {s, c, d, l} of FIGS. 4A-4D</div> <table><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>$r1 = x$</td><td>$r1$ is integer pixel x</td><td>0</td><td>255</td><td>8u</td></tr><tr><td>$r1 = r1 \ll 5$</td><td>$r1$ is $32 * x$</td><td>0</td><td>8160</td><td>13u</td></tr><tr><td>$r2 = y0$</td><td>$r2$ is $y0$ ($y0$ is a one-dimensional (1-D) half-pixel such as $b1$, $h1$, $ee1$ and $hh1$ before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>$r1 = r1 + r2$</td><td>$r1$ is $32 * x + y0$</td><td>-2550</td><td>18870</td><td>16s</td></tr><tr><td>$r1 = r1 + 32$</td><td>$r1$ is $32 * x + y0 + 32$</td><td>-2518</td><td>18902</td><td>16s</td></tr><tr><td>$r1 = r1 \gg 6$</td><td>$r1$ is $(32 * x + y0 + 32) \gg 6$</td><td>-39</td><td>295</td><td>11s</td></tr><tr><td>$r1 = \max(0, r1)$</td><td>clip $r1$ on the low side</td><td>0</td><td>295</td><td>10u</td></tr><tr><td>$r1 = \min(255, r1)$</td><td>clip $r1$ on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table>	Operation	Comment	Min value	Max value	Register size	$r1 = x$	$r1$ is integer pixel x	0	255	8u	$r1 = r1 \ll 5$	$r1$ is $32 * x$	0	8160	13u	$r2 = y0$	$r2$ is $y0$ ($y0$ is a one-dimensional (1-D) half-pixel such as $b1$, $h1$, $ee1$ and $hh1$ before shifting down)	-2550	10710	15s	$r1 = r1 + r2$	$r1$ is $32 * x + y0$	-2550	18870	16s	$r1 = r1 + 32$	$r1$ is $32 * x + y0 + 32$	-2518	18902	16s	$r1 = r1 \gg 6$	$r1$ is $(32 * x + y0 + 32) \gg 6$	-39	295	11s	$r1 = \max(0, r1)$	clip $r1$ on the low side	0	295	10u	$r1 = \min(255, r1)$	clip $r1$ on the high side	0	255	8u
Operation	Comment	Min value	Max value	Register size																																										
$r1 = x$	$r1$ is integer pixel x	0	255	8u																																										
$r1 = r1 \ll 5$	$r1$ is $32 * x$	0	8160	13u																																										
$r2 = y0$	$r2$ is $y0$ ($y0$ is a one-dimensional (1-D) half-pixel such as $b1$, $h1$, $ee1$ and $hh1$ before shifting down)	-2550	10710	15s																																										
$r1 = r1 + r2$	$r1$ is $32 * x + y0$	-2550	18870	16s																																										
$r1 = r1 + 32$	$r1$ is $32 * x + y0 + 32$	-2518	18902	16s																																										
$r1 = r1 \gg 6$	$r1$ is $(32 * x + y0 + 32) \gg 6$	-39	295	11s																																										
$r1 = \max(0, r1)$	clip $r1$ on the low side	0	295	10u																																										
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ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																																								
	<div>TABLE 6</div> <div><u>positions {e, g, m, o} of FIGS. 4A-4D</u></div> <table><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>$r1 = y0$</td><td>$r1$ is $y0$ ($y0$ is a 1-D half-pixel such as $b1$, $h1$, $ee1$ and $hh1$ before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>$r2 = y1$</td><td>$r2$ is $y1$ ($y1$ is a 1-D half-pixel such as $b1$, $h1$, $ee1$ and $hh1$ before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>$r1 = r1 + r2$</td><td>$r1$ is $y0 + y1$</td><td>-5100</td><td>21420</td><td>16s</td></tr><tr><td>$r1 = r1 + 32$</td><td>$r1$ is $y0 + y1 + 32$</td><td>-5068</td><td>21452</td><td>16s</td></tr><tr><td>$r1 = r1 \gg 6$</td><td>$r1$ is $(y0 + y1 + 32) \gg 6$</td><td>-79</td><td>335</td><td>11s</td></tr><tr><td>$r1 = \max(0, r1)$</td><td>clip $r1$ on the low side</td><td>0</td><td>335</td><td>10u</td></tr><tr><td>$r1 = \min(255, r1)$</td><td>clip $r1$ on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table> <p>Table 7, below, illustrates a sixteen-bit implementation of deriving pixel value at pixel location “j.” All 1-D half-pixels “aa1,” “bb1,” “b1,” “hh1,” “ii1” and “jj1” are obtained without any shift down. The clipping range to avoid overflow is (−2372,18640). Again shaded rows in the table indicate operations that are only performed at the end of the interpolation process, and these results are not used for the interpolation of other sub-pixel positions. The intermediate value “j1” will be saved for the interpolation of “f,” “i,” “k” and “n.”</p>	Operation	Comment	Min value	Max value	Register size	$r1 = y0$	$r1$ is $y0$ ($y0$ is a 1-D half-pixel such as $b1$, $h1$, $ee1$ and $hh1$ before shifting down)	-2550	10710	15s	$r2 = y1$	$r2$ is $y1$ ($y1$ is a 1-D half-pixel such as $b1$, $h1$, $ee1$ and $hh1$ before shifting down)	-2550	10710	15s	$r1 = r1 + r2$	$r1$ is $y0 + y1$	-5100	21420	16s	$r1 = r1 + 32$	$r1$ is $y0 + y1 + 32$	-5068	21452	16s	$r1 = r1 \gg 6$	$r1$ is $(y0 + y1 + 32) \gg 6$	-79	335	11s	$r1 = \max(0, r1)$	clip $r1$ on the low side	0	335	10u	$r1 = \min(255, r1)$	clip $r1$ on the high side	0	255	8u
Operation	Comment	Min value	Max value	Register size																																					
$r1 = y0$	$r1$ is $y0$ ($y0$ is a 1-D half-pixel such as $b1$, $h1$, $ee1$ and $hh1$ before shifting down)	-2550	10710	15s																																					
$r2 = y1$	$r2$ is $y1$ ($y1$ is a 1-D half-pixel such as $b1$, $h1$, $ee1$ and $hh1$ before shifting down)	-2550	10710	15s																																					
$r1 = r1 + r2$	$r1$ is $y0 + y1$	-5100	21420	16s																																					
$r1 = r1 + 32$	$r1$ is $y0 + y1 + 32$	-5068	21452	16s																																					
$r1 = r1 \gg 6$	$r1$ is $(y0 + y1 + 32) \gg 6$	-79	335	11s																																					
$r1 = \max(0, r1)$	clip $r1$ on the low side	0	335	10u																																					
$r1 = \min(255, r1)$	clip $r1$ on the high side	0	255	8u																																					

ASSERTED CLAIMS

PRIOR ART REFERENCE: KARCZEWICZ II

TABLE 7

position [j] of FIGS. 4A-4D

Operation	Comment	Min value	Max value	Register size
$r1 = y0$	$y0$ is 1-D half-pixel a0 before shifting down	-2550	10710	15s
$r2 = y5$	$y5$ is 1-D half-pixel j1 before shifting down	-2550	10710	15s
$r1 = r1 + r2$	$r1$ is $y0 + y5$	-5100	21420	16s
$r2 = y1$	$y1$ is 1-D half-pixel b0 before shifting down	-2550	10710	15s
$r3 = y4$	$y4$ is 1-D half-pixel i1 before shifting down	-2550	10710	15s
$r2 = r2 + r3$	$r2$ is $y1 + y4$	-5100	21420	16s
$r1 = r1 - r2$	$r1$ is $(y0 + y5) - (y1 + y4)$	-26520	26520	16s
$r1 = r1 >> 2$	$r1$ is $((y0 + y5) - (y1 + y4)) >> 2$	-6630	6630	14s
$r3 = y2$	$y2$ is 1-D half-pixel b1 before shifting down	-2550	10710	15s
$r4 = y3$	$y3$ is 1-D half-pixel h1 before shifting down	-2550	10710	15s
$r3 = r3 + r4$	$r3$ is $y2 + y3$	-5100	21420	16s
$r3 = \max(-2372, r3)$	clip $r3$ to avoid overflow (low side)	-2372	21420	16s
$r3 = \min(18640, r3)$	clip $r3$ to avoid overflow (high side)	-2372	18640	16s
$r2 = r3 - r4$	$r2$ is $(y2 + y3) - (y1 + y4)$	-23792	23740	16s
$r1 = r1 + r2$	$r1$ is $((y0 + y5) - 5*(y1 + y4)) + 4*(y2 + y3) >> 2$	-30422	30370	16s
$r1 = r1 >> 2$	$r1$ is $((y0 + y5) - 5*(y1 + y4)) + 4*(y2 + y3) >> 4$	-7605	7592	14s
$r1 = r1 + r3$	$r1$ is $((y0 + y5) - 5*(y1 + y4)) + 20*(y2 + y3) >> 4$	-9977	26232	16s
$j1 = r1$	store result			
$r1 = r1 > 32$	$r1$ is $((y0 + y5) - 5*(y1 + y4)) + 20*(y2 + y3) + 512 >> 4$	-5945	26264	16s
$r1 = r1 >> 5$	$r1$ is $((y0 + y5) - 5*(y1 + y4)) + 20*(y2 + y3) + 512 >> 10$	-155	410	10s
$r1 = \max(0, r1)$	clip $r1$ on the low side	0	410	9s
$r1 = \min(255, r1)$	clip $r1$ on the high side	0	255	8s

Table 8, below demonstrates steps that can be taken for sixteen-bit implementation of interpolating {f,i,k,n}, which are the positions that use to interpolate the intermediate value “j1.”

ASSERTED CLAIMS

PRIOR ART REFERENCE: KARCZEWICZ II

TABLE 8

positions {f, i, k, n} of FIGS. 4A-4D

Operation	Comment	Min value	Max value	Register size
r1 = y0	r1 is y0 (1-D half-pixel such as b1, h1, eel and hh1 before shifting down)	-2550	10710	15s
r2 = j1	r2 is j1 (2-D half-pixel j1 before shifting down)	-9914	26232	16s

TABLE 8-continued

positions {f, i, k, n} of FIGS. 4A-4D

Operation	Comment	Min value	Max value	Register size
r2 = r2 >> 1	r2 is j1 >> 1	-4957	13116	15s
r1 = r1 + r2	r1 is y0 + (j1 >> 1)	-7507	23826	16s
r1 = r1 + 32	r1 is y0 + (j1 >> 1) + 32	-7491	23842	16s
r1 = r1 >> 6	r1 is (y0 + (j1 >> 1) + 32) >> 6	-235	745	11s
r1 = max (0, r1)	clip r1 on the low side	0	745	10u
r1 = min (255, r1)	clip r1 on the high side	0	255	8u

In short, the discussion above demonstrates a complete technique for interpolating every possible sub-pixel location “a,” “b,” “c,” “d,” “e,” “f,” “g,” “h,” “i,” “j,” “k,” “l,” “m,” “n,” and “o” without exceeding sixteen-bit storage elements, which is desirable for an implementation. Most intermediate half-pixel values remain unrounded for purposes of quarter-pixel interpolation. However, pixel location “j” is a special case that may require rounding of intermediate results for purposes of generating its half-pixel value since pixel location “j” requires two levels of half-pixel interpolation. In accordance with this disclosure, however, the rounding in pixel location “j” will not impact the accuracy of the final value of quarter-pixels which rely on “j.”

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
<p>6. The method according to claim 1, wherein the first precision indicates a number of bits needed to represent the values of the pixels, and the second precision indicates the number of bits needed to represent values of said first prediction and values of said second prediction.</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses that the first precision indicates a number of bits needed to represent the values of the pixels, and the second precision indicates the number of bits needed to represent values of the first prediction and values of the second prediction.</p> <p><i>See, e.g.,</i></p> <p>Karczewicz II, ¶0089:</p> <p>The average filter for a particular sub-pixel position may also be normalized such that all filter coefficients add up to 1.0. The average filter may also be quantized to a certain fixed-point precision (e.g., 13-bit precision). The use of fixed-point precision in the filter coefficient ensures that implementations across different platforms will not have drifts. Furthermore, a true fixed-point implementation may be derived from such pre-defined filter coefficients. Any customized filters may have different characteristics. Specifically, different filters may be non-separable filters or separable filters, and different filters may define different filter supports (e.g., 6×6 or 4×4).</p> <p>Karczewicz II, ¶0103:</p> <p>Unfortunately, interpolation of value “j” can be more complicated since the interpolation at position j requires two levels of half-pixel interpolation. In particular, to interpolate “j,” twenty bits may be required to represent the intermediate value “j1.” In this case, the</p>

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	<p>intermediate version “j1” of “j” may be rounded to fit within sixteen bits. In this case, the operations discussed above may be implemented with minor modification so that all operations can fit within sixteen bit data structures without sacrificing precision. The interpolation of “b” and “h” may be the same as that defined above, except that their intermediate versions “b1” and “h1” may be stored for other sub-pixel interpolation. The following Tables show the interpolation process for other sub-pixels in sixteen bit storage elements. In the Tables below, the operations defined in each column are performed sequentially through the respective table. Shaded rows contain operations that are performed at the end of the process, and are not used for interpolation of other sub-pixel positions.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																																													
	<div>TABLE 5</div> <div><div>positions {a, c, d, l} of FIGS. 4A-4D</div><table><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r1 = x</td><td>r1 is integer pixel x</td><td>0</td><td>255</td><td>8u</td></tr><tr><td>r1 = r1 << 5</td><td>r1 is 32 * x</td><td>0</td><td>8160</td><td>13u</td></tr><tr><td>r2 = y0</td><td>r2 is y0 (y0 is a one-dimensional (1-D) half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r1 = r1 + r2</td><td>r1 is 32 * x + y0</td><td>-2550</td><td>18870</td><td>16s</td></tr><tr><td>r1 = r1 + 32</td><td>r1 is 32 * x + y0 + 32</td><td>-2518</td><td>18902</td><td>16s</td></tr><tr><td>r1 = r1 >> 6</td><td>r1 is (32 * x + y0 + 32) >> 6</td><td>-39</td><td>295</td><td>11s</td></tr><tr><td>r1 = max (0, r1)</td><td>clip r1 on the low side</td><td>0</td><td>295</td><td>10u</td></tr><tr><td>r1 = min (255, r1)</td><td>clip r1 on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table></div>	Operation	Comment	Min value	Max value	Register size	r1 = x	r1 is integer pixel x	0	255	8u	r1 = r1 << 5	r1 is 32 * x	0	8160	13u	r2 = y0	r2 is y0 (y0 is a one-dimensional (1-D) half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r1 = r1 + r2	r1 is 32 * x + y0	-2550	18870	16s	r1 = r1 + 32	r1 is 32 * x + y0 + 32	-2518	18902	16s	r1 = r1 >> 6	r1 is (32 * x + y0 + 32) >> 6	-39	295	11s	r1 = max (0, r1)	clip r1 on the low side	0	295	10u	r1 = min (255, r1)	clip r1 on the high side	0	255	8u
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	<div>TABLE 6</div> <div><table><tr><th colspan="5">positions {e, g, m, o} of FIGS. 4A-4D</th></tr><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r1 = y0</td><td>r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r2 = y1</td><td>r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r1 = r1 + r2</td><td>r1 is y0 + y1</td><td>-5100</td><td>21420</td><td>16s</td></tr><tr><td>r1 = r1 + 32</td><td>r1 is y0 + y1 + 32</td><td>-5068</td><td>21452</td><td>16s</td></tr><tr><td>r1 = r1 >> 6</td><td>r1 is (y0 + y1 + 32) >> 6</td><td>-79</td><td>335</td><td>11s</td></tr><tr><td>r1 = max(0, r1)</td><td>clip r1 on the low side</td><td>0</td><td>335</td><td>10u</td></tr><tr><td>r1 = min(255, r1)</td><td>clip r1 on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table></div> <div>Karczewicz II, ¶0105: Table 8, below demonstrates steps that can be taken for sixteen-bit implementation of interpolating {f,i,k,n}, which are the positions that use to interpolate the intermediate value “j1.”</div>	positions {e, g, m, o} of FIGS. 4A-4D					Operation	Comment	Min value	Max value	Register size	r1 = y0	r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r2 = y1	r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r1 = r1 + r2	r1 is y0 + y1	-5100	21420	16s	r1 = r1 + 32	r1 is y0 + y1 + 32	-5068	21452	16s	r1 = r1 >> 6	r1 is (y0 + y1 + 32) >> 6	-79	335	11s	r1 = max(0, r1)	clip r1 on the low side	0	335	10u	r1 = min(255, r1)	clip r1 on the high side	0	255	8u
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ASSERTED CLAIMS

PRIOR ART REFERENCE:

KARCZEWICZ II

TABLE 8

positions {f, i, k, n} of FIGS. 4A-4D

Operation	Comment	Min value	Max value	Register size
r1 = y0	r1 is y0 (1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s
r2 = j1	r2 is j1 (2-D half-pixel j1 before shifting down)	-9914	26232	16s

TABLE 8-continued

positions {f, i, k, n} of FIGS. 4A-4D

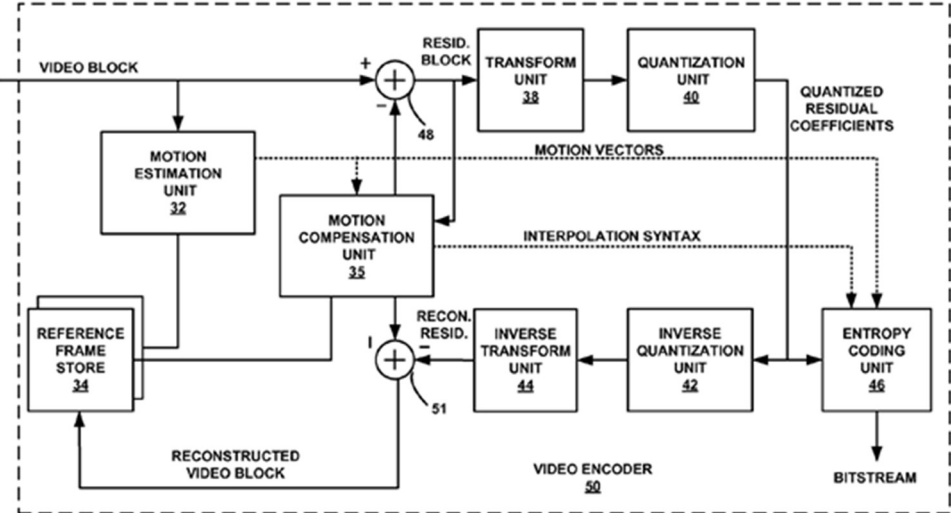
Operation	Comment	Min value	Max value	Register size
r2 = r2 >> 1	r2 is j1 >> 1	-4957	13116	15s
r1 = r1 + r2	r1 is y0 + (j1 >> 1)	-7507	23826	16s
r1 = r1 + 32	r1 is y0 + (j1 >> 1) + 32	-7491	23842	16s
r1 = r1 >> 6	r1 is (y0 + (j1 >> 1) + 32) >> 6	-235	745	11s
r1 = max (0, r1)	clip r1 on the low side	0	745	10u
r1 = min (255, r1)	clip r1 on the high side	0	255	8u

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
<p>7[preamble] An apparatus for encoding a block of pixels, the apparatus comprising:</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses an apparatus for encoding a block of pixels.</p> <p><i>See, e.g.,</i></p> <p><i>See limitation 1[preamble].</i></p> <p>Karczewicz II, ¶0003:</p> <p>Digital video capabilities can be incorporated into a wide range of devices, including digital televisions, digital direct broadcast systems, wireless broadcast systems, personal digital assistants (PDAs), laptop or desktop computers, digital cameras, digital recording devices, video gaming devices, video game consoles, cellular or satellite radio telephones, and the like. Digital video devices implement video compression techniques, such as MPEG-2, MPEG-4, or ITU-T H.264/MPEG-4, Part 10, Advanced Video Coding (AVC), to transmit and receive digital video information more efficiently. Video compression techniques may perform spatial prediction and/or temporal prediction to reduce or remove redundancy inherent in video sequences.</p> <p>Karczewicz II, ¶0015:</p> <p>In another example, this disclosure provides an apparatus that encodes video data, the apparatus comprising a video encoder that includes a motion compensation unit that generates prediction data. The motion compensation unit interpolates pixel values of reference video</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>data based on a plurality of different pre-defined interpolation filters, and the video encoder encodes the video data based on the prediction data.</p> <p>Karczewicz II, ¶0017:</p> <p>In another example, this disclosure provides an apparatus that interpolates predictive video data for video coding, wherein the apparatus includes a motion compensation unit that generates half-pixel values based on integer pixel values, rounds the half-pixel values to generate half-pixel interpolated values, stores the half-pixel values as non-rounded versions of the half-pixel values, and generates quarter-pixel values based on the non-rounded versions of the half-pixel values and the integer pixel values.</p> <p>Karczewicz II, ¶0021:</p> <p>In another example, this disclosure provides a device that encodes video data, the device comprising a video encoder including a motion compensation unit that generates prediction data by interpolating pixel values of reference video data based on a plurality of different pre-defined interpolation filters, and encodes the video data based on the prediction data, and a wireless transmitter that transmits the video data to another device.</p> <p>Karczewicz II, ¶0023:</p> <p>In another example, this disclosure provides a device that encodes video data, the device comprising a video encoder including a motion compensation unit that generates half-pixel values based on integer pixel values, rounds the half-pixel values to generate half-pixel interpolated values, stores the half-pixel values as non-rounded versions of the half-pixel values, and generates quarter-pixel values based on the non-rounded versions of the half-pixel values and the integer pixel values, and a wireless transmitter that transmits the video data</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>encoded by the motion compensation unit to another device.</p> <p>Karczewicz II, ¶0025:</p> <p>The techniques described in this disclosure may be implemented in hardware, software, firmware, or any combination thereof. If implemented in software, the software may be executed in one or more processors, such as a microprocessor, application specific integrated circuit (ASIC), field programmable gate array (FPGA), or digital signal processor (DSP). The software that executes the techniques may be initially stored in a computer-readable medium and loaded and executed in the processor.</p> <p>Karczewicz II, ¶0026:</p> <p>Accordingly, this disclosure also contemplates a computer-readable storage medium comprising instructions that upon execution cause a device to encode video data, wherein the instructions cause the device to generate prediction data, wherein generating the prediction data includes interpolating pixel values of reference video data based on a plurality of different pre-defined interpolation filters, and encode the video data based on the prediction data.</p> <p>Karczewicz II, ¶0048:</p> <p>Video encoder 22 and video decoder 28 each may be implemented as one or more microprocessors, digital signal processors (DSPs), application specific integrated circuits (ASICs), field programmable gate arrays (FPGAs), discrete logic, software, hardware, firmware or any combinations thereof. Each of video encoder 22 and video decoder 28 may be included in one or more encoders or decoders, either of which may be integrated as part of a combined CODEC in a respective mobile device, subscriber device, broadcast device,</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>server, or the like.</p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
<p>7[a] at least one processor and at least one memory including computer program code, the at least one memory and computer program code configured to, with the at least one processor, cause the apparatus to:</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses at least one processor and at least one memory including computer program code.</p> <p><i>See, e.g.,</i></p> <p>Karczewicz II, ¶0004:</p> <p>Block based inter-coding is a very useful coding technique that relies on temporal prediction to reduce or remove temporal redundancy between video blocks of successive coded units of a video sequence. The coded units may comprise video frames, slices of video frames, groups of pictures, or another defined unit of video blocks. For inter-coding, the video encoder performs motion estimation and motion compensation to track the movement of corresponding video blocks of two or more adjacent coded units. Motion estimation generates motion vectors, which indicate the displacement of video blocks relative to corresponding prediction video blocks in one or more reference frames or other coded units. Motion compensation uses the motion vectors to generate prediction video blocks from the reference frame or other coded unit. After motion compensation, residual video blocks are formed by subtracting prediction video blocks from the original video blocks being coded.</p>

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	<p>Karczewicz II, Fig. 2:</p>  <p>FIG. 2</p> <p>Karczewicz II, ¶0056:</p> <p>During the encoding process, video encoder 50 receives a video block to be coded, and motion estimation unit 32 and motion compensation unit 35 perform inter-predictive coding. Motion estimation unit 32 and motion compensation unit 35 may be highly integrated, but are illustrated separately for conceptual purposes. Motion estimation is typically considered the process of generating motion vectors, which estimate motion for video blocks. A motion vector, for example, may indicate the displacement of a predictive block within a predictive</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>frame (or other coded unit) relative to the current block being coded within the current frame (or other coded unit). Motion compensation is typically considered the process of fetching or generating the predictive block based on the motion vector determined by motion estimation. Again, motion estimation unit 32 and motion compensation unit 35 may be functionally integrated. The interpolation techniques described in this disclosure are described as being performed by motion compensation unit 35. However, interpolation may be performed during motion estimation in order to facilitate the selection of the best motion vectors.</p> <p>Karczewicz II, ¶0057:</p> <p>In accordance with this disclosure, motion estimation 32 selects the appropriate motion vector for the video block to be coded by comparing the video block to video blocks of a predictive coded unit (e.g., a previous frame). At this point, motion compensation unit 35 may perform interpolation in order to generate predictive data at sub-pixel resolution. In some cases, during motion estimation, the interpolation may be based on a fixed interpolation filter. In other cases, the different interpolation filters applied during motion compensation (as outlined below) may also be used during the motion estimation process for purposes of motion vector selection.</p> <p>Karczewicz II, ¶0058:</p> <p>Once motion estimation unit 32 has selected the motion vector for the video block to be coded, motion compensation unit 35 generates the predictive video block associated with that motion vector. According to this disclosure, however, motion compensation unit 35 may consider several versions of any predictive video block that has sub-pixel resolution. In this case, motion compensation unit 35 may apply a plurality of pre-defined interpolation filters in order to generate a plurality of different interpolated prediction data for the video block to be coded. Motion compensation unit 35 then selects the interpolated prediction data (e.g., an</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>interpolated video block associated with one of the interpolation filters) that achieves the highest levels of compression. The interpolation filter that was used to generate the interpolated data may be coded as interpolation syntax and communicated to entropy coding unit 46 for inclusion in the coded bitstream. Once motion compensation unit 35 has selected and applied the best interpolation filter, the motion compensation unit 35 generates the predictive data using that interpolation filter, and video encoder applies adder 48 to subtract that predictive data from the video block being coded to generate residual data.</p> <p>Karczewicz II, ¶0073:</p> <p>After motion estimation, the best motion vector for a given video block may be identified, e.g., possibly using a rate-distortion model to balance the coding rate and quality. Then, the prediction video block is formed during motion compensation using the best motion vector. As outlined above, the residual video block is formed by subtracting the prediction video block from the original video block. A transform is then applied on the residual block, and the transform coefficients are quantized and entropy coded to further reduce bit rate.</p> <p>Karczewicz II, ¶0089:</p> <p>The average filter for a particular sub-pixel position may also be normalized such that all filter coefficients add up to 1.0. The average filter may also be quantized to a certain fixed-point precision (e.g., 13-bit precision). The use of fixed-point precision in the filter coefficient ensures that implementations across different platforms will not have drifts. Furthermore, a true fixed-point implementation may be derived from such pre-defined filter coefficients. Any customized filters may have different characteristics. Specifically, different filters may be non-separable filters or separable filters, and different filters may define different filter supports (e.g., 6×6 or 4×4).</p>

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ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II																																													
	<div>TABLE 6</div> <div><table><tr><th colspan="5">positions {e, g, m, o} of FIGS. 4A-4D</th></tr><tr><th>Operation</th><th>Comment</th><th>Min value</th><th>Max value</th><th>Register size</th></tr><tr><td>r1 = y0</td><td>r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r2 = y1</td><td>r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)</td><td>-2550</td><td>10710</td><td>15s</td></tr><tr><td>r1 = r1 + r2</td><td>r1 is y0 + y1</td><td>-5100</td><td>21420</td><td>16s</td></tr><tr><td>r1 = r1 + 32</td><td>r1 is y0 + y1 + 32</td><td>-5068</td><td>21452</td><td>16s</td></tr><tr><td>r1 = r1 >> 6</td><td>r1 is (y0 + y1 + 32) >> 6</td><td>-79</td><td>335</td><td>11s</td></tr><tr><td>r1 = max(0, r1)</td><td>clip r1 on the low side</td><td>0</td><td>335</td><td>10u</td></tr><tr><td>r1 = min(255, r1)</td><td>clip r1 on the high side</td><td>0</td><td>255</td><td>8u</td></tr></table></div> <div><p>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</p></div>	positions {e, g, m, o} of FIGS. 4A-4D					Operation	Comment	Min value	Max value	Register size	r1 = y0	r1 is y0 (y0 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r2 = y1	r2 is y1 (y1 is a 1-D half-pixel such as b1, h1, ee1 and hh1 before shifting down)	-2550	10710	15s	r1 = r1 + r2	r1 is y0 + y1	-5100	21420	16s	r1 = r1 + 32	r1 is y0 + y1 + 32	-5068	21452	16s	r1 = r1 >> 6	r1 is (y0 + y1 + 32) >> 6	-79	335	11s	r1 = max(0, r1)	clip r1 on the low side	0	335	10u	r1 = min(255, r1)	clip r1 on the high side	0	255	8u
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7[b] determine, for a current block, a first reference block based on a first motion vector and a second reference block	Karczewicz II, as evidenced by the example citations below, discloses determining, for a current block, a first reference block based on a first motion vector and a second reference block based on a second motion vector, wherein the pixels of the current block, the first reference block, and the																																													

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
<p>based on a second motion vector, wherein the pixels of the current block, the first reference block, and the second reference block have values with a first precision;</p>	<p>second reference block have values with a first precision.</p> <p><i>See, e.g.,</i></p> <p><i>See limitation 1[a].</i></p>
<p>7[c] use said first reference block to obtain a first prediction, said first prediction having a second precision, which is higher than said first precision;</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses using the first reference block to obtain a first prediction, the first prediction having a second precision, which is higher than the first precision.</p> <p><i>See, e.g.,</i></p> <p><i>See limitation 1[b].</i></p>
<p>7[d] use said second reference block to obtain a second prediction, said second prediction having the second precision;</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses using the second reference block to obtain a second prediction, the second prediction having the second precision.</p> <p><i>See, e.g.,</i></p> <p><i>See limitation 1[c].</i></p>
<p>7[e] obtain a combined prediction based at least partly upon said first prediction and said second prediction;</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses obtaining a combined prediction based at least partly upon the first prediction and the second prediction.</p> <p><i>See, e.g.,</i></p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<i>See limitation 1[d].</i>
7[f] decrease a precision of said combined prediction by shifting bits of the combined prediction to the right; and	<p>Karczewicz II, as evidenced by the example citations below, discloses decreasing a precision of the combined prediction by shifting bits of the combined prediction to the right.</p> <p><i>See, e.g.,</i></p> <p><i>See limitation 1[e].</i></p>
7[g] encode residual data in a bitstream, wherein the residual data is determined based upon a difference between the combined prediction and the block of pixels.	<p>Karczewicz II, as evidenced by the example citations below, discloses encoding residual data in a bitstream, wherein the residual data is determined based upon a difference between the combined prediction and the block of pixels.</p> <p><i>See, e.g.,</i></p> <p><i>See limitation 1[f].</i></p>
8. The apparatus according to claim 7, wherein in an instance in which said first motion vector points to a subpixel, said first prediction is obtained by interpolation using pixel values of said first reference block.	<p>Karczewicz II, as evidenced by the example citations below, discloses that in an instance in which the first motion vector points to a subpixel, the first prediction is obtained by interpolation using pixel values of the first reference block.</p> <p><i>See, e.g.,</i></p> <p><i>See claim 2.</i></p>
9. The apparatus according to	Karczewicz II, as evidenced by the example citations below, discloses that the first prediction is obtained by interpolation using values of the first reference block by right shifting a sum of a P-tap

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
claim 8, wherein said first prediction is obtained by interpolation using values of said first reference block by: right shifting a sum of a P-tap filter using values of said first reference block.	filter using values of the first reference block. <i>See, e.g.,</i> <i>See claim 3.</i>
10. The apparatus according to claim 8, wherein in an instance in which said second motion vector points to an integer sample, said second prediction is obtained by shifting values of said second reference block to the left.	Karczewicz II, as evidenced by the example citations below, discloses that in an instance in which the second motion vector points to an integer sample, the second prediction is obtained by shifting values of the second reference block to the left. <i>See, e.g.,</i> <i>See claim 4.</i>
11. The apparatus according to claim 7, wherein the at least one memory and computer code are configured to cause the apparatus to decrease said precision of said combined prediction by shifting bits of the combined prediction to the right, by: inserting a rounding offset to the combined prediction before said decreasing.	Karczewicz II, as evidenced by the example citations below, discloses that the at least one memory and computer code are configured to cause the apparatus to decrease the precision of the combined prediction by shifting bits of the combined prediction to the right, by inserting a rounding offset to the combined prediction before the decreasing. <i>See, e.g.,</i> <i>See claim 5.</i> <i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the</i>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<i>references identified in Exhibit E-9</i>
<p>12. The apparatus according to claim 7, wherein the first precision indicates a number of bits needed to represent the values of the pixels, and the second precision indicates the number of bits needed to represent values of said first prediction and values of said second prediction.</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses that the first precision indicates a number of bits needed to represent the values of the pixels, and the second precision indicates the number of bits needed to represent values of the first prediction and values of the second prediction.</p> <p><i>See, e.g.,</i></p> <p><i>See claim 6.</i></p>
<p>13[preamble] A computer program product for encoding a block of pixels, the computer program product comprising at least one non-transitory computer readable storage medium having computer executable program code portions stored therein, the computer executable program code portions comprising program code instructions configured to:</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses a computer program product for encoding a block of pixels, the computer program product comprising at least one non-transitory computer readable storage medium having computer executable program code portions stored therein.</p> <p><i>See, e.g.,</i></p> <p><i>See limitations 1[preamble], 7[preamble], and 7[a].</i></p> <p>Karczewicz II, ¶0028:</p> <p>In addition, this disclosure also contemplates a computer-readable storage medium comprising instructions that upon execution cause a device to interpolate predictive video data for video coding, wherein the instructions cause the device to generate half-pixel values based on integer pixel values, round the half-pixel values to generate half-pixel interpolated</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>values, store the half-pixel values as non-rounded versions of the half-pixel values, and generate quarter-pixel values based on the non-rounded versions of the half-pixel values and the integer pixel values.</p> <p>Accordingly, this disclosure also contemplates a computer-readable storage medium comprising instructions that upon execution cause a device to encode video data, wherein the instructions cause the device to generate prediction data, wherein generating the prediction data includes interpolating pixel values of reference video data based on a plurality of different pre-defined interpolation filters, and encode the video data based on the prediction data.</p>
<p>13[a] determine, for a current block, a first reference block based on a first motion vector and a second reference block based on a second motion vector, wherein the pixels of the current block, the first reference block, and the second reference block have values with a first precision;</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses determining, for a current block, a first reference block based on a first motion vector and a second reference block based on a second motion vector, wherein the pixels of the current block, the first reference block, and the second reference block have values with a first precision.</p> <p><i>See, e.g.,</i></p> <p><i>See limitation 1[a].</i></p>
<p>13[b] use said first reference block to obtain a first prediction, said first prediction having a second precision, which is higher than said first precision;</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses using the first reference block to obtain a first prediction, the first prediction having a second precision, which is higher than the first precision.</p> <p><i>See, e.g.,</i></p> <p><i>See limitation 1[b].</i></p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
13[c] use said second reference block to obtain a second prediction, said second prediction having the second precision;	Karczewicz II, as evidenced by the example citations below, discloses using the second reference block to obtain a second prediction, the second prediction having the second precision. <i>See, e.g.,</i> <i>See limitation 1[c].</i>
13[d] obtain a combined prediction based at least partly upon said first prediction and said second prediction;	Karczewicz II, as evidenced by the example citations below, discloses obtaining a combined prediction based at least partly upon the first prediction and the second prediction. <i>See, e.g.,</i> <i>See limitation 1[d].</i>
13[e] decrease a precision of said combined prediction by shifting bits of the combined prediction to the right; and	Karczewicz II, as evidenced by the example citations below, discloses decreasing a precision of the combined prediction by shifting bits of the combined prediction to the right. <i>See, e.g.,</i> <i>See limitation 1[e].</i>
13[f] encode residual data in a bitstream, wherein the residual data is determined based upon a difference between the combined prediction and the block of	Karczewicz II, as evidenced by the example citations below, discloses encoding residual data in a bitstream, wherein the residual data is determined based upon a difference between the combined prediction and the block of pixels. <i>See, e.g.,</i>

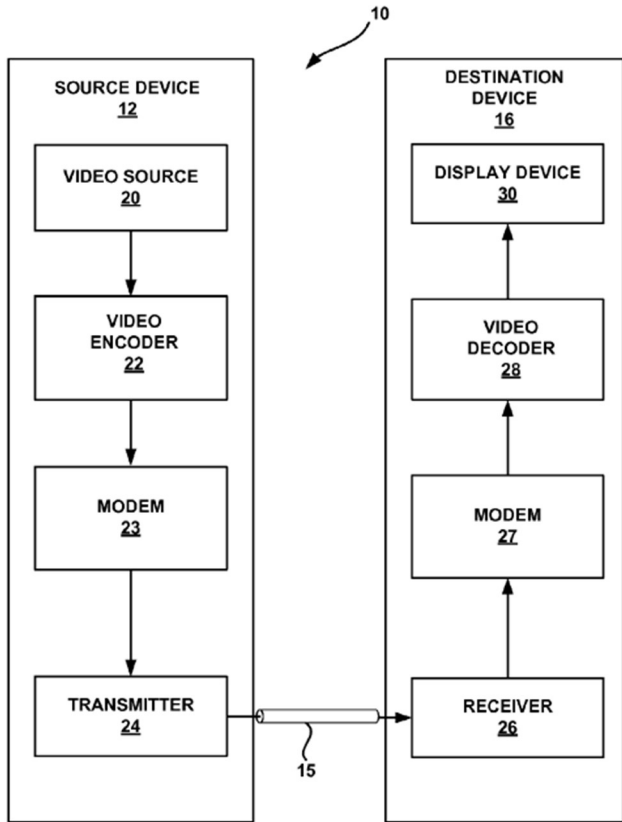
ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
pixels.	<i>See limitation 1[f].</i>
14. The computer program product according to claim 13, wherein in an instance in which said first motion vector points to a subpixel, said first prediction is obtained by interpolation using pixel values of said first reference block.	Karczewicz II, as evidenced by the example citations below, discloses that in an instance in which the first motion vector points to a subpixel, the first prediction is obtained by interpolation using pixel values of the first reference block. <i>See, e.g.,</i> <i>See claim 2</i>
15. The computer program product according to claim 14, wherein said first prediction is obtained by interpolation using values of said first reference block by: right shifting a sum of a P-tap filter using values of said first reference block.	Karczewicz II, as evidenced by the example citations below, discloses that the first prediction is obtained by interpolation using values of the first reference block by right shifting a sum of a P-tap filter using values of the first reference block. <i>See, e.g.,</i> <i>See claim 3.</i>
16. The computer program product according to claim 14, wherein in an instance in which said second motion vector points to an integer sample, said second prediction is obtained by shifting values of said second	Karczewicz II, as evidenced by the example citations below, discloses that in an instance in which the second motion vector points to an integer sample, the second prediction is obtained by shifting values of the second reference block to the left. <i>See, e.g.,</i> <i>See claim 4.</i>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
reference block to the left.	
<p>17. The computer program product according to claim 13, wherein the program code instructions configured to decrease said precision of said combined prediction by shifting bits of the combined prediction to the right, further comprise program code instructions configured to: insert a rounding offset to the combined prediction before said decreasing.</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses that the program code instructions configured to decrease the precision of the combined prediction by shifting bits of the combined prediction to the right, further comprise program code instructions configured to insert a rounding offset to the combined prediction before the decreasing.</p> <p><i>See, e.g.,</i></p> <p><i>See claims 5 and 11.</i></p>
<p>18. The computer program product according to claim 13, wherein the first precision indicates a number of bits needed to represent the values of the pixels, and the second precision indicates the number of bits needed to represent values of said first prediction and values of said second prediction.</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses that the first precision indicates a number of bits needed to represent the values of the pixels, and the second precision indicates the number of bits needed to represent values of the first prediction and values of the second prediction.</p> <p><i>See, e.g.,</i></p> <p><i>See claim 6.</i></p>
19[preamble] A method for	Karczewicz II, as evidenced by the example citations below, discloses a method for decoding a block

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
<p>decoding a block of pixels, the method comprising:</p>	<p>of pixels.</p> <p><i>See, e.g.,</i></p> <p>Karczewicz II, Abstract:</p> <p>This disclosure describes various interpolation techniques performed by an encoder and a decoder during the motion compensation process of video coding. In one example, an encoder interpolates pixel values of reference video data based on a plurality of different pre-defined interpolation filters. In this example, the decoder receives a syntax element that identifies an interpolation filter, and interpolates pixel values of reference video data based on the interpolation filter identified by the syntax element. In another example, a method of interpolating predictive video data includes generating half-pixel values based on integer pixel values, rounding the half-pixel values to generate half-pixel interpolated values, storing the half-pixel values as non-rounded versions of the half-pixel values, and generating quarter-pixel values based on the non-rounded versions of the half-pixel values and the integer pixel values.</p> <p>Karczewicz II, ¶0002:</p> <p>This disclosure relates to digital video coding and, more particularly, fractional interpolations of predictive data used in video coding.</p> <p>Karczewicz II, ¶0013:</p> <p>In another example, this disclosure provides a method of decoding video data. The decoding method comprises receiving a syntax element that identifies an interpolation filter from a plurality of different pre-defined interpolation filters, generating prediction data, wherein</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>generating the prediction data includes interpolating pixel values of reference video data based on the interpolation filter identified by the syntax element, and decoding the video data based on the prediction data.</p> <p>Karczewicz II, ¶0014:</p> <p>In another example, this disclosure provides a method of interpolating predictive video data for video coding. The method comprises generating half-pixel values based on integer pixel values, rounding the half-pixel values to generate half-pixel interpolated values, storing the half-pixel values as non-rounded versions of the half-pixel values, and generating quarter-pixel values based on the non-rounded versions of the half-pixel values and the integer pixel values.</p> <p>Karczewicz II, ¶0035:</p> <p>This disclosure describes various interpolation techniques performed by an encoder and a decoder during the motion compensation process of video coding. According to one aspect of this disclosure, the encoder may apply a plurality of pre-defined interpolation filters in order to generate a plurality of different interpolated prediction data. The interpolated prediction data that achieves the highest levels of compression may be selected at the encoder, and the interpolation filter that was used may be coded as syntax and communicated to the decoding device as part of the coded video data. The interpolated prediction data comprises reference data. Video data to be coded (e.g., a video block) may be subtracted from interpolated prediction data (e.g., an interpolated predictive reference block) in order to define a residual block of encoded data, which may then be transformed, quantized and entropy coded.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	Karczewicz II, Fig.1:

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	 <p data-bbox="732 1279 810 1312">FIG. 1</p> <p data-bbox="611 1349 894 1386">Karczewicz II, ¶0041:</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>In the example of FIG. 1, source device 12 may include a video source 20, video encoder 22, a modulator/demodulator (modem) 23 and a transmitter 24. Destination device 16 may include a receiver 26 a modem 27, a video decoder 28, and a display device 30. In accordance with this disclosure, video encoder 22 of source device 12 may be configured to apply one or more of the interpolation techniques of this disclosure as part of a video encoding process. Similarly, video decoder 28 of destination device 16 may be configured to apply one or more of the interpolation techniques of this disclosure as part of a video decoding process.</p> <p>Karczewicz II, Fig. 3:</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<div data-bbox="625 446 1711 1096"><pre>graph LR E[ENCODED VIDEO BITSTREAM] --> 52[ENTROPY DECODING UNIT 52] 52 -.-> INTERPOLATION SYNTAX 55[MOTION COMPENSATION UNIT 55] 52 -.-> MOTION VECTORS 55 52 --> QUANTIZ. COEFF. 56[INVERSE QUANTIZ. 56] 56 --> 58[INVERSE TRANSFORM 58] 58 --> RESIDUAL 64((+)) 55 --> 64 64 --> 62[REFERENCE FRAME STORE 62] 62 --> 60[VIDEO DECODER 60] 60 --> D[DECODED VIDEO]</pre><p>The diagram illustrates the internal structure of a video decoder (60). An 'ENCODED VIDEO BITSTREAM' enters from the left into the 'ENTROPY DECODING UNIT 52'. This unit has two dashed output paths: one for 'INTERPOLATION SYNTAX' and another for 'MOTION VECTORS', both leading to the 'MOTION COMPENSATION UNIT 55'. A solid arrow labeled 'QUANTIZ. COEFF.' leads from unit 52 to the 'INVERSE QUANTIZ. 56' block. This block connects to the 'INVERSE TRANSFORM 58' block, which then outputs a 'RESIDUAL' signal to a circular adder (64). The 'MOTION COMPENSATION UNIT 55' also feeds into this adder. The output of the adder is sent to the 'REFERENCE FRAME STORE 62'. A feedback loop arrow connects the 'REFERENCE FRAME STORE 62' back to the 'MOTION COMPENSATION UNIT 55'. The entire process is contained within a box labeled 'VIDEO DECODER 60'. An output arrow from the bottom of this box points to 'DECODED VIDEO'.</p></div> <p data-bbox="709 977 806 1015">FIG. 3</p> <p data-bbox="611 1149 894 1187">Karczewicz II, ¶0049:</p> <p data-bbox="705 1219 1911 1401">A video sequence typically includes a series of video frames. Video encoder 22 operates on video blocks within individual video frames in order to encode the video data. The video blocks may have fixed or varying sizes, and may differ in size according to a specified coding standard. Each video frame includes a series of slices. Each slice may include a series of macroblocks, which may be arranged into sub-blocks. As an example, the ITU-T H.264</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>standard supports intra prediction in various block sizes, such as 16 by 16, 8 by 8, or 4 by 4 for luma components, and 8×8 for chroma components, as well as inter prediction in various block sizes, such as 16 by 16, 16 by 8, 8 by 16, 8 by 8, 8 by 4, 4 by 8 and 4 by 4 for luma components and corresponding scaled sizes for chroma components. Video blocks may comprise blocks of pixel data, or blocks of transformation coefficients, e.g., following a transformation process such as discrete cosine transform or a conceptually similar transformation process.</p> <p>Karczewicz II, ¶0050:</p> <p>Smaller video blocks can provide better resolution, and may be used for locations of a video frame that include high levels of detail. In general, macroblocks (MBs) and the various sub-blocks may be considered to be video blocks. In addition, a slice may be considered to be a series of video blocks, such as MBs and/or sub-blocks. Each slice may be an independently decodable unit of a video frame. Video encoder 22 and video decoder 28 perform inter-based predictive coding, which involves the generation of predictive reference data and the subtraction of a video block to be coded from the predictive reference data to generate residual data, which may then be transformed, quantized and entropy coded. The inter-based predictive coding may include interpolation of the predictive data in accordance with this disclosure.</p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
19[a] determining, for a current block, a first reference	Karczewicz II, as evidenced by the example citations below, discloses determining, for a current block, a first reference block based on a first motion vector and a second reference block based on a

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
<p>block based on a first motion vector and a second reference block based on a second motion vector, wherein the pixels of the current block, the first reference block, and the second reference block have values with a first precision;</p>	<p>second motion vector, wherein the pixels of the current block, the first reference block, and the second reference block have values with a first precision.</p> <p><i>See, e.g.,</i></p> <p><i>See limitation 1[a].</i></p> <p>Karczewicz II, ¶0006:</p> <p>A coded video block may be represented by prediction information that can be used to create or identify a predictive block, and a residual block of data indicative of differences between the block being coded and the predictive block. The prediction information may comprise the one or more motion vectors that are used to identify the predictive block of data. Given the motion vectors, the decoder is able to reconstruct the predictive blocks that were used to code the residual. Thus, given a set of residual blocks and a set of motion vectors (and possibly some additional syntax), the decoder may be able to reconstruct a video frame that was originally encoded. Inter-coding based on motion estimation and motion compensation can achieve very good compression because successive video frames or other types of coded units are often very similar. An encoded video sequence may comprise blocks of residual data, motion vectors, and possibly other types of syntax.</p> <p>Karczewicz II, ¶0063:</p> <p>FIG. 3 is a block diagram illustrating an example of a video decoder 60, which decodes a video sequence that is encoded in the manner described herein. Video decoder 60 includes a motion compensation unit 55 that performs the interpolation techniques of this disclosure for decoding. In particular, on the decoding side, motion compensation unit 55 may receive a syntax element from entropy decoding unit 52 that identifies an interpolation filter from a</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>plurality of different pre-defined interpolation filters. Motion compensation unit 55 may generate prediction data, which includes interpolating pixel values of reference video data based on the interpolation filter identified by the syntax element. Specifically, motion compensation unit 55 may generate the prediction data based on motion vectors received from entropy decoding unit 52 and the interpolations as defined by syntax element (labeled interpolation syntax in FIG. 3). Based on this interpolated prediction data, the video data (e.g., a reconstructed residual video block) can be decoded.</p> <p>Karczewicz II, Fig. 3:</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<div data-bbox="619 438 1585 1015"><pre>graph LR subgraph VIDEO_DECODER_60 [VIDEO DECODER 60] direction TB subgraph Row1 [] direction LR E52[ENTROPY DECODING UNIT 52] M55[MOTION COMPENSATION UNIT 55] end E52 -.-> INTERPOLATION SYNTAX M55 E52 -.-> MOTION VECTORS M55 E52 --> QUANTIZ. COEFF. I56[INVERSE QUANTIZ. 56] I56 --> I58[INVERSE TRANSFORM 58] I58 --> RESIDUAL Add((+)) M55 --> Add Add --> RFS[REFERENCE FRAME STORE 62] RFS --> DV[DECODED VIDEO] end</pre></div> <p>FIG. 3</p> <p>Karczewicz II, ¶0064:</p> <p>Entropy decoding unit 52 entropy decodes the received bitstream to generate quantized coefficients and the syntax (e.g., interpolation syntax and motion vectors sent to motion compensation unit 55). Inverse quantization unit 56 inverse quantizes, i.e., de-quantizes, the quantized block coefficients. The inverse quantization process may be a conventional process as defined by H.264 decoding. Inverse transform unit 58 applies an inverse transform, e.g., an inverse DCT or conceptually similar inverse transform process, to the transform coefficients in order to produce residual blocks in the pixel domain. Motion compensation unit 55</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>produces motion compensated blocks in the manner described herein, e.g., including interpolation based on a set of interpolation filter coefficients identified by the syntax element (i.e., the interpolation syntax).</p> <p>Karczewicz II, ¶0110:</p> <p>FIG. 6 is a flow diagram consistent with one or more aspects of video decoding consistent with this disclosure. FIG. 6 will be discussed from the perspective of video encoder 50 shown in FIG. 2. In particular, video encoder 60 receives an encoded video data (111), and receives a syntax element that identifies an interpolation filter from a plurality of interpolation filters (112). The encoded bitstream may include both the encoded residual video data and the syntax element that identifies the interpolation filter that was used at the encoder. Entropy decoding unit 52 may entropy decode the received bitstream to parse out the transformed and quantized residual blocks, which are sent to inverse transform quantization unit 56 and inverse transform unit 58, and to parse out the interpolation syntax element and motion vectors, which are sent to motion compensation unit 55.</p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
19[b] using said first reference block to obtain a first prediction, said first prediction having a second precision, which is higher than said first precision;	<p>Karczewicz II, as evidenced by the example citations below, discloses using the first reference block to obtain a first prediction, the first prediction having a second precision, which is higher than the first precision.</p> <p><i>See, e.g.,</i></p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p><i>See limitation 1[b].</i></p> <p>Karczewicz II, ¶0008:</p> <p>In general, this disclosure describes interpolation techniques performed by an encoder and a decoder during the motion compensation process of video coding. According to the techniques of this disclosure, the encoder may apply a plurality of pre-defined interpolation filters in order to generate a plurality of different interpolated prediction data. The interpolated prediction data that achieves the highest levels of compression may be selected, and the interpolation filter that was used may be coded as syntax and communicated to the decoding device as part of the coded video data. The interpolation filter may be defined once per predictive unit (e.g., once per prediction frame), or may be defined on a block basis (e.g., different filtering may be applied to different video blocks within a predictive unit). Alternatively, the interpolation filter may be defined on a sample basis, or may be defined on a sub-sample basis.</p> <p>Karczewicz II, ¶0009:</p> <p>The decoder receives the coded video data, and can interpret the syntax in order to identify the interpolation filter that was used by the encoder. In this way, the decoder can identify and use the same interpolation filters during its motion compensation processes that were used during the encoding process. By considering multiple different interpolation filters at the encoder, compression may be improved relative to techniques that have a fixed interpolation filter. At the same time, the techniques may be significantly less complex than conventional adaptive interpolation filtering, which adaptively defines the interpolation filter coefficients as part of the coding process. The different interpolation filters, according to some aspects of this disclosure, may be pre-defined by implementing adaptive interpolation filtering or other techniques on test video sequences in order to pre-define sets of interpolation filter</p>

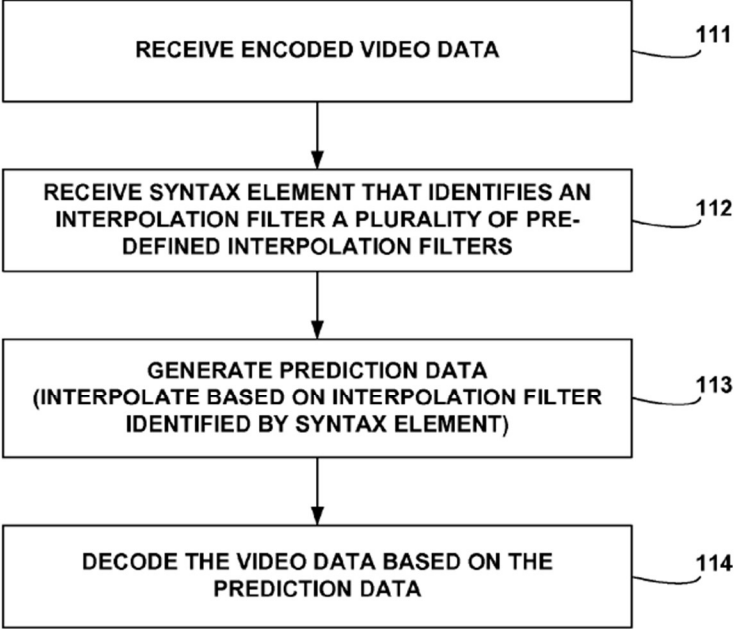
ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>coefficients that will likely result in good compression. Alternatively, different numbers of filter coefficients (different number of taps) or different types of filters altogether may be pre-defined, and then selected and used during the encoding and decoding processes.</p> <p>Karczewicz II, ¶0013:</p> <p>In another example, this disclosure provides a method of decoding video data. The decoding method comprises receiving a syntax element that identifies an interpolation filter from a plurality of different pre-defined interpolation filters, generating prediction data, wherein generating the prediction data includes interpolating pixel values of reference video data based on the interpolation filter identified by the syntax element, and decoding the video data based on the prediction data.</p> <p>Karczewicz II, ¶0016:</p> <p>In another example, this disclosure provides an apparatus that decodes video data, the apparatus comprising a video decoder that includes a motion compensation unit. The video decoder receives a syntax element that identifies an interpolation filter from a plurality of different pre-defined interpolation filters. The motion compensation unit generates prediction data, wherein generating the prediction data includes interpolating pixel values of reference video data based on the interpolation filter identified by the syntax element. The video decoder then decodes the video data based on the prediction data.</p> <p>Karczewicz II, ¶0041:</p> <p>In the example of FIG. 1, source device 12 may include a video source 20, video encoder 22, a modulator/demodulator (modem) 23 and a transmitter 24. Destination device 16 may include a receiver 26 a modem 27, a video decoder 28, and a display device 30. In accordance</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>with this disclosure, video encoder 22 of source device 12 may be configured to apply one or more of the interpolation techniques of this disclosure as part of a video encoding process. Similarly, video decoder 28 of destination device 16 may be configured to apply one or more of the interpolation techniques of this disclosure as part of a video decoding process.</p> <p>Karczewicz II, ¶0042:</p> <p>The illustrated system 10 of FIG. 1 is merely exemplary. The interpolation techniques of this disclosure may be performed by any encoding device that supports motion compensated interpolation to sub-pixel resolution. Source device 12 and destination device 16 are merely examples of such coding devices. In this case, source device 12 generates coded video data for transmission to destination device 16. Devices 12, 16 may operate in a substantially symmetrical manner such that each of devices 12, 16 include video encoding and decoding components, e.g., in a combined encoder-decoder (CODEC). Hence, system 10 may support one-way or two-way video transmission between video devices 12, 16, e.g., for video streaming, video playback, video broadcasting, or video telephony.</p> <p>Karczewicz II, ¶0046:</p> <p>Video encoder 22 and video decoder 28 may operate according to a video compression standard, such as the ITU-T H.264 standard, alternatively described as MPEG-4, Part 10, Advanced Video Coding (AVC). The techniques of this disclosure, however, are not limited to any particular coding standard. Although not shown in FIG. 1, in some aspects, video encoder 22 and video decoder 28 may each be integrated with an audio encoder and decoder, and may include appropriate MUX-DEMUX units, or other hardware and software, to handle encoding of both audio and video in a common data stream or separate data streams. If applicable, MUX-DEMUX units may conform to the ITU H.223 multiplexer protocol, or other protocols such as the user datagram protocol (UDP).</p>

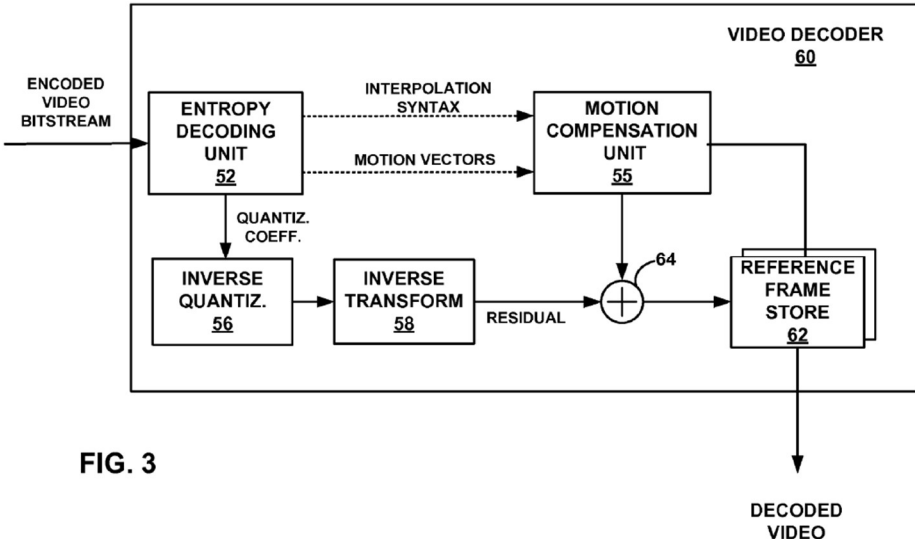
ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>Karczewicz II, ¶0063:</p> <p>Video encoder 22 and video decoder 28 may operate according to a video compression standard, such as the ITU-T H.264 standard, alternatively described as MPEG-4, Part 10, Advanced Video Coding (AVC). The techniques of this disclosure, however, are not limited to any particular coding standard. Although not shown in FIG. 1, in some aspects, video encoder 22 and video decoder 28 may each be integrated with an audio encoder and decoder, and may include appropriate MUX-DEMUX units, or other hardware and software, to handle encoding of both audio and video in a common data stream or separate data streams. If applicable, MUX-DEMUX units may conform to the ITU H.223 multiplexer protocol, or other protocols such as the user datagram protocol (UDP).</p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
<p>19[c] using said second reference block to obtain a second prediction, said second prediction having the second precision;</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses using the second reference block to obtain a second prediction, the second prediction having the second precision.</p> <p><i>See, e.g.,</i></p> <p><i>See limitation 1[c].</i></p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9.</i></p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
<p>19[d] obtaining a combined prediction based at least partly upon said first prediction and said second prediction;</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses obtaining a combined prediction based at least partly upon the first prediction and the second prediction.</p> <p><i>See, e.g.,</i></p> <p><i>See limitation 1[d].</i></p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
<p>19[e] decreasing a precision of said combined prediction by shifting bits of the combined prediction to the right; and</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses decreasing a precision of the combined prediction by shifting bits of the combined prediction to the right.</p> <p><i>See, e.g.,</i></p> <p><i>See limitation 1[e].</i></p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
<p>19[f] reconstructing the block of pixels based on the combined prediction.</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses reconstructing the block of pixels based on the combined prediction.</p> <p><i>See, e.g.,</i></p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>Karczewicz II, ¶0111:</p> <p>Motion compensation unit 55 generates prediction data (113), which includes interpolations based on the interpolation filter identified by the syntax element. Video decoder 60 decodes the video data based on the prediction data (114). In particular, the prediction data generated by motion compensation unit 55 may be combined with residual data via adder 64 to reconstruct the video data in the pixel domain. Decoded video may then be stored in reference frame store 62 for subsequent use in prediction, and may also be output for presentation to the user, e.g., via a display or other output device.</p> <p>Karczewicz II, Fig. 6:</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	 <pre> graph TD 111[RECEIVE ENCODED VIDEO DATA] --> 112[RECEIVE SYNTAX ELEMENT THAT IDENTIFIES AN INTERPOLATION FILTER A PLURALITY OF PRE-DEFINED INTERPOLATION FILTERS] 112 --> 113[GENERATE PREDICTION DATA (INTERPOLATE BASED ON INTERPOLATION FILTER IDENTIFIED BY SYNTAX ELEMENT)] 113 --> 114[DECODE THE VIDEO DATA BASED ON THE PREDICTION DATA] </pre> <p>FIG. 6</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>Karczewicz II, ¶0065:</p> <p>Summer 64 decodes residual blocks by summing the residual blocks with the corresponding prediction blocks generated by motion compensation unit 55 to form decoded blocks. If desired, a deblocking filter may also be applied to filter the decoded blocks in order to remove blockiness artifacts. The decoded video blocks are then stored in reference frame store 62, which is a storage element that provides reference blocks for subsequent motion compensation and also produces decoded video to a drive display device (such as device 30 of FIG. 1).</p> <p>Karczewicz II, Fig. 3:</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	 <p>The diagram illustrates a video decoder (60) architecture. An 'ENCODED VIDEO BITSTREAM' enters from the left into the 'ENTROPY DECODING UNIT 52'. This unit outputs 'QUANTIZ. COEFF.' to the 'INVERSE QUANTIZ. 56' block. The 'INVERSE QUANTIZ. 56' block outputs to the 'INVERSE TRANSFORM 58' block. The 'INVERSE TRANSFORM 58' block outputs a 'RESIDUAL' to a summation node (a circle with a plus sign). The 'ENTROPY DECODING UNIT 52' also outputs 'INTERPOLATION SYNTAX' and 'MOTION VECTORS' (indicated by dashed lines) to the 'MOTION COMPENSATION UNIT 55'. The 'MOTION COMPENSATION UNIT 55' outputs to the same summation node. The summation node is labeled with a '64' and its output goes to the 'REFERENCE FRAME STORE 62'. The 'REFERENCE FRAME STORE 62' also receives input from the 'MOTION COMPENSATION UNIT 55'. The output of the 'REFERENCE FRAME STORE 62' is the 'DECODED VIDEO'.</p> <p>FIG. 3</p> <p>Karczewicz II, ¶0006:</p> <p>A coded video block may be represented by prediction information that can be used to create or identify a predictive block, and a residual block of data indicative of differences between the block being coded and the predictive block. The prediction information may comprise the one or more motion vectors that are used to identify the predictive block of data. Given the motion vectors, the decoder is able to reconstruct the predictive blocks that were used to code the residual. Thus, given a set of residual blocks and a set of motion vectors (and possibly some additional syntax), the decoder may be able to reconstruct a video frame that was originally encoded. Inter-coding based on motion estimation and motion compensation can</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>achieve very good compression because successive video frames or other types of coded units are often very similar. An encoded video sequence may comprise blocks of residual data, motion vectors, and possibly other types of syntax.</p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
<p>20. The method according to claim 19, wherein in an instance in which said first motion vector points to a subpixel, said first prediction is obtained by interpolation using pixel values of said first reference block.</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses that in an instance in which the first motion vector points to a subpixel, the first prediction is obtained by interpolation using pixel values of the first reference block.</p> <p><i>See, e.g.,</i></p> <p><i>See claim 2.</i></p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
<p>21. The method according to claim 20, wherein said first prediction is obtained by interpolation using values of said first reference block by: right shifting a sum of a P-tap filter using values of said first</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses that the first prediction is obtained by interpolation using values of the first reference block by right shifting a sum of a P-tap filter using values of the first reference block.</p> <p><i>See, e.g.,</i></p> <p><i>See claim 3.</i></p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
reference block.	<p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
<p>22. The method according to claim 20, wherein in an instance in which said second motion vector points to an integer sample, said second prediction is obtained by shifting values of said second reference block to the left.</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses that in an instance in which the second motion vector points to an integer sample, the second prediction is obtained by shifting values of the second reference block to the left.</p> <p><i>See, e.g.,</i></p> <p><i>See claim 4.</i></p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
<p>23. The method according to claim 19, wherein said decreasing said precision of said combined prediction by shifting bits of the combined prediction to the right, further comprises: inserting a rounding offset to the combined prediction before said decreasing.</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses that the decreasing the precision of the combined prediction by shifting bits of the combined prediction to the right, further comprises inserting a rounding offset to the combined prediction before the decreasing.</p> <p><i>See, e.g.,</i></p> <p><i>See claim 5.</i></p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the</i></p>

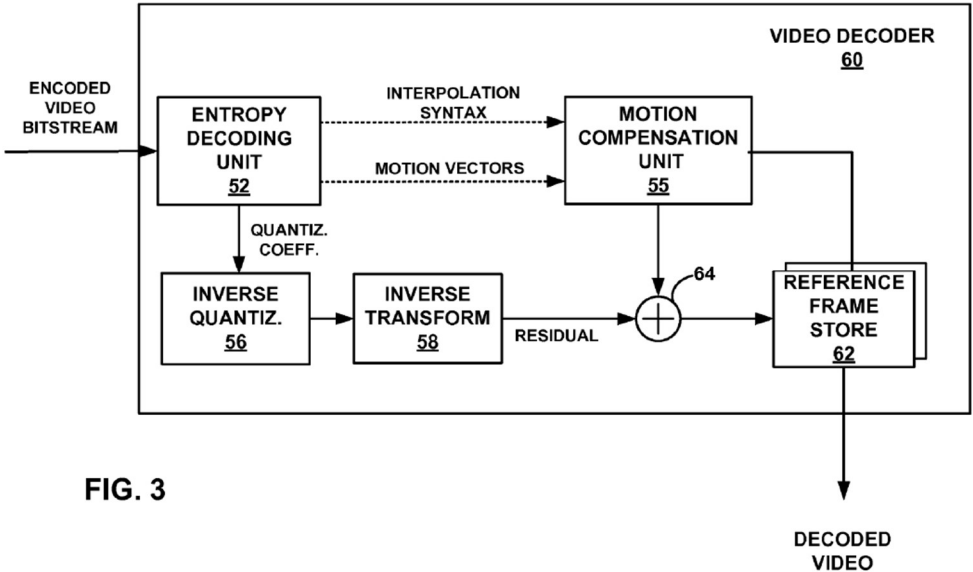
ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<i>references identified in Exhibit E-9</i>
<p>24. The method according to claim 19, wherein the first precision indicates a number of bits needed to represent the values of the pixels, and the second precision indicates the number of bits needed to represent values of said first prediction and values of said second prediction.</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses that the first precision indicates a number of bits needed to represent the values of the pixels, and the second precision indicates the number of bits needed to represent values of the first prediction and values of the second prediction.</p> <p><i>See, e.g.,</i></p> <p><i>See claim 6.</i></p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
<p>25[preamble] An apparatus for decoding a block of pixels, the apparatus comprising:</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses an apparatus for decoding a block of pixels.</p> <p><i>See, e.g.,</i></p> <p><i>See limitation 19[preamble].</i></p> <p>Karczewicz II, ¶0003:</p> <p>Digital video capabilities can be incorporated into a wide range of devices, including digital televisions, digital direct broadcast systems, wireless broadcast systems, personal digital assistants (PDAs), laptop or desktop computers, digital cameras, digital recording devices, video gaming devices, video game consoles, cellular or satellite radio telephones, and the</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>like. Digital video devices implement video compression techniques, such as MPEG-2, MPEG-4, or ITU-T H.264/MPEG-4, Part 10, Advanced Video Coding (AVC), to transmit and receive digital video information more efficiently. Video compression techniques may perform spatial prediction and/or temporal prediction to reduce or remove redundancy inherent in video sequences.</p> <p>Karczewicz II, ¶0016:</p> <p>In another example, this disclosure provides an apparatus that decodes video data, the apparatus comprising a video decoder that includes a motion compensation unit. The video decoder receives a syntax element that identifies an interpolation filter from a plurality of different pre-defined interpolation filters. The motion compensation unit generates prediction data, wherein generating the prediction data includes interpolating pixel values of reference video data based on the interpolation filter identified by the syntax element. The video decoder then decodes the video data based on the prediction data.</p> <p>Karczewicz II, ¶0017:</p> <p>In another example, this disclosure provides an apparatus that interpolates predictive video data for video coding, wherein the apparatus includes a motion compensation unit that generates half-pixel values based on integer pixel values, rounds the half-pixel values to generate half-pixel interpolated values, stores the half-pixel values as non-rounded versions of the half-pixel values, and generates quarter-pixel values based on the non-rounded versions of the half-pixel values and the integer pixel values.</p> <p>Karczewicz II, ¶0022:</p> <p>In another example, this disclosure provides a device that decodes video data, the device</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>comprising a wireless receiver that receives the video data, and a video decoder including a motion compensation unit that receives a syntax element that identifies an interpolation filter from a plurality of different pre-defined interpolation filters, generates prediction data, wherein generating the prediction data includes interpolating pixel values of reference video data based on the interpolation filter identified by the syntax element, and decodes the video data based on the prediction data.</p> <p>Karczewicz II, ¶0024:</p> <p>In another example, this disclosure provides a device that decodes video data, the device comprising a wireless receiver that receives the video data, and a video decoder including a motion compensation unit that decodes the video data. In decoding the video data, the motion compensation unit generates half-pixel values based on integer pixel values, rounds the half-pixel values to generate half-pixel interpolated values, stores the half-pixel values as non-rounded versions of the half-pixel values, and generates quarter-pixel values based on the non-rounded versions of the half-pixel values and the integer pixel values.</p> <p>Karczewicz II, ¶0025:</p> <p>The techniques described in this disclosure may be implemented in hardware, software, firmware, or any combination thereof. If implemented in software, the software may be executed in one or more processors, such as a microprocessor, application specific integrated circuit (ASIC), field programmable gate array (FPGA), or digital signal processor (DSP). The software that executes the techniques may be initially stored in a computer-readable medium and loaded and executed in the processor.</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>Karczewicz II, ¶0026:</p> <p>Accordingly, this disclosure also contemplates a computer-readable storage medium comprising instructions that upon execution cause a device to encode video data, wherein the instructions cause the device to generate prediction data, wherein generating the prediction data includes interpolating pixel values of reference video data based on a plurality of different pre-defined interpolation filters, and encode the video data based on the prediction data.</p> <p>Karczewicz II, ¶0048:</p> <p>Video encoder 22 and video decoder 28 each may be implemented as one or more microprocessors, digital signal processors (DSPs), application specific integrated circuits (ASICs), field programmable gate arrays (FPGAs), discrete logic, software, hardware, firmware or any combinations thereof. Each of video encoder 22 and video decoder 28 may be included in one or more encoders or decoders, either of which may be integrated as part of a combined CODEC in a respective mobile device, subscriber device, broadcast device, server, or the like.</p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
25[a] at least one processor and at least one memory including computer program code, the at least one memory	Karczewicz II, as evidenced by the example citations below, discloses that at least one processor and at least one memory including computer program code, the at least one memory and computer program code.

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
<p>and computer program code configured to, with the at least one processor, cause the apparatus to:</p>	<p><i>See, e.g.,</i></p> <p><i>See limitation 7[a].</i></p> <p>Karczewicz II, ¶0006:</p> <p>A coded video block may be represented by prediction information that can be used to create or identify a predictive block, and a residual block of data indicative of differences between the block being coded and the predictive block. The prediction information may comprise the one or more motion vectors that are used to identify the predictive block of data. Given the motion vectors, the decoder is able to reconstruct the predictive blocks that were used to code the residual. Thus, given a set of residual blocks and a set of motion vectors (and possibly some additional syntax), the decoder may be able to reconstruct a video frame that was originally encoded. Inter-coding based on motion estimation and motion compensation can achieve very good compression because successive video frames or other types of coded units are often very similar. An encoded video sequence may comprise blocks of residual data, motion vectors, and possibly other types of syntax.</p> <p>Karczewicz II, ¶0063:</p> <p>FIG. 3 is a block diagram illustrating an example of a video decoder 60, which decodes a video sequence that is encoded in the manner described herein. Video decoder 60 includes a motion compensation unit 55 that performs the interpolation techniques of this disclosure for decoding. In particular, on the decoding side, motion compensation unit 55 may receive a syntax element from entropy decoding unit 52 that identifies an interpolation filter from a plurality of different pre-defined interpolation filters. Motion compensation unit 55 may generate prediction data, which includes interpolating pixel values of reference video data based on the interpolation filter identified by the syntax element. Specifically, motion</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>compensation unit 55 may generate the prediction data based on motion vectors received from entropy decoding unit 52 and the interpolations as defined by syntax element (labeled interpolation syntax in FIG. 3). Based on this interpolated prediction data, the video data (e.g., a reconstructed residual video block) can be decoded.</p> <p>Karczewicz II, Fig. 3:</p>  <p>The diagram illustrates a video decoder (60) architecture. An 'ENCODED VIDEO BITSTREAM' enters from the left into the 'ENTROPY DECODING UNIT 52'. This unit outputs 'QUANTIZ. COEFF.' to the 'INVERSE QUANTIZ. 56' block. The 'INVERSE QUANTIZ. 56' block outputs to the 'INVERSE TRANSFORM 58' block. The 'INVERSE TRANSFORM 58' block outputs a 'RESIDUAL' to a summation node (a circle with a plus sign). The 'MOTION COMPENSATION UNIT 55' also receives input from the 'ENTROPY DECODING UNIT 52' via 'INTERPOLATION SYNTAX' and 'MOTION VECTORS' (indicated by dashed lines). The 'MOTION COMPENSATION UNIT 55' outputs to the same summation node. The summation node is labeled with a '64' and its output goes to the 'REFERENCE FRAME STORE 62'. The 'REFERENCE FRAME STORE 62' outputs to the 'DECODED VIDEO' at the bottom. The entire process is enclosed in a box labeled 'VIDEO DECODER 60'.</p> <p>FIG. 3</p> <p>Karczewicz II, ¶0064:</p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<p>Entropy decoding unit 52 entropy decodes the received bitstream to generate quantized coefficients and the syntax (e.g., interpolation syntax and motion vectors sent to motion compensation unit 55). Inverse quantization unit 56 inverse quantizes, i.e., de-quantizes, the quantized block coefficients. The inverse quantization process may be a conventional process as defined by H.264 decoding. Inverse transform unit 58 applies an inverse transform, e.g., an inverse DCT or conceptually similar inverse transform process, to the transform coefficients in order to produce residual blocks in the pixel domain. Motion compensation unit 55 produces motion compensated blocks in the manner described herein, e.g., including interpolation based on a set of interpolation filter coefficients identified by the syntax element (i.e., the interpolation syntax).</p> <p>Karczewicz II, ¶0110:</p> <p>FIG. 6 is a flow diagram consistent with one or more aspects of video decoding consistent with this disclosure. FIG. 6 will be discussed from the perspective of video encoder 50 shown in FIG. 2. In particular, video encoder 60 receives an encoded video data (111), and receives a syntax element that identifies an interpolation filter from a plurality of interpolation filters (112). The encoded bitstream may include both the encoded residual video data and the syntax element that identifies the interpolation filter that was used at the encoder. Entropy decoding unit 52 may entropy decode the received bitstream to parse out the transformed and quantized residual blocks, which are sent to inverse transform quantization unit 56 and inverse transform unit 58, and to parse out the interpolation syntax element and motion vectors, which are sent to motion compensation unit 55.</p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the</i></p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
	<i>references identified in Exhibit E-9</i>
25[b] determine, for a current block, a first reference block based on a first motion vector and a second reference block based on a second motion vector, wherein the pixels of the current block, the first reference block, and the second reference block have values with a first precision;	<p>Karczewicz II, as evidenced by the example citations below, discloses determining, for a current block, a first reference block based on a first motion vector and a second reference block based on a second motion vector, wherein the pixels of the current block, the first reference block, and the second reference block have values with a first precision.</p> <p><i>See, e.g.,</i></p> <p><i>See limitation 19[a].</i></p>
25[c] use said first reference block to obtain a first prediction, said first prediction having a second precision, which is higher than said first precision;	<p>Karczewicz II, as evidenced by the example citations below, discloses using the first reference block to obtain a first prediction, the first prediction having a second precision, which is higher than the first precision.</p> <p><i>See, e.g.,</i></p> <p><i>See limitation 19[b].</i></p>
25[d] use said second reference block to obtain a second prediction, said second prediction having the second precision;	<p>Karczewicz II, as evidenced by the example citations below, discloses using the second reference block to obtain a second prediction, the second prediction having the second precision.</p> <p><i>See, e.g.,</i></p> <p><i>See limitation 19[c].</i></p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
25[e] obtain a combined prediction based at least partly upon said first prediction and said second prediction;	Karczewicz II, as evidenced by the example citations below, discloses obtaining a combined prediction based at least partly upon the first prediction and the second prediction. <i>See, e.g.,</i> <i>See limitation 19[d].</i>
25[f] decrease a precision of said combined prediction by shifting bits of the combined prediction to the right; and	Karczewicz II, as evidenced by the example citations below, discloses decreasing a precision of the combined prediction by shifting bits of the combined prediction to the right. <i>See, e.g.,</i> <i>See limitation 19[e].</i>
25[g] reconstruct the block of pixels based on the combined prediction.	Karczewicz II, as evidenced by the example citations below, discloses reconstructing the block of pixels based on the combined prediction. <i>See, e.g.,</i> <i>See limitation 19[f].</i>
26. The apparatus according to claim 25, wherein in an instance in which said first motion vector points to a subpixel, said first prediction is obtained by interpolation	Karczewicz II, as evidenced by the example citations below, discloses that in an instance in which the first motion vector points to a subpixel, the first prediction is obtained by interpolation using pixel values of the first reference block. <i>See, e.g.,</i>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
using pixel values of said first reference block.	<i>See claim 20.</i>
27. The apparatus according to claim 26, wherein said first prediction is obtained by interpolation using values of said first reference block by: right shifting a sum of a P-tap filter using values of said first reference block.	Karczewicz II, as evidenced by the example citations below, discloses that the first prediction is obtained by interpolation using values of the first reference block by right shifting a sum of a P-tap filter using values of the first reference block. <i>See, e.g.,</i> <i>See claim 21.</i>
28. The apparatus according to claim 26, wherein in an instance in which said second motion vector points to an integer sample, said second prediction is obtained by shifting values of said second reference block to the left.	Karczewicz II, as evidenced by the example citations below, discloses that in an instance in which the second motion vector points to an integer sample, the second prediction is obtained by shifting values of the second reference block to the left. <i>See, e.g.,</i> <i>See claim 22.</i>
29. The apparatus according to claim 25, wherein the at least one memory and computer code are configured to cause the apparatus to decrease said precision of said combined prediction by	Karczewicz II, as evidenced by the example citations below, discloses that the at least one memory and computer code are configured to cause the apparatus to decrease the precision of the combined prediction by shifting bits of the combined prediction to the right, by inserting a rounding offset to the combined prediction before the decreasing. <i>See, e.g.,</i>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
<p>shifting bits of the combined prediction to the right, by: inserting a rounding offset to the combined prediction before said decreasing.</p>	<p><i>See claim 23.</i></p> <p><i>Further, to the extent Complainant contends this element is not expressly or inherently disclosed by Karczewicz II, it would have been obvious in view of the disclosures in Karczewicz II, in combination with the knowledge of a person of ordinary skill in the art, or in light of one or more of any of the references identified in Exhibit E-9</i></p>
<p>30. The apparatus according to claim 25, wherein the first precision indicates a number of bits needed to represent the values of the pixels, and the second precision indicates the number of bits needed to represent values of said first prediction and values of said second prediction.</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses that the first precision indicates a number of bits needed to represent the values of the pixels, and the second precision indicates the number of bits needed to represent values of the first prediction and values of the second prediction.</p> <p><i>See, e.g.,</i></p> <p><i>See claim 24.</i></p>
<p>31[preamble] A computer program product for decoding a block of pixels, the computer program product comprising at least one non-transitory computer readable storage medium having computer executable program code portions stored therein, the computer executable program code portions</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses a computer program product for decoding a block of pixels, the computer program product comprising at least one non-transitory computer readable storage medium having computer executable program code portions stored therein.</p> <p><i>See, e.g.,</i></p> <p><i>See limitations 19[preamble], 25[preamble], and 25[a].</i></p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
<p>comprising program code instructions configured to:</p>	<p>Karczewicz II, ¶0028:</p> <p>In addition, this disclosure also contemplates a computer-readable storage medium comprising instructions that upon execution cause a device to interpolate predictive video data for video coding, wherein the instructions cause the device to generate half-pixel values based on integer pixel values, round the half-pixel values to generate half-pixel interpolated values, store the half-pixel values as non-rounded versions of the half-pixel values, and generate quarter-pixel values based on the non-rounded versions of the half-pixel values and the integer pixel values.</p> <p>Karczewicz II, ¶0027:</p> <p>This disclosure also contemplates a computer-readable storage medium comprising instructions that upon execution cause a device to decode video data, wherein the instructions cause the device to, upon receiving a syntax element that identifies an interpolation filter from a plurality of different pre-defined interpolation filters, generate prediction data, wherein generating the prediction data includes interpolating pixel values of reference video data based on the interpolation filter identified by the syntax element, and decode the video data based on the prediction data.</p>
<p>31[a] determine, for a current block, a first reference block based on a first motion vector and a second reference block based on a second motion vector, wherein the pixels of the current block, the first reference block, and the</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses determining, for a current block, a first reference block based on a first motion vector and a second reference block based on a second motion vector, wherein the pixels of the current block, the first reference block, and the second reference block have values with a first precision.</p> <p><i>See, e.g.,</i></p>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
second reference block have values with a first precision;	<i>See limitation 19[a].</i>
31[b] use said first reference block to obtain a first prediction, said first prediction having a second precision, which is higher than said first precision;	Karczewicz II, as evidenced by the example citations below, discloses using the first reference block to obtain a first prediction, the first prediction having a second precision, which is higher than the first precision. <i>See, e.g.,</i> <i>See limitation 19[b].</i>
31[c] use said second reference block to obtain a second prediction, said second prediction having the second precision;	Karczewicz II, as evidenced by the example citations below, discloses using the second reference block to obtain a second prediction, the second prediction having the second precision. <i>See, e.g.,</i> <i>See limitation 19[c].</i>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
31[d] obtain a combined prediction based at least partly upon said first prediction and said second prediction;	Karczewicz II, as evidenced by the example citations below, discloses obtaining a combined prediction based at least partly upon the first prediction and the second prediction. <i>See, e.g.,</i> <i>See limitation 19[d].</i>
31[e] decrease a precision of said combined prediction by shifting bits of the combined prediction to the right; and	Karczewicz II, as evidenced by the example citations below, discloses decreasing a precision of the combined prediction by shifting bits of the combined prediction to the right. <i>See, e.g.,</i> <i>See limitation 19[e].</i>
31[f] reconstruct the block of pixels based on the combined prediction.	Karczewicz II, as evidenced by the example citations below, discloses reconstructing the block of pixels based on the combined prediction. <i>See, e.g.,</i> <i>See limitation 19[f].</i>
32. The computer program product according to claim 31, wherein in an instance in which said first motion vector points to a subpixel, said first prediction is obtained by	Karczewicz II, as evidenced by the example citations below, discloses that in an instance in which the first motion vector points to a subpixel, the first prediction is obtained by interpolation using pixel values of the first reference block. <i>See, e.g.,</i>

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
interpolation using pixel values of said first reference block.	<i>See claim 20.</i>
33. The computer program product according to claim 32, wherein said first prediction is obtained by interpolation using values of said first reference block by: right shifting a sum of a P-tap filter using values of said first reference block.	Karczewicz II, as evidenced by the example citations below, discloses that the first prediction is obtained by interpolation using values of the first reference block by right shifting a sum of a P-tap filter using values of the first reference block. <i>See, e.g.,</i> <i>See claim 21.</i>
34. The computer program product according to claim 32, wherein in an instance in which said second motion vector points to an integer sample, said second prediction is obtained by shifting values of said second reference block to the left.	Karczewicz II, as evidenced by the example citations below, discloses that in an instance in which the second motion vector points to an integer sample, the second prediction is obtained by shifting values of the second reference block to the left. <i>See, e.g.,</i> <i>See claim 22.</i>
35. The computer program product according to claim 31, wherein the program code instructions configured to decrease said precision of said combined prediction by	Karczewicz II, as evidenced by the example citations below, discloses that the program code instructions configured to decrease the precision of the combined prediction by shifting bits of the combined prediction to the right, further comprise program code instructions configured to insert a rounding offset to the combined prediction before the decreasing.

ASSERTED CLAIMS	PRIOR ART REFERENCE: KARCZEWICZ II
<p>shifting bits of the combined prediction to the right, further comprise program code instructions configured to: insert a rounding offset to the combined prediction before said decreasing.</p>	<p><i>See, e.g.,</i></p> <p><i>See claims 23 and 29.</i></p>
<p>36. The computer program product according to claim 31, wherein the first precision indicates a number of bits needed to represent the values of the pixels, and the second precision indicates the number of bits needed to represent values of said first prediction and values of said second prediction.</p>	<p>Karczewicz II, as evidenced by the example citations below, discloses that the first precision indicates a number of bits needed to represent the values of the pixels, and the second precision indicates the number of bits needed to represent values of the first prediction and values of the second prediction.</p> <p><i>See, e.g.,</i></p> <p><i>See claim 24.</i></p>