

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

CROWDSTRIKE, INC.,
Petitioner

v.

SKYSONG INNOVATIONS, LLC,
Patent Owner

Inter Partes Review Case No. IPR2025-01399
U.S. Patent No. 11,775,831

PETITIONER'S UPDATED EXHIBIT LIST

APPENDIX OF EXHIBITS

Exhibit 1001	U.S. Patent No. 11,775,831 (“the ’831 Patent”)
Exhibit 1002	File History of the ’831 Patent
Exhibit 1003	Declaration of Eli Saber, Ph.D. (“Dec”)
Exhibit 1004	Takayuki Ujiie <i>et al.</i> , <i>Approximated Prediction Strategy for Reducing Power Consumption of Convolutional Neural Network Processor</i> , 2016 IEEE Conference on Computer Vision and Pattern Recognition Workshops (2016) (“Ujiie”)
Exhibit 1005	Bert Moons <i>et al.</i> , <i>Energy-Efficient ConvNets Through Approximate Computing</i> , IEEE Winter Conference on Applications of Computer Vision (2016) (“Moons”)
Exhibit 1006	Himanshu Kaul <i>et al.</i> , <i>A 21.5M-Query-Vectors/s 3.37nJ/Vector Reconfigurable k-Nearest-Neighbor Accelerator with Adaptive Precision in 14nm Tri-Gate CMOS</i> , 2016 IEEE International Solid-State Circuits Conference (2016) (“Kaul”)
Exhibit 1007	U.S. Patent Application Publication No. 2016/0259995 A1 to Ishii <i>et al.</i> (“Ishii”)
Exhibit 1008	U.S. Patent Application Publication No. 2016/0259994 A1 to Ravindran <i>et al.</i> (“Ravindran”)
Exhibit 1009	Declaration of June Munford (“Munford Dec.”)
Exhibit 1010	Declaration of Gordon MacPherson (“MacPherson Dec.”)
Exhibit 1011	Matthieu Courbariaux <i>et al.</i> , <i>Binarized Neural Networks: Training Neural Networks with Weights and Activations Constrained to +1 or -1</i> , arXiv: 1602.02830v3 [cs.LG] (March 17, 2016) (“Courbariaux”)
Exhibit 1012	Minkyu Kim <i>et al.</i> , <i>An Energy-Efficient Deep Convolutional Neural Network Accelerator Featuring Conditional Computing and Low External Memory Access</i> , IEEE Journal of Solid-State Circuits, Vol. 56, No. 3 (2020) (“Kim”)
Exhibit 1013	Yann LeCun <i>et al.</i> , <i>Gradient-Based Learning Applied to Document Recognition</i> , Proceedings of the IEEE (1998) (“LeCun I”)
Exhibit 1014	Jiuxiang Gu <i>et al.</i> , <i>Recent Advances in Convolutional Neural Networks</i> , arXiv: 1512.07108v1 [cs.CV] (December 22, 2015) (“Gu”)
Exhibit 1015	Dominik Scherer <i>et al.</i> , <i>Evaluation of Pooling Operations in Convolutional Architectures for Object Recognition</i> , 20 th

	International Conference on Artificial Neural Networks (ICANN), Thessaloniki, Greece (2010) (“ <i>Scherer</i> ”)
Exhibit 1016	U.S. Patent No. 9,886,948 to Garimella <i>et al.</i> (“ <i>Garimella</i> ”)
Exhibit 1017	Dingjun Yu <i>et al.</i> , <i>Mixed Pooling for Convolutional Neural Networks</i> , The 9 th International Conference on Rough Sets and Knowledge Technology (2014) (“ <i>Yu</i> ”)
Exhibit 1018	Alex Krizhevsky <i>et al.</i> , <i>ImageNet Classification with Deep Convolutional Neural Networks</i> , Advances in Neural Information Processing Systems (2012) (“ <i>Krizhevsky</i> ”)
Exhibit 1019	Thomas L. Floyd, <i>Digital Fundamentals</i> , 11 th ed., Pearson Education Ltd. 2015, excerpts (“ <i>Floyd</i> ”)
Exhibit 1020	<i>Intentionally left blank</i>
Exhibit 1021	Yoshua Bengio <i>et al.</i> , <i>Estimating or Propagating Gradients Through Stochastic Neurons for Conditional Computation</i> , arXiv: 1308.3432v1 [cs.LG] (August 15, 2013) (“ <i>Bengio 1</i> ”)
Exhibit 1022	Emmanuel Bengio <i>et al.</i> , <i>Conditional Computation in Neural Networks for Faster Models</i> , arXiv: 1511.06297v1 [cs.LG] (November 19, 2015) (“ <i>Bengio 2</i> ”)
Exhibit 1023	Vincent Vanhoucke <i>et al.</i> , <i>Improving the speed of neural networks on CPUs</i> , Deep Learning and Unsupervised Feature Learning Workshop, NIPS (2011) (“ <i>Vanhoucke</i> ”)
Exhibit 1024	Patrick Judd <i>et al.</i> , <i>Reduced-Precision Strategies for Bounded Memory in Deep Neural Nets</i> , International Conference on Learning Representations 2016 (2016) (“ <i>Judd</i> ”)
Exhibit 1025	Microsoft Computer Dictionary, 3 rd ed., 1997, excerpts (“ <i>Computer Dictionary</i> ”)
Exhibit 1026	Song Han <i>et al.</i> , <i>EIE: Efficient Inference Engine on Compressed Deep Neural Network</i> , arXiv:1602.01528v2 [cs.CV] (May 3, 2016) (“ <i>Han</i> ”)
Exhibit 1027	Vinay K. Chippa <i>et al.</i> , <i>Analysis and Characterization of Inherent Application Resilience for Approximate Computing</i> , DAC ’13: Proceedings of the 50 th Annual Design Automation Conference (2013) (“ <i>Chippa</i> ”)
Exhibit 1028	Pakorn Watanachaturaporn <i>et al.</i> , <i>Hyperspectral Image Classification Using Support Vector Machines: A Comparison with Decision Tree and Neural Network Classifiers</i> , ASPRS 2005 Annual Conference (2005) (“ <i>Watanachaturaporn</i> ”)

Exhibit 1029	Wei Wen <i>et al.</i> , <i>An EDA Framework for Large Scale Hybrid Neuromorphic Computing Systems</i> , DAC '15: Proceedings of the 52 nd Annual Design Automation Conference (2015) (“Wen”)
Exhibit 1030	<i>Intentionally left blank</i>
Exhibit 1031	Yann LeCun <i>et al.</i> , <i>Deep learning</i> , Nature Vol. 521 (May 28, 2015) (“LeCun 2”)
Exhibit 1032	Ahmed AK Tahir, <i>Integrating artificial neural network and classical methods for unsupervised classification of optical remote sensing data</i> , EURASIP Journal on Advances in Signal Processing (2012) (“Tahir”)
Exhibit 1033	Petitioner CrowdStrike, Inc.’s Notice of Sotera Plus Stipulation

Respectfully submitted,

By: /s/ Paul R. Hart
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COUNSEL FOR PETITIONER

**CERTIFICATE OF SERVICE ON PATENT OWNER
UNDER 37 C.F.R. § 42.105**

Pursuant to 37 C.F.R. §§ 42.6(e) and 42.105, the undersigned certifies that on November 3, 2025, a true and correct copy of the foregoing Petitioner's Updated Exhibit List and EX1033 was served electronically on counsel for Patent Owner at the following address:

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