

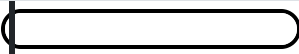
Bots

Examiner Hoang Huan

Employment Information

Art Unit:	2827 – Static information storage and retrieval
Group:	2810-2820 – Semiconductors A/Memory
Classes:	365 – Static information storage and retrieval 711 – Electrical computers and digital processing systems: memory 257 – Active solid-state devices (e.g., transistors, solid-state diodes) 702 – Data processing: measuring, calibrating, or testing 706 – Data processing: artificial intelligence 714 – Error detection/correction and fault detection/recovery 700 – Data processing: generic control systems or specific applications 370 – Multiplex communications 438 – Semiconductor device manufacturing: process
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Location:	VA 22314
Title:	Pat Examnr Elctrl Engrg
Service:	33 years
Grade:	GS-GS-14

Grant Rate and Difficulty Ranking

3-Year Grant rate:	96% over 523 cases
Difficulty: ?	Extremely Easy
Difficulty Percentile: ?	4th 

With Examiner Hoang, you have a 96% chance of getting an issued patent by 3 years after the first office action. Examiner Hoang is an extremely easy examiner and in the 4th percentile across all examiners (with 100th percentile most difficult).

Grant Rate

Grant Rate Timeline

Below is the grant rate timeline for Examiner Hoang, where the timeline is relative to the date of the first office action. The three-year grant rate is the percentage of applications granted at three years after the first office action.



Comparison with Art Unit 2827

Examiner Hoang's grant rate is higher than that of Art Unit 2827 and higher than that of the USPTO.



Average Office Actions Per Grant	
Examiner Hoang	1.0
Art Unit 2827	0.9

Interview Benefit

Grant Rate without Interview

Examiner Hoang has granted 443 of 461 cases without any applicant-requested interviews for a grant rate of 96%.



Grant Rate with Interview

Examiner Hoang has granted 60 of 62 cases with at least one applicant-requested interview for a grant rate of 97%.

Interview Benefit

With an interview, your chance of getting a patent is significantly higher than without an interview.



Recent Dispositions



Number	Title	OA Rejections	Status	IFW 
18642283	Dynamic Sensing Levels For Nonvolatile Memory Devices	Rejection information available with a Premium Stats subscription. See our pricing.	Patented	View
18509318	Memory Physical Layer Interface, Memory Apparatus And Method Thereof		Patented	View
18494258	Memory Package Performing Training Operation Using Address-Delay Mapping And Memory System Including The Same		Patented	View
18416777	Row Clear Features For Memory Devices And Associated Methods And Systems		Patented	View
18228447	Semiconductor Element Memory Cell And Semiconductor Element Memory Device		Patented	View

Number	Title	OA Rejections	Status	IFW ?
18219439	Ferroelectric Memory Reference Generation		Patented	View
18199114	Semiconductor Memory Device Capable Of Adaptively Controlling Bias And Method Of Operating The Same		Patented	View
18763048	Switches To Reduce Routing Rails Of Memory System		Patented	View
18136491	In-Memory Computation Circuit Using Static Random Access Memory (Sram) Array Segmentation		Patented	View
18461914	Memory Device Using Semiconductor Element		Patented	View
17961116	Nonvolatile Resistive Memory Device Using Dynamic Reference In Dual Domain And Read Method Thereof		Patented	View
17993543	Time-Division Multiplexing For Reading Superconducting Memory		Patented	View
18654443	Memory Device For Supporting New Command Input Scheme And Method Of Operating The Same		Patented	View
18370404	Anti-Fuse Memory Device		Patented	View
18520401	Sot Mram Including Mtj And Selector Located On Opposite Sides Of Sot Layer And Method Of Making The Same		Patented	View

Number	Title	OA Rejections	Status	IFW ?
18330768	Power Source Switching Circuit For Memory Device		Patented	View
18333592	Tracking Circuitry And Memory Devices Including The Same		Patented	View
18514707	Acceleration Of Data Queries In Memory		Patented	View
18639690	Pre-Decoder Circuitry		Patented	View
18602521	Using Embedded Switches For Reducing Capacitive Loading On A Memory System		Patented	View
18500394	Sensing Amplifier Circuit And Memory Device		Patented	View
18361306	Transimpedance Amplifiers For Crossbar Circuits		Patented	View
18097710	Content Addressable Memory With Multi-Iteration Lookup		Patented	View
18196152	Static Random Access Memory Supporting A Single Clock Cycle Read-Modify-Write Operation With A Modulated Word Line Assertion		Patented	View
18298735	Aging Monitoring Circuit Of Semiconductor Memory Device		Patented	View
18631706	Vertical Memory Devices And Methods For Operating The Same		Patented	View
18132472	Data Storage Device For Checking A Defect Of Row Lines And An		Patented	View

Number	Title	OA Rejections	Status	IFW ?
	Operation Method Thereof			
18438174	Magnetic Memory Using Spin-Orbit Torque		Patented	View
17896060	Phase-Change Memory Device And Method For Fabricating The Same		Patented	View
17694649	Memory Package Having Stacked Array Dies And Reduced Driver Load		Patented	View
18149676	Memory Device And Test Method Thereof		Abandoned	View
18530185	Unified Micro System With Memory Integrated Circuit And Logic Integrated Circuit		Patented	View
18178017	Semiconductor Memory Device And Method Of Reading A Semiconductor Memory Device		Patented	View
18439982	System For Differentiated Thermal Throttling Of Memory And Method Of Operating Same		Patented	View
17936823	Word Line Driver And Memory Device		Patented	View
17960411	Memory Device Using Semiconductor Device		Patented	View
18299714	Semiconductor Device Capable Of Checking Deterioration Of Select Transistor And Operating Method Thereof		Patented	View

Number	Title	OA Rejections	Status	IFW ?
17965592	Extended Address Interface Activate Sequence Using Mode Register Write		Patented	View
18093771	Static Random Access Memory Circuit And Read/write Operation Method Thereof		Patented	View
18156133	Control Method, Semiconductor Memory, And Electronic Device		Patented	View
17955555	Memory Device And Management Method Thereof		Patented	View
18117268	Faster Multi-Cell Read Operation Using Reverse Read Calibrations		Patented	View
17757321	Data Transmission Circuit, Data Transmission Method And Storage Device		Patented	View
17994061	Memory Device And Method Of Operating The Same		Patented	View
18522564	Memory Circuit And Method Of Operating Same		Patented	View
17858406	Memory Circuit And Memory		Patented	View
17867687	Local Sensing Amplifier And Memory		Patented	View
17901459	Memory Device		Patented	View
18502580	Non-Volatile Memory Device Including Sense Amplifier And Method For Operating The Same		Patented	View

Number	Title	OA Rejections	Status	IFW ?
17907276	Semiconductor Storage Apparatus		Patented	View

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