SEMICONDUCTOR PROCESS PORTFOLIO

MOSAID's Semiconductor Process Portfolio includes core technologies applicable to a wide range of manufacturing methods and physical structures of semiconductor memory and processor products.

Memory Process Patents

MOSAID's Memory Process patents were selected and acquired from the world's top semiconductor memory manufacturers, and apply to state-of-the-art DRAM and Flash memory devices. In addition, our portfolio also includes patents applicable to CMOS image sensor products, covering a wide range of current pixel array technologies for both Front Side and Back Side illuminated CMOS image sensors.

Logic Process Patents

MOSAID's Logic Process patent portfolio includes patents related to the manufacture of semiconductor processors that are ubiquitous at the heart of all digital products including consumer products such as personal computers and smartphones.

Important technologies covered by our patents are Front End-Of-Line (FEOL) processes and structures particularly related to transistor devices; and Back End-Of-Line (BEOL) processes related to metal interconnections, dielectric isolation layers and chip passivation. A significant portion of the Logic portfolio deals with advanced processor technologies such as High-K Metal Gate (HKMG), 3D transistors or FinFET, as well as advanced metallization processes.

Packaging Patents

Complementing our Memory and Logic portfolios, our Packaging portfolio comprises patents related to the final stage of fabricating an integrated circuit component, in which the semiconductor chip is encased in a protective package.

MOSAID's Packaging patents cover various types of widely used packaging technologies, including Ball Grid Array (BGA), flip-chip, and System-in-Package (SiP). The patents also cover emerging packaging technologies such as 3D IC, in which two or more electronic components are integrated both vertically and horizontally into a single package.

PATENT CATEGORIES

Memory Portfolio (https://mosaid.com/patent-category/memory-portfolio/)

Semiconductor Process Portfolio

Microcomponents Portfolio (https://mosaid.com/patent-category/microcomponents-portfolio/)

Conversant Wireless (https://mosaid.com/patent-category/conversant-wireless/)

U.S.
PATENT OFFICE

All MOSAID patents are available for review at the U.S. Patent Office (http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO2&Sect2=HITOFF&p=1&u=%2Fnetahtml%2FPTO%2Fsearch-bool.html&r=0&f=S&l=50&TERM1=MOSAID&FIELD1=ASNM&co1=OR&TERM2=%22Core+Wirel

Alternatively, visit Google Patents (https://www.google.com/?tbm=pts#q=%22MOSAID%22+OR+%22Core+Wireless%22&tbm=pts)

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