

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

MICRON TECHNOLOGY, INC., and
MICRON SEMICONDUCTOR PRODUCTS, INC.,
Petitioner

v.

PALISADE TECHNOLOGIES, LLP,
Patent Owner.

Case No. IPR2025-01561
U.S. Patent No. 8,148,962

PETITIONER'S UPDATED LIST OF EXHIBITS

LIST OF EXHIBITS

Ex-1001	U.S. Patent No. 8,148,962 (“the ’962 Patent”)
Ex-1002	Declaration of Dr. R. Jacob Baker
Ex-1003	Curriculum Vitae of Dr. R. Jacob Baker
Ex-1004	Prosecution History of U.S. Patent No. 8,148,962 (App. No. 12/464,301)
Ex-1005	U.S. Patent No. 7,151,363 (“Scott”)
Ex-1006	U.S. Patent No. 7,026,802 (“Gradinariu”)
Ex-1007	Def.’s Opening Claim Construction Br., <i>Palisade Technologies, LLP. v. Micron Tech., Inc.</i> , ECF No. 58, No. 7:24-cv-00262 (W.D. Tex. July 30, 2025)
Ex-1008	Joint Claim Construction Statement, <i>Palisade Technologies, LLP. v. Micron Tech., Inc.</i> , ECF No. 90, No. 7:24-cv-00262 (W.D. Tex. Sept. 22, 2025)
Ex-1009	Claim Construction Order and Memorandum in Support Thereof, <i>Palisade Technologies, LLP. v. Micron Tech., Inc.</i> , ECF No. 93, No. 7:24-cv-00262 (W.D. Tex. Oct. 3, 2025)
Ex-1010	John Watson, <i>Mastering Electronics</i> (3d ed. 1990)
Ex-1011	Yuan Taur & Tak H. Ning, <i>Fundamentals of Modern VLSI Devices</i> (1998)
Ex-1012	Paul R. Gray & Robert G. Meyer, <i>Analysis and Design of Analog Integrated Circuits</i> (2d ed. 1984)
Ex-1013	[Intentionally left blank]
Ex-1014	[Intentionally left blank]
Ex-1015	[Intentionally left blank]
Ex-1016	[Intentionally left blank]
Ex-1017	[Intentionally left blank]
Ex-1018	[Intentionally left blank]
Ex-1019	[Intentionally left blank]
Ex-1020	U.S. Patent No. 8,148,962 Claim Comparison Table
Ex-1021	Proposed Protective Order
Ex-1022	Search Disclosure Declaration

Respectfully submitted,

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CERTIFICATE OF SERVICE

I hereby certify that on November 26, 2025, I caused a true and correct copy of the foregoing PETITIONER’S UPDATED LIST OF EXHIBIT was served in its entirety by filing these documents through the PTAB P-TACTS System, as well as by email, on the counsel of record for the Patent Owner:

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