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Reel/ Frame	Assignors & execution date	Correspondent
56991/0292	UNITED MICROELECTRONICS CORPORATION	LONGITUDE LICENSING LTD
Pages 37	Jun 18, 2021	BRACKEN ROAD, SANDYFORD FIRST FLOOR, BLACKTHORN EXCHANGE DUBLIN, D18 P3Y9 IRELAND
Conveyance ASSIGNMENT OF ASSIGNOR'S INTEREST	Assignee MARLIN SEMICONDUCTOR LIMITED BRACKEN ROAD, SANDYFORD FIRST FLOOR, BLACKTHORN EXCHANGE DUBLIN IRELAND D18 P3Y9	Attorney docket number UMC TO MARLIN
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▼ Property 1

Invention title

SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME

Inventors

Te-Chang Hsu, Chun-Chia Chen, Yao-Jhan Wang

Patent number

[11355619](#)

Issue date

Jun 7, 2022

Application number

[16836872](#)

Filing date

Mar 31, 2020

Publication number

[20200235224](#)

Publication date

Jul 23, 2020

PCT number

N/A

N/A

International number

N/A

▼ **Property 2**

Invention title

DELAY LOCK CIRCUIT USING BISECTION ALGORITHM AND RELATED METHOD

Inventors

Jui-Lung Chen, Shih-Huang Huang

Patent number

[6900678](#)

Issue date

May 31, 2005

Application number

[09682303](#)

Filing date

Aug 16, 2001

Publication number

[20030034814](#)

Publication date

Feb 20, 2003

PCT number

N/A

N/A

International number

N/A

▼ **Property 3**

Invention title

METHOD OF REPAIRING A LOW DIELECTRIC CONSTANT MATERIAL LAYER

Inventors

Ting-Chang Chang, Po-Tsun Liu, Yi-Shien Mor

Patent number

[6521547](#)

Issue date

Feb 18, 2003

Application number

[09682479](#)

Filing date

Sep 7, 2001

Publication number

N/A

Publication date

N/A

PCT number

N/A

N/A

International number

N/A

▼ **Property 4**

Invention title

STRUCTURE OF A TEST KEY FOR MONITORING SALICIDE RESIDUE

Inventors

Cheng-Nan Lin

Patent number

[6521910](#)

Issue date

Feb 18, 2003

Application number

[09682946](#)

Filing date

Nov 2, 2001

Publication number

N/A

Publication date

N/A

PCT number

N/A

N/A

International number

N/A

▼ **Property 5**

Invention title

MULTI-DIE PACKAGE

Inventors

Tzung Lee Han, Kun-Chi Lin

Patent number

[6570246](#)

Issue date

May 27, 2003

Application number

[10064110](#)

Filing date

Jun 12, 2002

Publication number

N/A

Publication date

N/A

PCT number

N/A

N/A

International number

N/A

✓ **Property 6**

Invention title

NON-VOLATILE MEMORY TECHNOLOGY SUITABLE FOR FLASH AND BYTE OPERATION APPLICATION

Inventors

Chih-Jen Huang

Patent number

[7088623](#)

Issue date

Aug 8, 2006

Application number

[10685496](#)

Filing date

Oct 16, 2003

Publication number

[20050083738](#)

Publication date

Apr 21, 2005

PCT number

N/A

N/A

International number

N/A

✓ **Property 7**

Invention title

COMPOSITE PHOTORESIST STRUCTURE

Inventors

Jui-Tsen Huang

Patent number

[7718345](#)

Issue date

May 18, 2010

Application number

[12102029](#)

Filing date

Apr 14, 2008

Publication number

[20080193875](#)

Publication date

Aug 14, 2008

PCT number

N/A

N/A

International number

N/A

▼ Property 8

Invention title

METHOD OF FABRICATING A SEMICONDUCTOR DEVICE

Inventors

Jui-Tsen Huang

Patent number

[7384728](#)

Issue date

Jun 10, 2008

Application number

[11465811](#)

Filing date

Aug 20, 2006

Publication number

[20060286486](#)

Publication date

Dec 21, 2006

PCT number

N/A

N/A

International number

N/A

▼ **Property 9**

Invention title

METHOD OF FABRICATING A SEMICONDUCTOR DEVICE

Inventors

Jui-Tsen Huang

Patent number

[7157211](#)

Issue date

Jan 2, 2007

Application number

[10708983](#)

Filing date

Apr 5, 2004

Publication number

[20040191699](#)

Publication date

Sep 30, 2004

PCT number

N/A

N/A

International number

N/A

▼ **Property 10**

Invention title

COMPOSITE PHOTORESIST FOR PATTERN TRANSFERRING

Inventors

Jui-Tsen Huang

Patent number

[7125645](#)

Issue date

Oct 24, 2006

Application number

[10063307](#)

Filing date

Apr 10, 2002

Publication number

[20030194647](#)

Publication date

Oct 16, 2003

PCT number

N/A

N/A

International number

N/A

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