UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

TAIWAN SEMICONDUCTOR MANUFACTURING COMPANY LTD.,

and

APPLE INC.,

Petitioners

v.

MYW SEMITECH, LLC,

Patent Owner

Inter Partes Review Case No. IPR2026-00067
U.S. Patent No. 11,894,306

PETITIONERS' UPDATED EXHIBIT LIST

Petitioners submit this updated Appendix of Exhibits with newly-filed Exhibit 1138.

APPENDIX OF EXHIBITS

Exhibit 1001	U.S. Patent No. 11,894,306
Exhibit 1002	File History of the ’306 Patent
Exhibit 1003	Declaration of Stanley Shanfield, Ph.D.
Exhibit 1004	U.S. Patent No. 9,167,694 to Sundaram, et al.
Exhibit 1005	U.S. Patent Publication No. 2010/0290191 to Lin, et al.
Exhibit 1006	U.S. Patent Publication No. 2011/0215458 to Camacho, et al.
Exhibit 1007	U.S. Patent No. 9,679,863 to Lin, et al.
Exhibits 1008-1009	Intentionally left blank
Exhibit 1010	U.S. Patent No. 9,123,763 to Yu, et al.
Exhibit 1011	Intentionally left blank
Exhibit 1012	Andrea Chen, et al. *Semiconductor Packaging Materials Interaction and Reliability*, CRC Press, 2012
Exhibit 1013	Philip Garrou, et al., *RDL: an integral part of today’s advanced packaging technologies*, Solid State Technology, May 2011
Exhibit 1014	U.S. Patent No. 8,780,576 to Haba, et al.
Exhibit 1015	U.S. Patent No. 7,902,648 to Lee
Exhibit 1016	U.S. Patent No. 7,964,961 to Lee, et al.
Exhibit 1017	U.S. Patent No. 7,241,641 to Savastiouk, et al.
Exhibit 1018	U.S. Patent Publication No. 2005/0110168 to Chuang
Exhibit 1019	U.S. Patent Publication No. 2011/0026232 to Lin, et al.
Exhibit 1020	U.S. Patent No. 5,086,558 to Grube, et al.
Exhibit 1021	U.S. Patent Publication No. 2011/0193219 to Lai, et al.
Exhibit 1022	U.S. Patent No. 6,818,545 to Lee, et al.
Exhibit 1023	U.S. Patent No. 6,974,659 to Su, et al.
Exhibit 1024	U.S. Patent Publication No. 2006/0175686 to Murata, et al.
Exhibit 1025	U.S. Patent Publication No. 2012/0098124 to Wu, et al.
Exhibit 1026	U.S. Patent No. 8,193,604 to Lin et al.
Exhibit 1027	U.S. Patent Publication No. 2004/0203224 to Halahan, et al.

Exhibit 1028	Philip Garrou, *Introduction to 3D Integration,* Handbook of 3D integration: Volumes 1 and 2, Technology and Application of 3D Integrated Circuits, 2012
Exhibit 1029	U.S. Patent Publication No. 2003/0068877 to Kinsman.
Exhibit 1030	U.S. Patent Publication No. 2010/0246152 to Lin, et al.
Exhibit 1031	Yi Yang, et al, *CPU-Assisted GPGPU on Fused CPU-GPU Architectures,* IEEE Xplore
Exhibit 1032	File History of U.S. Patent No. 11,107,768
Exhibit 1033	U.S. Patent No. 4,863,773 to Rousseau, et al.
Exhibit 1034	U.S. Patent No. 6,365,265 to Tose, et al.
Exhibit 1035	E.D. Blackshear, et al., *The evolution of build-up package technology and its design challenges*, International Business Machines Corporation, 2005
Exhibit 1036	Intentionally left blank
Exhibit 1037	U.S. Patent No. 7,374,969 to Cho, et al.
Exhibit 1038	U.S. Patent No. 7,199,036 to Chan, et al.
Exhibit 1039	U.S. Patent No. 8,158,508 to Lin, et al.
Exhibit 1040	U.S. Patent No. 6,619,538 to Test, et al.
Exhibit 1041	U.S. Patent Publication No. 2011/0101523 to Hwang, et al.
Exhibit 1042	U.S. Patent No. 6,831,003 to Huang, et al.
Exhibit 1043	U.S. Patent Publication No. 2009/0226611 to Suzuki, et al.
Exhibit 1044	U.S. Patent No. 6,077,726 to Mistry, et al.
Exhibit 1045	U.S. Patent Publication No. 2010/0244245 to Pendse, et al.,
Exhibit 1046	U.S. Patent Publication No. 2012/0206160 to Wu, et al.
Exhibit 1047	U.S. Patent Publication No. 2009/0174052 to Sogawa, et al.
Exhibits 1048-1053	Intentionally left blank
Exhibit 1054	U.S. Patent No. 6,489,240 to Iacoponi, et al.
Exhibit 1055	U.S. Patent No. 6,578,178 to Mau
Exhibit 1056	U.S. Patent Publication No. 2012/0001190 to Okabe, et al.
Exhibit 1057	U.S. Patent No. 7,307,348 to Wood, et al.
Exhibit 1058	U.S. Patent No. 8,138,014 to Chi, et al.
Exhibit 1059	U.S. Patent No. 6,506,672 to Dagenais, et al.
Exhibit 1060	U.S. Patent No. 8,518,798 to Graetz.
Exhibit 1061	U.S. Patent Publication No. 2009/0261473 to Lee, et al.
Exhibit 1062	Intentionally left blank
Exhibit 1063	U.S. Patent No. 6,812,143 to Lane, et al.
Exhibit 1064	Intentionally left blank
Exhibit 1065	Intentionally left blank

Exhibit 1066	U.S. Patent Publication No. 2009/0070727 to Solomon.
Exhibit 1067	U.S. Patent Publication No. 2012/0098114 to Ishibashi.
Exhibit 1068	U.S. Patent Publication No. 2011/0074028 to Pendse.
Exhibit 1069	U.S. Patent No. 7,485,955 to Kang, et al.
Exhibit 1070	U.S. Patent Publication No. 2012/0104623 to Pagaila, et al.
Exhibit 1071	Intentionally left blank
Exhibit 1072	U.S. Patent No. 7,772,880 to Solomon.
Exhibit 1073	U.S. Patent No. 7,989,270 to Huang, et al.
Exhibit 1074	U.S. Patent Publication No. 2011/0304999 to Yu, et al.
Exhibits 1075-1079	Intentionally left blank
Exhibit 1080	U.S. Patent Publication No. 2012/0319251 to Yu, et al.
Exhibits 1081-1082	Intentionally left blank
Exhibit 1083	U.S. Patent Publication No. 2009/0166069 to Kinoshita.
Exhibits 1084-1092	Intentionally left blank
Exhibit 1093	U.S. Patent No. 6,107,180 to Munroe, et al.
Exhibit 1094	U.S. Patent Publication No. 2011/0210283 to Ramirez, et al.
Exhibit 1095	U.S. Patent No. 6,542,502 to Dishongh, et al.
Exhibit 1096	Intentionally left blank
Exhibit 1097	Richard K. Ulrich, et al., *Advanced Electronic Packaging,* IEEE Press Series on Microelectrical Systems, 2006
Exhibit 1098	Mulugeta Abtew, et al., *Lead-free Solders in Microelectronics*, Materials Science and Engineering, 2000
Exhibit 1099	Karl J. Puttlitz, et al., *Handbook of Lead-Free Solder Technology for Microelectronic Assemblies*, 2005
Exhibit 1100	Intentionally left blank
Exhibit 1101	U.S. Patent Publication No. 2013/0151869 to Steinman, et al.
Exhibit 1102	Linely Gwennap, *Sandy Bridge Spans Generations: Intel Focuses on Graphics, Multimedia in New Processor Design*, Microprocessor, 2010
Exhibit 1103	U.S. Patent No. 6,330,164 to Khandros, et al.
Exhibit 1104	U.S. Patent No. 6,710,447 to Nogami.
Exhibits 1105-1116	Intentionally left blank
Exhibit 1117	U.S. Patent No. 11,538,763 File History
Exhibits 1118-1119	Intentionally left blank

Exhibit 1120	U.S. Patent No. 6,282,100 to Degani, et al.
Exhibit 1121	U.S. Patent No. 6,818,980 to Pedron.
Exhibit 1122	U.S. Patent No. 6,842,391 to Fujioka, et al.
Exhibit 1123	U.S. Patent No. 7,193,917 to Takahashi, et al.
Exhibit 1124	U.S. Patent No. 4,603,345 to Lee, et al.
Exhibit 1125	U.S. Patent Publication No. 2012/0146177 to Choi, et al.
Exhibit 1126	U.S. Patent No. 8,592,988 to Lee, et al.
Exhibit 1127	U.S. Patent No. 8,238,113 to Iihola, et al.
Exhibit 1128	U.S. Patent No. 6,528,179 to Jimarez, et al.
Exhibit 1129	U.S. Patent No. 6,468,413 to Fanti, et al.
Exhibit 1130	U.S. Patent No. 8,132,775 to Goldmann.
Exhibit 1131	U.S. Patent Publication No. 2011/0186977 to Chi, et al.
Exhibit 1132	U.S. Patent No. 7,169,627 to Duh, et al.
Exhibit 1133	U.S. Patent No. 6,614,243 to Klehn, et al.
Exhibit 1134	U.S. Patent Publication No. 2009/0162622 to Van Veen, et al.
Exhibit 1135	U.S. Patent No. 6,570,259 to Alcoe, et al.
Exhibit 1136	U.S. Patent No. 7,741,567 to Beddingfield, et al.
Exhibit 1137	U.S. Patent No. 6,407,341 to Anstrom, et al.
Exhibit 1138	Email to Patent Owner's Counsel with Stipulation

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COUNSEL FOR PETITIONERS

CERTIFICATE OF SERVICE ON PATENT OWNER
UNDER 37 C.F.R. § 42.105

Pursuant to 37 C.F.R. § 42.6(e), the undersigned certifies that on January 8, 2026, the foregoing *Petitioner's Updated Exhibit List and Exhibit 1138* were served via electronic filing with the Board and via Electronic Mail on the following counsel of record for Patent Owner:

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