



AFFIDAVIT OF MINA CHING

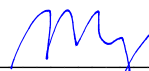
1. I am a Records Request Processor at the Internet Archive. I make this declaration of my own personal knowledge.
2. The Internet Archive is a website that provides access to a digital library of Internet sites and other cultural artifacts in digital form. Like a paper library, we provide free access to researchers, historians, scholars, and the general public. The Internet Archive has partnered with and receives support from various institutions, including the Library of Congress.
3. The Internet Archive has created a service known as the Wayback Machine. The Wayback Machine makes it possible to browse more than 450 billion pages stored in the Internet Archive's web archive. Visitors to the Wayback Machine can search archives by URL (i.e., a website address). If archived records for a URL are available, the visitor will be presented with a display of available dates. The visitor may select one of those dates, and begin browsing an archived version of the Web. Links on archived files in the Wayback Machine point to other archived files (whether HTML pages or other file types), if any are found for the URL indicated by a given link. For instance, the Wayback Machine is designed such that when a visitor clicks on a hyperlink on an archived page that points to another URL, the visitor will be served the archived file found for the hyperlink's URL with the closest available date to the initial file containing the hyperlink.
4. The archived data made viewable and browseable by the Wayback Machine is obtained by use of web archiving software that automatically stores copies of files available via the Internet, each file preserved as it existed at a particular point in time.
5. The Internet Archive assigns a URL on its site to the archived files in the format `http://web.archive.org/web/[Year in yyyy][Month in mm][Day in dd][Time code in hh:mm:ss]/[Archived URL]` aka an "extended URL". Thus, the extended URL `http://web.archive.org/web/19970126045828/http://www.archive.org/` would be the URL for the record of the Internet Archive home page HTML file (`http://www.archive.org/`) archived on January 26, 1997 at 4:58 a.m. and 28 seconds (1997/01/26 at 04:58:28). The date indicated by an extended URL applies to a preserved instance of a file for a given URL, but not necessarily to any other files linked therein. Thus, in the case of a page constituted by a primary HTML file and other separate files (e.g., files with images, audio, multimedia, design elements, or other embedded content) linked within that primary HTML file, the primary HTML file and the other files will each have their own respective extended URLs and may not have been archived on the same dates.





6. Attached hereto as Exhibit A are true and accurate copies of browser screenshots of the Internet Archive's records of the archived files for the URLs and the dates specified in the attached coversheet of each screenshot.
7. I declare under penalty of perjury under the laws of the United States of America that the foregoing is true and correct.

DATE: 09/28/2025



Mira Ching

Please see attached
All Purpose
Jurat form
for additional
Notary Events



EXHIBIT A



<https://web.archive.org/web/20170109072556/https://www.altera.com/products/fpga/stratix-series/stratix-v/support.html>

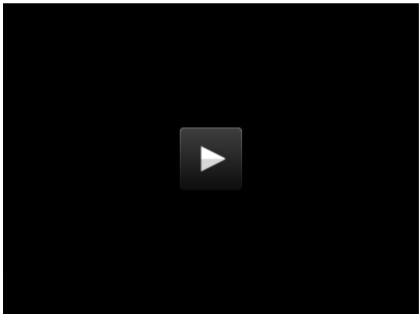
Note: After loading this page, I clicked on the arrow next to the section titled "Stratix V Device Handbook, Volume 1: Device Interfaces and Integration" in order to hide the contents from this view. I then clicked on the arrow next to the section labeled "Stratix V Device Handbook, Volume 2: Transceivers" in order to show the contents of this section.



STRATIX V FPGAS

Show Features Overview +

- Backplane Applications ▶ Driving Backplanes with High-Speed FPGA Transceivers
- Jump Start DSP Designs
- Reference Designs



Overview Features Design Tools Applications Support

- Upcoming Stratix V Device Features (PDF)
 - Stratix V I/O Timing Spreadsheet
 - Stratix V Device Design Guidelines (PDF)
 - Altera Transceiver PHY IP Core User Guide
 - Stratix V Errata (PDF)
 - Stratix V ES Errata and Guidelines (PDF)
 - Known Stratix V Issues
 - Board Design Guidelines
 - PowerPlay Early Power Estimator
- All Packaging Specifications and Dimensions
 - Stratix V Package Specifications
 - Stratix V Thermal Specifications
 - Pin Connection Guidelines
 - Stratix V E, GS, and GX Device Family Pin Connection Guidelines (PDF)
 - Stratix V GT Device Family Pin Connection Guidelines (PDF)
 - Device Pin-Outs
 - BSDL Files

PDF Documentation

- Stratix V Device Overview
- Stratix V Device Datasheet

Stratix V Device Handbook, Volume 1: Device Interfaces and Integration	▼
Stratix V Device Handbook, Volume 2: Transceivers	▲
- Transceiver Architecture in Stratix V Devices - Transceiver Clocking in Stratix V Devices - Transceiver Reset Control in Stratix V Devices - Transceiver Configurations in Stratix V Devices - Transceiver Loopback Support in Stratix V Devices - Dynamic Reconfiguration in Stratix V Devices	
External Memory Interfaces	▼
Power and Thermal Management	▼
I/O Interfaces, Protocols and Signal Integrity	▼
Embedded Memory	▼
DSP	▼
Device Configuration and Remote System Upgrades	▼
Design Guidelines	▼
PCB Layout and Packaging	▼
Development Kits	▼
End Applications	▼





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JURAT ATTACHMENT

A notary public or other officer completing this certificate verifies only the identity of the individual who signed the document to which this certificate is attached, and not the truthfulness, accuracy, or validity of that document.

STATE OF Texas }

COUNTY OF Tarrant }

The foregoing instrument was subscribed and sworn before me this date of
09/28/2025 by Mina Ching

This notarial act was an online notarization.



(Notary Seal)

Notary's Signature Brittany Rene Copeland

Registration No.: 133645929

Commission Expiration Date: March 15, 2026

