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Patent assignment abstract of title

PUBLIC VIEW

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Invention title
METAL GATE TRANSISTOR AND METHOD FOR FABRICATING THE SAME

Inventors
Chien-Ting Lin, Li-Wei Cheng, Jung-Tsung Tseng, Che-Hua Hsu, Chih-Hao Yu, Tian-Fu Chiang, Yi-Wen Chen, Chien-Ming Lai, Cheng-Hsien Chou

Patent number
7888195

Issue date
Feb 15, 2011

Application number
12198128

View in PPUBS

Filing date
Aug 26, 2008

Publication number
20100052074

View in PPUBS

Publication date
Mar 4, 2010

PCT number
N/A
N/A

International number
N/A

Viewing 1-2 of 2 total assignments

TSMC 1033
TSMC v. Marlin,
IPR2026-00261

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Assignment 1

Reel/ Frame
21438/0123

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5

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Recorded
Aug 26, 2008

Assignors & execution date

LIN, CHIEN-TING
Aug 24, 2008

CHENG, LI-WEI
Aug 24, 2008

TSENG, JUNG-TSUNG
Aug 24, 2008

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Assignee

UNITED MICROELECTRONICS CORP.
NO.3, LI-HSIN ROAD 2, SCIENCE-BASED INDUSTRIAL PARK
HSIN-CHU CITY
TAIWAN

Correspondent
WINSTON HSU

P.O.BOX 506
MERRIFIELD, VA 22116

Attorney docket number
NAUP1008USA

Assignment 2

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56991/0292

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37

Conveyance
ASSIGNMENT OF ASSIGNOR'S INTEREST

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Recorded
Jul 26, 2021

Assignors & execution date

UNITED MICROELECTRONICS CORPORATION
Jun 18, 2021

Assignee

MARLIN SEMICONDUCTOR LIMITED
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FIRST FLOOR, BLACKTHORN EXCHANGE
DUBLIN
IRELAND
D18 P3Y9

Correspondent
LONGITUDE LICENSING LTD

BRACKEN ROAD, SANDYFORD
FIRST FLOOR, BLACKTHORN EXCHANGE
DUBLIN, D18 P3Y9 IRELAND

Attorney docket number
UMC TO MARLIN

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EPAS ID: PAT6830810

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
UNITED MICROELECTRONICS CORPORATION	06/18/2021

RECEIVING PARTY DATA

Name:	MARLIN SEMICONDUCTOR LIMITED
Street Address:	BRACKEN ROAD, SANDYFORD
Internal Address:	FIRST FLOOR, BLACKTHORN EXCHANGE
City:	DUBLIN
State/Country:	IRELAND
Postal Code:	D18 P3Y9

PROPERTY NUMBERS Total: 483

Property Type	Number
Patent Number:	6900678
Patent Number:	6521547
Patent Number:	6521910
Patent Number:	6570246
Patent Number:	7088623
Patent Number:	7718345
Patent Number:	7384728
Patent Number:	7157211
Patent Number:	7125645
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Patent Number:	6696222
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Patent Number:	10153210
Patent Number:	10229995
Patent Number:	10438893
Patent Number:	10504903
Patent Number:	10461200
Patent Number:	6649512
Patent Number:	10629734
Application Number:	16836872

CORRESPONDENCE DATA

Fax Number:

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 6138802091
Email: docketing@longitudelicensing.ie
Correspondent Name: LONGITUDE LICENSING LTD
Address Line 1: BRACKEN ROAD, SANDYFORD
Address Line 2: FIRST FLOOR, BLACKTHORN EXCHANGE
Address Line 4: DUBLIN, IRELAND D18 P3Y9

ATTORNEY DOCKET NUMBER:	UMC TO MARLIN
NAME OF SUBMITTER:	RONAN O'BYRNE
SIGNATURE:	/Ronan O'Byrne/
DATE SIGNED:	07/26/2021

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FORM OF ASSIGNMENT

ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, United Microelectronics Corporation, a Taiwanese corporation, ("**Assignor**"), having a place of business at No. 3, Li-Hsin 2nd Road, Hsinchu Science Park, Hsinchu, Taiwan, R.O.C., hereby irrevocably assigns, sells, transfers, and conveys to Marlin Semiconductor Limited., a company organized and existing under the laws of Ireland ("**Assignee**"), having a place of business at 1st Floor, Blackthorn Exchange, Bracken Road, Sandyford, Dublin D18 P3Y9, Ireland, all rights, title and interest that exist as of June 18, 2021 and may exist in the future in and to any of the following (collectively, the "***Patent Rights***"):

(a) the patent applications and patents listed in the attached Schedule A (collectively, the "***Assigned Assets***"), including all rights of priority;

(b) all patents, divisionals, continuations, continuations-in-part, continued prosecution applications, provisional applications, requests for continuing examinations, reissues, reexaminations and extensions thereof and all pending applications therefor, and any other patent or patent application related to such patent or patent application by claim of direct or indirect priority or terminal disclaimer, and all pending applications therefor, and any patents resulting therefrom for all Assigned Assets in (a) above;

(c) all foreign patents, foreign patent applications, and foreign counterparts relating to any item in any of the foregoing categories by claim of direct or indirect priority or terminal disclaimer (a) and (b), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;

(d) all items in any of the foregoing categories (a) through (c), whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;

(e) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (d), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;

(f) all claims and causes of action (whether known or unknown or whether currently pending, filed, or otherwise) for damages, injunctive relief, and any other remedies of any kind, accounting and information, and other secondary claims arising out of the past, current, and future infringement of the Assigned Assets and/or any item in any of the foregoing categories (b) through (e) by third parties which have accrued with Assignor or to which Assignor otherwise holds title; and

(g) all rights to collect royalties and other payments under or on account of any of the Assigned Assets and/or any item in any of the foregoing categories (b) through (f).

Assignor also hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models or other governmental grants or issuances which may be granted upon any of the Assigned Assets in the name of Assignee, as the assignee to the entire interest therein.

Assignor will, at the reasonable request of Assignee, do all things necessary, proper, or advisable, including without limitation, the execution, acknowledgment, and recordation of specific assignments, oaths, declarations, and other documents on a country-by-country basis, to assist Assignee in obtaining, perfecting, sustaining, and/or enforcing the Patent Rights.

The terms and conditions of this assignment will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF, Assignor has caused its duly authorized representative to execute this assignment.

United Microelectronics Corporation

By: Brian H.H. Shih
Name: Brian H.H. Shih
Title: Sr. Director of Intellectual Property
Date: June 18, 2021

ATTESTATION OF SIGNATURE PURSUANT TO 28 U.S.C. § 1746

The undersigned witnessed the signature of Brian H.H. Shih to the above ASSIGNMENT on behalf of United Microelectronics Corporation and makes the following statements:

1. I am over the age of 18 and competent to testify as to the facts in this Attestation if called upon to do so.

2. Brian H.H. Shih is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me on June 18, 2021 to execute the above assignment document on behalf of United Microelectronics Corporation.

3. Brian H.H. Shih executed the above assignment on behalf of United Microelectronics Corporation.

I declare under penalty of perjury under the laws of the United States of America that the statements made above in this Attestation are true and correct.

EXECUTED on June 18, 2021

Signature: Yi-Chin Wu

Print Name: Yi-Chin Wu

Assignee hereby declares the acceptance of the assignment of Patent Rights.

ASSIGNEE:

Marlin Semiconductor Limited

By: _____
Name: Garrett Dempsey
Title: Director

SCHEDULE A

PATENTS

Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
1	US6900678	US	2001-08-16	2005-05-31	US09/682303
2	TWI163758	TW	2001-08-10	2002-09-21	TW090119702
3	CN1212707C	CN	2002-08-01	2005-07-27	CN02127453.3
4	US6521547	US	2001-09-07	2003-02-18	US09/682479
5	TWI161300	TW	2001-08-30	2002-07-21	TW090121523
6	CN1205654C	CN	2001-09-20	2005-06-08	CN1133185.2
7	US6521910	US	2001-11-02	2003-02-18	US09/682946
8	TWI164135	TW	2001-11-01	2002-09-11	TW090127193
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PATENT
REEL: 056991 FRAME: 0311

Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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PATENT
REEL: 056991 FRAME: 0314

Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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PATENT
REEL: 056991 FRAME: 0317

Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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PATENT
REEL: 056991 FRAME: 0323

Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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PATENT
REEL: 056991 FRAME: 0324

Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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Index	Publication/Patent Number	Country	File Date	Publication/Issue Date	Application No.
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