UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

ADVANCED MICRO DEVICES, INC.,

Petitioner,

v.

FERMAT INTERNATIONAL, INC.,

Patent Owner.

IPR2026-00358

U.S. Patent No. 11,886,750

PETITIONER'S UPDATED EXHIBIT LIST

LIST OF EXHIBITS

AMD1001	U.S. Patent No. 11,886,750 to Bismuth et al. ("the '750 patent")
AMD1002	Excerpts from the Prosecution History of the '750 Patent ("the Prosecution History")
AMD1003	Declaration of Dr. Donald Alpert
AMD1004	U.S. Publication No. 2019/0109793 ("Dalal")
AMD1005	U.S. Publication No. 2019/0114538 ("Ng")
AMD1006	U.S. Publication No. 2019/0108145 ("Raghava")
AMD1007	[RESERVED]
AMD1008	U.S. Patent No. 9,043,526 ("Freking")
AMD1009	[RESERVED]
AMD1010	U.S. Publication No. 2016/0306668 ("Heil")
AMD1011	Jun et al. "BlueDBM: An Appliance for Big Data Analytics" ACM (June 13, 2015) ("Jun")
AMD1012	Liu et al. "BlueFlash : a reconfigurable flash controller for BlueDBM" Massachusetts Institute of Technology (2014) ("Liu")
AMD1013	Declaration of June Ann Munford
AMD1014	Putnam et al. "A Reconfigurable Fabric for Accelerating Large-Scale Datacenter Services" Microsoft, (2014)

AMD1015	Vipin et al. "FPGA Dynamic and Partial Reconfiguration: A Survey of Architectures, Methods, and Applications" ACM Comput. Surv. Vol. 51, No. 4, Article 72 (July 2018)
AMD1016	[RESERVED]
AMD1017	[RESERVED]
AMD1018	Verma et al. "PCIe BUS: A State-of-the-Art-Review" (2017)
AMD1019	[RESERVED]
AMD1020	U.S. Patent No. 11,169,727 ("Doucette")
AMD1021	U.S. Patent No. 10,802,995 ("Singh")
AMD1022	Park et al. "PFFS: A Scalable Flash Memory File System for the Hybrid Architecture of Phase-change RAM and NAND Flash" (2008)
AMD1023	Prasad et al. "Design and Verification of Phy Interface for PCIe Gen 3.0 and USB Gen 3.1 Using UVM Methodology" (2017)
AMD1024	U.S. Patent No. 11,055,252 ("Constantinides")
AMD1025	"AXI Central Direct Memory Access v4.1: LogiCORE IP Product Guide" (April 4, 2018)
AMD1026	"PCI Express Base Specification Revision 1.0" (April 29, 2002)
AMD1027	IEEE "IEEE Standard for In-System Configuration of Programmable Devices" IEEE Std 1532™ (2002)
AMD1028	U.S. Patent No. 10,831,376 ("Jung")

AMD1029	Jung et al. "HIOS: A Host Interface I/O Scheduler for Solid State Disks" (2014)
AMD1030	IEEE "The Authoritative dictionary of IEEE Standards Terms, Seventh Edition" (2000)
AMD1031	U.S. Patent No. 9,152,592 ("Galles")
AMD1032	McCarthy et al. "Visualizing the Five-dimensional Torus Network of the IBM Blue Gene/Q" (2014)
AMD1033	"Serial ATA: High Speed Serialized AT Attachment" Revision 1.0a (January 7, 2003)
AMD1034	U.S. Patent No. 6,711,701 ("Roohparvar")
AMD1035	U.S. Publication No. 2013/0318285 ("Pignatelli")
AMD1036	U.S. Patent No. 9,678,842 ("Zhang")
AMD1037	Ousterhout et al. "The Case for RAMClouds: Scalable High-Performance Storage Entirely in DRAM" SIGOPS Operating Systems Review, Vol. 43, No. 4, December 2009, pp. 92-105.
AMD1038	Kang et al. "Performance Trade-Offs in Using NVRAM Write Buffer for Flash Memory-Based Storage Devices" IEEE Transactions on Computers, Vol. 58, No. 6, June 2009, pp. 744-758.
AMD1039	Zazo et al. "A PCIe DMA engine to support the virtualization of 40 Gbps FPGA-accelerated network appliances" 2015 International Conference on ReConFigurable Computing and FPGAs (ReConFig) (2015).
AMD1040	[RESERVED]
AMD1041	[RESERVED]

AMD1042	[RESERVED]
AMD1043	U.S. Publication No. 2020/0233588 ("Li")
AMD1044	Voluntary Search Disclosure Declaration of Kenneth Darby
AMD1045	[RESERVED]
AMD1046	U.S. Patent No. 8,527,949 ("Pleis")
AMD1047	Toulson et al. "Fast and Effective Embedded Systems Design: Applying the ARM mbed. 2nd ed." Newnes (2017)
AMD1048	[RESERVED]
AMD1049	Petitioner's Stipulation

Dated: June 18, 2026

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CERTIFICATE OF SERVICE

Pursuant to 37 CFR § 42.6(e)(4)(i), the undersigned certifies that on June 18, 2026, a complete and entire copy of this Petitioner's Updated Exhibit List and Exhibit AMD1049 were provided electronically via email, to the Patent Owner as follows:

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