

U.S. Patent No. 11,257,891		
1[pre]: A display panel, comprising:	11[pre]: A display panel comprising:	16[pre]: A display panel comprising:
1[a]: an active region and a non-display region, the non-display region disposed around the active region and extending to form a bending region, a fanout routing region, and a component disposing region; the component disposing region having a plurality of component connecting pads;	11[a]: an active region and a non-display region, the non-display region disposed around the active region and extending to form a bending region and a component disposing region, wherein the bending region defines a left section, a middle section, and a right section, the component disposing region having a plurality of component connecting pads; and	16[a]: an active region and a non-display region, the non-display region disposed around the active region and extending to form a bending region and a component disposing region;
1[b]: wherein a first section of a first power trace is disposed within the non-display region around the active region, the first section of the first power trace is connected to a second section of the first power trace disposed within the component disposing region, and the second section of the first power trace is connected to the plurality of component connecting pads; and		16[b]: wherein a first section of a power trace is disposed adjacent to the active region, the first section of the power trace is separately connected to a second section of the power trace disposed within the component disposing region by at least one set of connecting sub-traces passing through the bending region,
	11[b]: a plurality of data traces passing through the bending region and connected with the active	

	region and the plurality of component connecting pads;	
1[c]: a first section of a second power trace is disposed adjacent to the active region and disposed between the active region and the bending region, the first section of the second power trace is connected to a second section of the second power trace disposed within the component disposing region by at least a set of second connecting sub-traces disposed within the bending region, and the second portion of the second power trace is connected to the plurality of component connecting pads,	11[c]: wherein a first section of a power trace is disposed adjacent to the active region, the first section of the power trace is connected to a second section of the power trace disposed within the component disposing region by three sets of connecting sub-traces passing through the left section, the middle section and the right section, and the second section of the power trace is connected to the plurality of component connecting pads.	
1[d]: wherein the first section of the second power trace is separately connected to the second section of the second power trace disposed within the component disposing region by two sets of the second connecting sub-traces passing through two sections of the bending region.		16[c]: wherein the first section of the power trace is separately connected to the second section of the power trace disposed within the component disposing region by three sets of connecting sub-traces passing through a left section, a middle section and a right section of the bending region.

2: The display panel according to claim 1, wherein the first section of the second power trace and the second section of the second power trace are parallel to each other, and a width of the first section of the second power trace is less than a width of the second section of the second power trace, and is greater than a width of the second connecting sub-trace.	12: The display panel according to claim 11, wherein the first section of the power trace and the second section of the power trace are parallel to each other, and a width of the first section of the power trace is less than a width of the second section of the power trace, and is greater than a width of the connecting sub-trace.	17: The display panel according to claim 16, wherein the first section of the power trace and the second section of the power trace are parallel to each other, and a width of the first section of the power trace is less than a width of the second section of the power trace, and is greater than a width of the connecting sub-trace.
3: The display panel according to claim 1, wherein the first section of the second power trace is separately connected to the second section of the second power trace disposed within the component disposing region by three sets of the second connecting sub-traces passing through a left section, a middle section, and a right section of the bending region.		18: The display panel according to claim 16, wherein the first section of the power trace is separately connected to the second section of the power trace disposed within the component disposing region by two sets of the connecting sub-traces passing through two sections of the bending region.
4: The display panel according to claim 2, wherein a width of the first section of the second power trace is not greater than [150] microns.	13: The display panel according to claim 12, wherein a width of the first section of the power trace is not greater than 150 microns.	
5: The display panel according to claim 2, wherein a width of the	14: The display panel according to claim 12, wherein a width of the	

second connecting sub-trace is not greater than 20 microns.	connecting sub-trace is not greater than 20 microns.	
6: The display panel according to claim 3, wherein a width of the second section of the second power trace is not greater than 250 microns.	15: The display panel according to claim 12, wherein a width of the second section of the power trace is not greater than 250 microns.	
7: The display panel according to claim 1, wherein the first section of the first power trace is separately connected to the second section of the first power trace disposed within the component disposing region by a plurality of first connecting sub-traces disposed within the bending region.		
8: The display panel according to claim 6, wherein a width of the first section of the first power trace and a width of the second section of the first power trace are greater than a width of the first connecting sub-trace.		
9: The display panel according to claim 6, wherein a width of the first connecting sub-trace is not greater than 20 microns.		

10: The display panel according to claim 1, wherein the fanout routing region further comprises a plurality of data traces, and wherein a material of the first connecting sub-traces and the second connecting sub-traces located within the bending region and a material of the plurality of data traces is selected from a group consisting of titanium-aluminum-titanium, molybdenum, copper, and silver nanowire.		
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