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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
15/685,429	08/24/2017	Michael Miskin	53998.14US04	4750

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HAYNES AND BOONE, LLP
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EXAMINER
HAMMOND, CRYSTAL L

ART UNIT	PAPER NUMBER
2844	

NOTIFICATION DATE	DELIVERY MODE
11/19/2018	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

ipdocketing@haynesboone.com

Lynk Exhibit 2025
Home Depot v. Lynk Labs
PGR2023-00016, Page 1 of 9

Office Action Summary	Application No. 15/685,429	Applicant(s) Miskin et al.	
	Examiner CRYSTAL L HAMMOND	Art Unit 2844	AIA Status No

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTHS FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☑ Responsive to communication(s) filed on 07/10/2018.

☐ A declaration(s)/affidavit(s) under **37 CFR 1.130(b)** was/were filed on _____.

2a)☑ This action is **FINAL.** 2b)☐ This action is non-final.

3)☐ An election was made by the applicant in response to a restriction requirement set forth during the interview on _____; the restriction requirement and election have been incorporated into this action.

4)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims*

5) ☑ Claim(s) 1-20 is/are pending in the application.

5a) Of the above claim(s) _____ is/are withdrawn from consideration.

6) ☐ Claim(s) _____ is/are allowed.

7) ☑ Claim(s) 1-11,14-15 and 17-20 is/are rejected.

8) ☑ Claim(s) 12-13 and 16 is/are objected to.

9) ☐ Claim(s) _____ are subject to restriction and/or election requirement

* If any claims have been determined allowable, you may be eligible to benefit from the **Patent Prosecution Highway** program at a participating intellectual property office for the corresponding application. For more information, please see http://www.uspto.gov/patents/init_events/pph/index.jsp or send an inquiry to **PPHfeedback@uspto.gov.**

Application Papers

10)☐ The specification is objected to by the Examiner.

11)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

Certified copies:

a)☐ All b)☐ Some** c)☐ None of the:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

** See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☑ Notice of References Cited (PTO-892)

2) ☑ Information Disclosure Statement(s) (PTO/SB/08a and/or PTO/SB/08b) Paper No(s)/Mail Date 07/12/2018 and 10/03/2018.

3) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date _____.

4) ☐ Other: _____.

DETAILED ACTION

Notice of Pre-AIA or AIA Status

1. The present application is being examined under the pre-AIA first to invent provisions.

Claim Objections

2. Claim 14 objected to because of the following informalities: It appears Claim 14 should depend on Claim 11 instead of Claim 1. Appropriate correction is required.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of pre-AIA 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

4. Claim(s) 1-11, 14, 15 and 17-20 is/are rejected under pre-AIA 35 U.S.C. 102(a) as being anticipated by Miskin et al. (US 2007/0273299).

Figures 6, 15 and 22 of Miskin discloses an LED lighting device comprising:

(1) regarding Claims 1, 11 and 20:

a first LED circuit (66 in Fig 6) and a second LED circuits (68 in Fig 6), each of the first LED circuit and the second LED circuit including at least two LEDs connected in either series or parallel (See 66 and 68); and

a switch that is selectable by a user and capable of independently connecting an AC voltage power source to or disconnecting the AC voltage

power source from each of the first LED circuit and the second LED circuit ([0030]-dimmer switch and [0089][0082]);

wherein the LED lighting device is configured to connect to the AC voltage power source (See Fig 15).

(2) regarding Claim 2:

wherein the at least two LEDs of each of the first LED circuit and the second LED circuit are connected in series (See Fig 6).

(3) regarding Claim 3:

wherein each of the first LED circuit and the second LED circuit is arranged in a bridge circuit having at least one LED connected across the outputs ([0089]-see Fig 19).

(4) regarding Claim 4:

wherein the at least two LEDs in each of the first LED circuit and the second LED circuit are connected in parallel (See Fig 6).

(5) regarding Claim 5:

at least four voltage input electrical contacts (See Fig 16), the at least four voltage input electrical contacts being configured so that opposing ends of each of the first LED circuit and the second LED circuit has one voltage input electrical contact connected thereto (See Fig 16),

wherein the at least four voltage input electrical contacts are configured such that a first voltage input electrical contact is connected to an anode of one LED in the first LED circuit (See Fig 16); and a cathode of one LED in the first LED circuit; a second voltage input electrical contact is connected to an anode of

one LED in the first LED circuit (See Fig 16); and a cathode of one LED in the first circuit (See Fig 16);

a third voltage input electrical contact is connected to an anode of one LED in second LED circuit (See Fig 16); and a cathode of one LED in the second LED circuit; a fourth voltage input electrical contact is connected to an anode of one LED in the second LED circuit; and a cathode of one LED in the second LED circuit (See Fig 16); wherein the first voltage input electrical contact is connected to a different anode and a different cathode than the second voltage input electrical contact, and the third voltage input electrical contact is connected to a different anode and a different cathode than the fourth voltage input electrical contact (See Fig 16).

(6) regarding Claim 6:

wherein the first and second LED circuits are connected in:

a series relationship, by connecting the first voltage input electrical contact to a first output of a power supply, connecting the third voltage input electrical contact to a second output of a power supply, and connecting the second voltage input electrical contact and the fourth voltage input electrical contact to each other (See Fig 16 connection options); or

a parallel relationship, by connecting the first voltage input electrical contact and the third voltage input electrical contact to a first output of a power supply, and the second voltage input electrical contact and the fourth voltage input electrical contact to a second output of a power supply (See Fig 16 connection options).

(7) regarding Claim 7:

wherein the LED lighting device is an LED package (See Fig 6).

(8) regarding Claim 8:

wherein the at least two LED circuits are connected in series or parallel utilizing the at least four voltage electrical input contacts within the LED package (See Fig 15).

(9) regarding Claim 9:

wherein the at least two LEDs forming each of the at least two LED circuits are discrete die integrated on a substrate (See Fig 17).

(10) regarding Claim 10:

wherein the LED lighting device is an LED assembly of discretely packaged LEDs (See Fig 17).

(11) regarding Claim 14:

wherein the switch is connected to each of the plurality of LED circuits such that the switch can electrically connect plurality of LED circuits in either series or parallel ([0030] and Fig 16).

(12) regarding Claim 15:

wherein the plurality of LED circuits are integrated into a lamp ([0008]).

(13) regarding Claim 17:

wherein the first LED circuit, the second LED circuit, and the switch are integrated into a lamp ([0008] and see Fig 17).

(14) regarding Claim 18:

wherein the plurality of LED circuits and the switch are integrated into a lamp ([0008] and see Fig 17).

(15) regarding Claim 19:

A system comprising the multi-brightness LED device of claim 11 and the AC voltage power source ([0008] and see Fig 17).

Allowable Subject Matter

5. Claims 12, 13 and 16 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

6. The following is a statement of reasons for the indication of allowable subject matter: Claims 12, 13 and 16 require independently controllable first and second LED circuits by the switch.

Conclusion

7. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to CRYSTAL L HAMMOND whose telephone number is (571)270-1682. The examiner can normally be reached on M-F 12pm-4pm Alt Fridays.

Examiner interviews are available via telephone, in-person, and video conferencing using a USPTO supplied web-based collaboration tool. To schedule an interview, applicant is encouraged to use the USPTO Automated Interview Request (AIR) at http://www.uspto.gov/interviewpractice.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Alexander Taningco can be reached on 571-272-8048. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/CRYSTAL L HAMMOND/
Primary Examiner, Art Unit 2844