

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

OKTA, INC.,
Petitioner,

v.

THALES DIS FRANCE SAS,
Patent Owner.

PGR2026-00045
Patent 12,395,359

PETITIONER'S UPDATED EXHIBIT LIST

LIST OF EXHIBITS

OKTA-1001	U.S. Patent No. 12,395,359 to Duval et al. (“the ’359 Patent”)
OKTA-1002	Prosecution History of the ’359 patent (“the Prosecution History”)
OKTA-1003	Declaration and Curriculum Vitae of Dr. Patrick Traynor
OKTA-1004	U.S. Patent Application No. 2015/0188717 (“Wu”)
OKTA-1005	S. Muelich, & M. Bossert, “A New Error Correction Scheme for Physical Unclonable Functions,” SCC 2017; 11th International ITG Conference on Systems, Communications and Coding. VDE, 2017 (“Muelich”)
OKTA-1006	U.S. Patent Application No. 2021/0051010 (“Yang”)
OKTA-1007	U.S. Patent Application No. 2012/0072737 (“Schrijen”)
OKTA-1008	A. Aghaie & A. Moradi, “TI-PUF: Toward Side-Channel Resistant Physical Unclonable Functions,” published April 2020, IEEE Transactions On Information Forensics And Security (“Aghaie”)
OKTA-1009	U.S. Patent Application No. 2019/0147967 (“Tuyls”)
OKTA-1010	G. Sundararajan, C. Winstead, & E. Boutillon, “Noisy Gradient Descent Bit-Flip Decoding for LDPC Codes,” published 2014, IEEE Transactions on Communications (“Sundararajan”)
OKTA-1011	A. Chan & F. Kschischang, “A Simple Taboo-Based Soft-Decision Decoding Algorithm for Expander Codes,” published 1998, IEEE Communications Letters (“Chan”)
OKTA-1012	USPTO First Inventor to File (FITF) Comprehensive Training presentation (available at

https://www.uspto.gov/sites/default/files/aia_implementation/fitf_comprehensive_training_prior_art_under_aia.pdf)

- OKTA-1013 Declaration of June Munford
- OKTA-1014 Memo, “Interim Processes for PTAB Workload Management,” March 26, 2025, *available at* <https://www.uspto.gov/sites/default/files/documents/InterimProcesses-PTABWorkloadMgmt-20250326.pdf>
- OKTA-1015 “A New Error Correction Scheme for Physical Unclonable Functions,” IEEE Xplore, Institute of Electrical and Electronics Engineers, <https://ieeexplore.ieee.org/abstract/document/7938055> (accessed 8 Apr. 2026)
- OKTA-1016 “A simple taboo-based soft-decision decoding algorithm for expander codes,” IEEE Xplore, Institute of Electrical and Electronics Engineers, <https://ieeexplore.ieee.org/document/703905> (accessed 8 Apr. 2026)
- OKTA-1017 “Noisy Gradient Descent Bit-Flip Decoding for LDPC Codes,” IEEE Xplore, Institute of Electrical and Electronics Engineers, <https://ieeexplore.ieee.org/document/6894606> (accessed 8 Apr. 2026)
- OKTA-1018 E. Stefanov, E. Shi, & D. Song, “Towards Practical Oblivious RAM,” *available at* <https://arxiv.org/abs/1106.3652> (accessed 28 Apr. 2026)
- OKTA-1019 M. Yu & S. Devadas, “Secure and Robust Error Correction for Physical Unclonable Functions,” IEEE Computer Society, published 2010
- OKTA-1020 U.S. Patent Application No. 2020/0059252 (“Fackenthal”)
- OKTA-1021 U.S. Patent Application No. 2015/0381206 (“Fainzilber”)

OKTA-1022	U.S. Patent Application No. 2017/0180140 (“Mai”)
OKTA-1023	U.S. Patent Application No. 2013/0194886 (“Schrijen-886”)
OKTA-1024	Liu et al., “Multi-Threshold Bit Flipping Algorithm for Decoding Structured LDPC Codes,” published Feb. 2015, IEEE Communications Letters (“Liu”)
OKTA-1025	U.S. Patent No. 10,749,694 (“Adams”)
OKTA-1026	U.S. Patent No. 10,574,467 (“Afghah”)
OKTA-1027	U.S. Patent No. 9,628,272 (“Rostami”)
OKTA-1028	Zhang et al., “Tabu-List Noisy Gradient Descent Bit Flipping Decoding of LDPC Codes,” published 2019, 11th International Conference on Wireless Communications and Signal Processing (WCSP) (“Zhang”)
OKTA-1029	Cui & Lin, “An Improved Gradient Descent Bit-Flipping Decoder for LDPC Codes,” published Aug. 2019, IEEE Transactions on Circuits and Systems (“Cui”)
OKTA-1030	RESERVED
OKTA-1031	T. Wadayama, K. Nakamura, M. Yagita, Y. Funahashi, S. Usmai, & I. Takumi, “Gradient Descent Bit Flipping Algorithms for Decoding LDPC Codes,” available at: https://arxiv.org/pdf/0711.0261 (“Wadayama”)
OKTA-1032	Pavithra G. & Naveen Kumar M., “BER Performance Comparison of Bit Flipping Algorithms used for Decoding of LDPC Codes,” available at: https://www.ijert.org/research/ber-performance-comparison-of-bit-flipping-algorithms-used-for-decoding-of-ldpc-codes-IJERTV4IS051311.pdf (“Pavithra”)
OKTA-1033	U.S. Patent Application No. 2019/0305972 A1 (“Yao”)

OKTA-1034 Petitioner's Stipulation dated June 17, 2026

Respectfully submitted,

Dated June 17, 2026

/Kenneth Wayne Darby Jr./
Kenneth W. Darby, Jr., Reg. No. 65,068
Ryan Chowdhury, Reg. No. 74,466
Chris A. von Gunten, Reg. No. 83,726
Fish & Richardson P.C.
60 South Sixth Street, Suite 3200
Minneapolis, MN 55402
T: 512-472-5070
F: 877-769-7945

Attorneys for Petitioner

CERTIFICATE OF SERVICE

Pursuant to 37 CFR § 42.6(e)(4), the undersigned certifies that on June 17, 2026, a complete and entire copy of this Petitioner's Stipulation and Petitioner's Updated Exhibit List were provided, via express mail, to the Patent Owner by serving the correspondence address of record as follows:

Thales Dis Cpl USA Inc.
Intellectual Property
900 South Pine Island Road, Suite 710
Plantation, Florida 33324
UNITED STATES

/Esther Goldschlager/
Esther Goldschlager
Fish & Richardson P.C.
60 South Sixth Street, Suite 3200
Minneapolis, MN 55402
goldschlager@fr.com